

dsPIC30F2023 44-pin to 80-pin TQFP Plug-In Module (PIM) Information Sheet

OVERVIEW

The dsPIC30F2023 PIM is designed to demonstrate the capabilities of the dsPIC30F2023 family of devices using an 80-pin PIM interface.

The dsPIC30F2023 is a high performance 16-bit Digital Signal Controller in a 44-pin TQFP package.

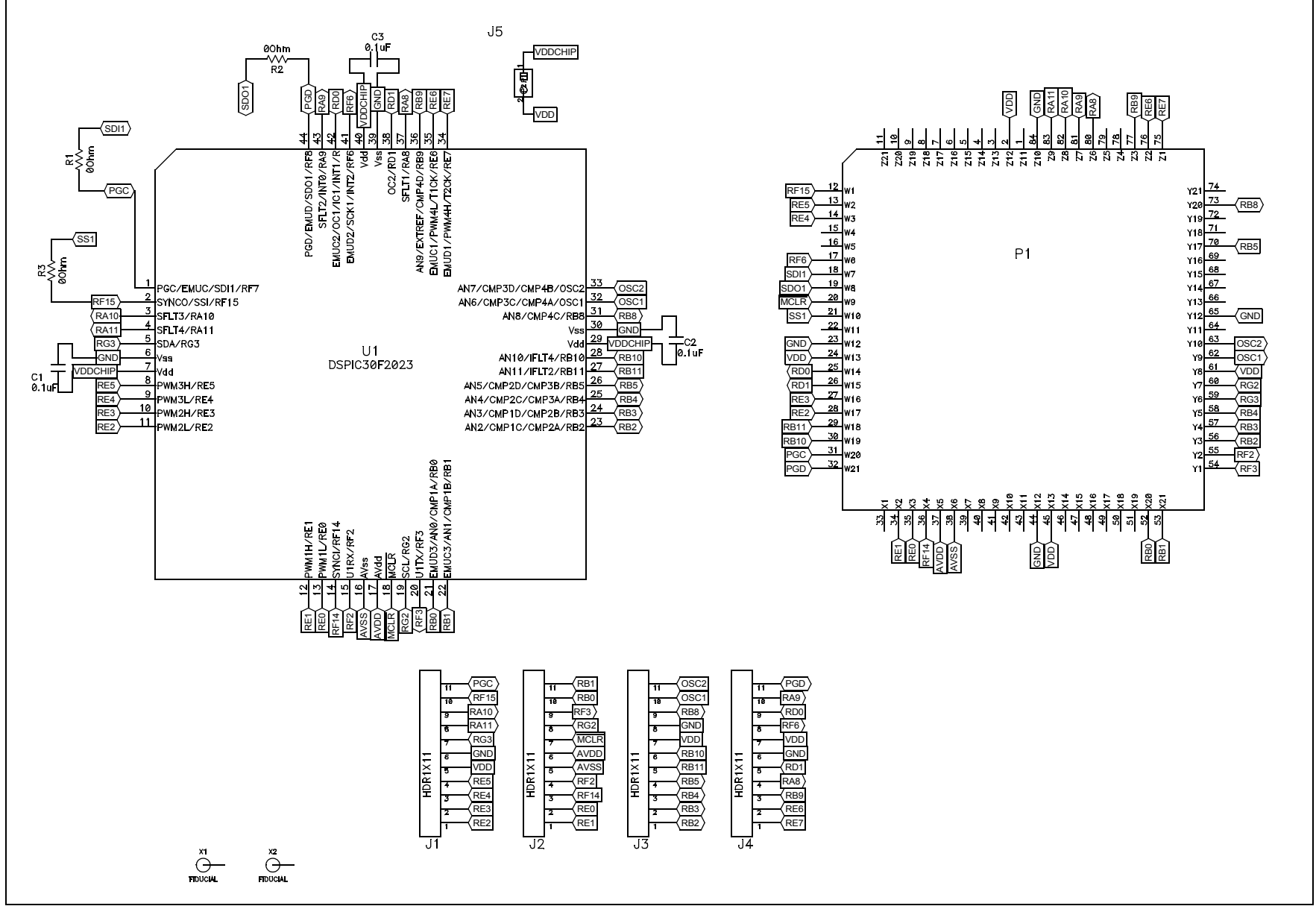
Table 1 shows the mapping between the 80-pin PIM interface board functions and the device pins.

TABLE 1: 44-PIN TO 80-PIN PIM

Device Pin #	dsPIC30F2023 Functional Description	PIM Pin #
1	PGC/EMUC/SDI1/RF7	31 (18)
2	SYNCO/SS1/RF15	12 (21)
3	SFLT3/RA10	82
4	SFLT4/RA11	83
5	SDA/RG3	59
6	Vss	N/A
7	VDD	N/A
8	PWM3H/RE5	13
9	PWM3L/RE4	14
10	PWM2H/RE3	27
11	PWM2L/RE2	28
12	PWM1H/RE1	34
13	PWM1L/RE0	35
14	SYNCl/RF14	36
15	U1RX/RF2	55
16	AVss	38
17	AVDD	37
18	MCLR	20
19	SCL/RG2	60
20	U1TX/RF3	54
21	AN0/CMP1A/CN2/RB0	52
22	AN1/CMP1B/CN3/RB1	53

Device Pin #	dsPIC30F2023 Functional Description	PIM Pin #
23	AN2/CMP1C/CMP2A/CN4/RB2	56
24	AN3/CMP1D/CMP2B/CN5/RB3	57
25	AN4/CMP2C/CMP3A/CN6/RB4	58
26	AN5/CMP2D/CMP3B/CN7/RB5	70
27	AN11/IFLT2/RB11	29
28	AN10/IFLT4/RB10	30
29	VDD	N/A
30	Vss	N/A
31	AN8/CMP4C/RB8	73
32	AN6/CMP3C/CMP4A/OSC1/CLKI/RB6	62
33	AN7/CMP3D/CMP4B/OSC2/CLKO/RB7	63
34	PGD1/EMUD1/PWM4H/T2CK/U1ATX/CN1/RE7	75
35	PGC1/EMUC1/PWM4L/T1CK/U1ARX/CN0/RE6	76
36	AN9/EXTREF/CMP4D/RB9	77
37	SFLT1/RA8	80
38	OC2/RD1	26
39	Vss	N/A
40	VDD	N/A
41	PGD2/EMUD2/SCK1/INT2/RF6	17
42	PGC2/EMUC2/OC1/IC1/INT1/RD0	25
43	SFLT2/INT0/OCFLTA/RA9	81
44	PGD/EMUD/SDO1/RF8	32 (19)

FIGURE 1: 44-PIN TO 80-PIN PIM SCHEMATIC



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