

NVIDIA Jetson Xavier NX and Jetson Nano Interface Comparison and Migration

Application Note

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Introduction

This application note compares the features and interfaces supported on the NVIDIA® Jetson Xavier™ NX and Jetson Nano™ modules. This application note also describes the migration path for designers familiar with Jetson Nano to design a carrier board for Jetson Xavier NX that will support the features available on Jetson Xavier NX.

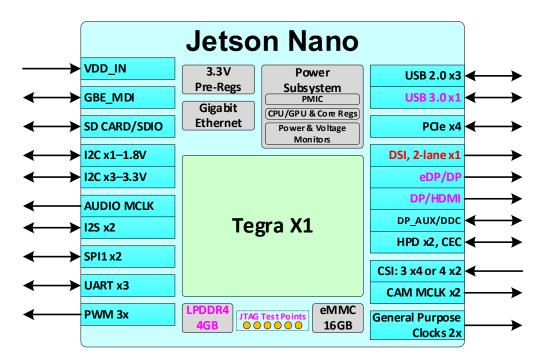
NVIDIA Jetson Xavier NX and Jetson Nano Interface Comparison and Migration

Jetson Xavier NX vs. Jetson Nano

The Jetson Xavier NX and Jetson Nano modules are pin compatible with a few exceptions. This application note describes the differences to allow users familiar with Jetson Nano to design a similar carrier board for Jetson Xavier NX.

The following figures show the Jetson Xavier NX and Jetson Nano block diagrams. The interfaces or blocks that are supported only by one of the modules are highlighted in red. The interface types that are supported on both modules but where the number of lanes/instances, voltage level, or access is different are highlighted in magenta.

Figure 1. Jetson Nano Block Diagram



Jetson Xavier NX Power **→** VDD_IN USB 2.0 3x ◀ Subsystem PMIC Gigabit → GBE_MDI USB 3.1 1x ◀ Ethernet CPU/GPU & Core Regs **QSPINOR** Power & Voltage ➤ SD CARD/SDIO PCle x1 + x4 ◀ 32MB Mon ito rs eMMC **► I2C 1x-1.8V** [E]DP/HDMI 2x 16GB ► 12C 3x-3.3V DP_AUX/DDC 2x HPD 2x, CEC ◀ **AUDIO MCLK →** 12S 2x CSI: 3 x4 or 6 x2 **Xavier CAM MCLK 2x** ➤ SPI 2x **General Purpose ►** UART 3x Clocks 2x CAN 1x PWM 3x

Figure 2. Jetson Xavier NX Block Diagram

Module Interface Comparisons

Table 1 lists the key system specifications, devices and interfaces that are supported on either the Jetson Xavier NX or the Jetson Nano module.

Table 1. Jetson Xavier and Jetson Nano Feature Comparison

Feature	Jetson Xavier NX	Jetson Nano				
System Specifications and Device on the Module						
GPU	NVIDIA Volta™ architecture with 384 NVIDIA® CUDA® cores and 48 Tensor cores NVIDIA Maxwell™ architecture CUDA cores					
CPU	6-core NVIDIA Carmel Armv8.2 64-bit CPU	Quad-core ARM Cortex-A57 MPCore processor				
Memory	8 GB 128-bit LPDDR4x	4 GB 64-bit LPDDR4				
Storage	16	GB eMMC				
Networking	10/10	00/1000 Mbit				
Video Encode	2x464 MP/sec	250MP/sec				
	2x 4K @ 30 (HEVC)	1x 4K @ 30 (HEVC)				
	6x 1080p @ 60 (HEVC)	2x 1080p @ 60 (HEVC)				
	14x 1080p @ 30 (HEVC)	4x 1080p @ 30 (HEVC)				
		4x 720p @ 60 (HEVC)				
		9x 720p @ 30 (HEVC)				
Video Decode	2x690MP/sec	500MP/sec				
	2x 4K @ 60 (HEVC)	1x 4K @ 60 (HEVC)				
	4x 4K @ 30 (HEVC)	2x 4K @ 30 (HEVC)				
	12x 1080p @ 60 (HEVC)	4x 1080p @ 60 (HEVC)				
	32x 1080p @ 30 (HEVC)	8x 1080p @ 30 (HEVC)				
	16х 1080р @ 30 (H.264)	9x 720p @ 60 (HEVC)				
Camera	12 lanes (3x4 or 6x2) MIPI CSI-2 D-PHY 1.2 (2.5 Gb/s per pair)	12 lanes (3x4 or 4x2) MIPI CSI-2 D-PHY 1.1 (1.5 Gb/s per pair)				
Mechanical	69.6 mm x 45 mm 260-pin edge connector					

Feature	Jetson Xavier NX	Jetson Nano				
System Specifications and Device on the Module						
Input Voltage	5V	(nominal)				
Interfaces						
USB 2.0		3x				
USB 3.x	1x (3.1 GEN2)	1x (3.0 GEN1)				
PCle	1 x1+ 1 x4 (Gen3). x1 is Root Port only. x4					
Display	Two multi-mode (e)DP 1.4/HDMI™ 2.0a					
Audio (I2S)	2x					
SDIO/SD Card	1x SD Card/SDIO					
I2C	4x					
CAN	1x Not supported					
UART	3x					
SPI	2x					
JTAG	Not supported Brought to on-module test points only					
Fan	PWM and Tach Input					

Function and Interface Difference Details

Mechanical Differences

Table 2 lists the mechanical differences.

Table 2. Mechanical Differences

Feature	Jetson Xavier NX	Jetson Nano	
Size	69.5 mm x 45 mm		
Built-in thermal solution	None		
Thermal solution mounting	4 holes in PCB for mounting thermal solution to Jetson Xavier NX.	Same approach as Jetson Xavier NX except that the thermal solution mounting hole locations are different.	

Figure 3. Jetson Nano vs. Jetson Xavier NX Module Top

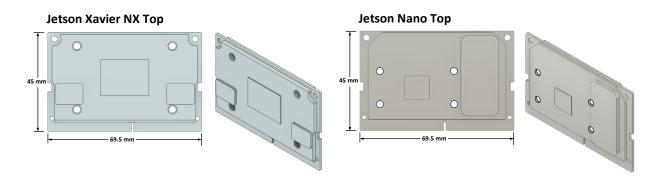
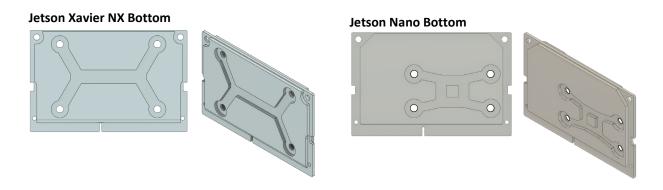


Figure 4. Jetson Nano vs. Jetson Xavier NX Module Bottom



USB 3.x and PCI Express Mapping

The following tables show the different options for mapping USB 3.x and PCIe to the common set of interface pins.

Table 3. Jetson Xavier NX USB 3.1 and PCIe Lane Mapping Configurations

	vier NX Pin mes	PCIEO_RX3 PCIEO_TX3	PCIE0_RX2 PCIE0_TX2	PCIE0_RX1 PCIE0_TX1	PCIE1_RX0 PCIE1_TX0	PCIE1_RX0 PCIE1_TX0	USBSS_RX USBSS_TX
Xavier Lanes		Lane 5	Lane 4	Lane 3	Lane 2	Lane 11	Lane 1
USB 3.1	PCle						
1	1x4 + 1x1	PCIe 0 lane 3 (Ctrl #5)	PCIe 0 lane 2 (Ctrl #5)	PCIe 0 lane 1 (Ctrl #5)	PCIe 0 lane 0 (Ctrl #5)	PCIe 1 lane 0 (Ctrl #4)	USB_SS Port #2
Recommended Usage		PCle	x4 connector or	device (i.e. M.2 K	(ey M)	PCle x1 conn. or device (i.e. M.2 Key E)	USB 3.1 connector, device or hub

Table 4. Jetson Nano USB 3.0 and PCle Lane Mapping Configurations

Jetson N		na	PCIE0_RX3 PCIE0_TX3	PCIE0_RX2 PCIE0_TX2	PCIE0_RX1 PCIE0_TX1	PCIE1_RX0 PCIE1_TX0	USBSS_RX USBSS_TX
NVIDIA Tegra X1 Lanes		Lane 0	Lane 1	Lane 2	Lane 3	Lane 4	Lane 6
USB 3.0	PCIe						
1	1 x4	PCIe 1 lane 0 – Used on- module for Ethernet	PCIe 0 lane 3 (Ctrl #0)	PCIe 0 lane 2 (Ctrl #0)	PCIe 0 lane 1 (Ctrl #0)	PCIe 0 lane 0 (Ctrl #0)	USB_SS Port #0
Usage or DevKit Car		Ethernet	Unused			M.2 Key E	USB 3.0 Type A

PCI Express

Jetson Xavier NX supports two PCIe interfaces: A x1 lane interface and a x4 lane interface (can be x2 or x1 instead) at the module pins. Jetson Nano supports only the x4 lane interface (can be x2 or x1 instead).at the module pins. Jetson Xavier NX supports both Root Port and Endpoint operation on the x4 interface (only Root Port on the x1) where Jetson Nano only supports Root Port operation.

Jetson Xavier NX Jetson 0.1uF 0.22uF Nano PCIEO_TX3_N PCIEO_TX3_P O.1uF PCle 0 Lane 3 O.22uF PCle 0 Lane 3 154 PCIE0 TX3 P PCIE0_RX3_N 155 PCIEO RX3 N 155 PCIE0_RX3_P 0.1uF PCIE0_RX3_P 0.<u>22uF</u> PCIE0_TX2_N PCIE0_TX2_N O.1uF PCIe 0 Lane 2 PCIe 0 La ne 2 PCIE0_TX2_P 150 PCIE0_TX2_P Unused on Carrier Board PCIE0 RX2 N PCIE0_RX2_N PCIE0_RX2_P 151 PCIE0 RX2 P 0.1uF 0.22uF PCIE0_TX1_N O.1 uF 0.22uF PCIE0 TX1 N PCIe 0 (Ctrl #5) - PCIe x4 PCIE0_TX1_P PCIE0_TX1_P conn/device (i.e. M.2 PCIEO RX1 N PCIEO RX1 N PCIEO RX1 P 139 PCIE0_RX1_P 139 0.1uF 0.22uF 0.22uF PCIE0 TX0 N 134 0.1 uF PCle 0 Lane 0 PCIE0_TX0_N PCIE0_TX0_P 136 PCIE0_TX0_P PCIEO_RXO_N 133 PCIEO RXO N PCIE0_RX0_P PCIe 0 (Ctrl #0) Lane PCIEO RXO P 0 - Routed to M.2 NVHSO_REFCLK PCIE0 CLK N Key E Connector on PCIE0 CLK N PCIE0 CLK P 162 Carrier Board PCIE0_CLK_P PCIE0_CLKREQ* PEX_CLK5 < 0.22uF PCIE0 RST* 181 PCIE1_TX0_N 134 0.22uF PCle 1 Lane 0 CAN0_EN PCIE WAKE* 136 PCIE1 TX0 P PCIe 1 (Ctrl #4) - PCIe x1 PCIE1 RX0 N conn/device (i.e. M.2 13 PCIE1_RX0_P Kev E) PCIE1_CLK_N PCIE1_CLK_P 162 PCIE1 CLKREQ* PCIe 1 (Ctrl #4) – PCIe x1

conn/device (i.e. M.2 Key E) PCIE1_RST* 181 Shared wake pin PCIE WAKE* 179 PCle 0 (Ctrl #5) – PCle x4 conn/device (i.e. M.2 Key M) PCIEO CLKREO* PCIE0_RST*

Figure 5. Jetson Xavier NX and Jetson Nano PCIe Block Diagram

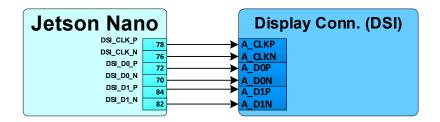
Display

Jetson Nano supports DSI, Vesa® DisplayPort™ (DP), embedded DisplayPort (eDP), and HDMI as described in this section. Jetson Xavier NX does not support DSI but does support DisplayPort (DP), embedded DisplayPort (eDP), and HDMI with some differences.

DSI

Jetson Xavier NX does not support DSI. Jetson Nano supports a single 2-lane DSI interface.

Figure 6. Jetson Nano DSI Block Diagram



eDP, DP, and HDMI

Both Jetson Xavier NX and Jetson Nano can support eDP, DP, and HDMI displays. Jetson Xavier NX can support any of these displays on either of the two interfaces. Jetson Nano has one interface that supports only eDP (or DP display only) while the other supports HDMI, eDP, and DP.

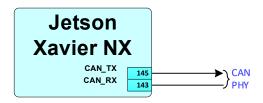
eDP, DP, and HDMI Display Support Table 5.

Feature	Jetson Xavier NX	Jetson Nano
eDP/DP	DP[1:0]_TXD[3:0]_P/N, DP[1:0] AUX P/N, DP[1:0] HPD	DP0_TXD[3:0]_P/N, DP0_AUX_P/N, DP0_HPD
HDMI/DP		DP1_TXD[3:0]_P/N, DP1_AUX_P/N, DP1_HPD, HDMI_CEC

CAN

Jetson Xavier NX supports a single CAN interface. Jetson Nano does not support CAN.

Jetson Xavier NX CAN Block Diagram Figure 7.

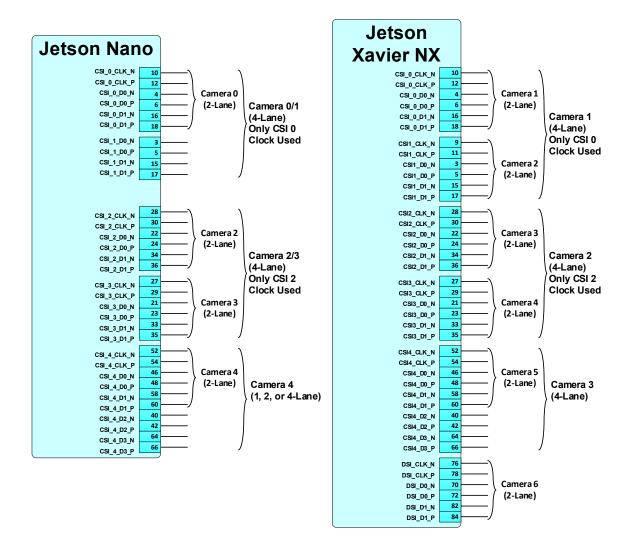


Camera

Both Jetson Xavier NX and Jetson Nano support up to 12 CSI data lanes and can support the following configurations to cameras or serializers:

- ► Jetson Xavier NX
 - 3×4 , $2 \times 4 + 2 \times 2$, $1 \times 4 + 4 \times 2$, or 6×2
- ▶ Jetson Nano
 - $3 \times 4.2 \times 4 + 2 \times 2.1 \times 4 + 3 \times 2.$ or 4×2

Jetson Xavier NX and Jetson Nano CSI Block Diagrams Figure 8.



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Debug

Jetson Nano brings the JTAG interface to test points on the module only. Jetson Xavier NX does not support JTAG. Both Jetson Nano and Jetson Xavier NX provide UART2 for debug purposes.

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