

ODROID - H2 Schematics

Intel GeminiLake Platform Revision -B

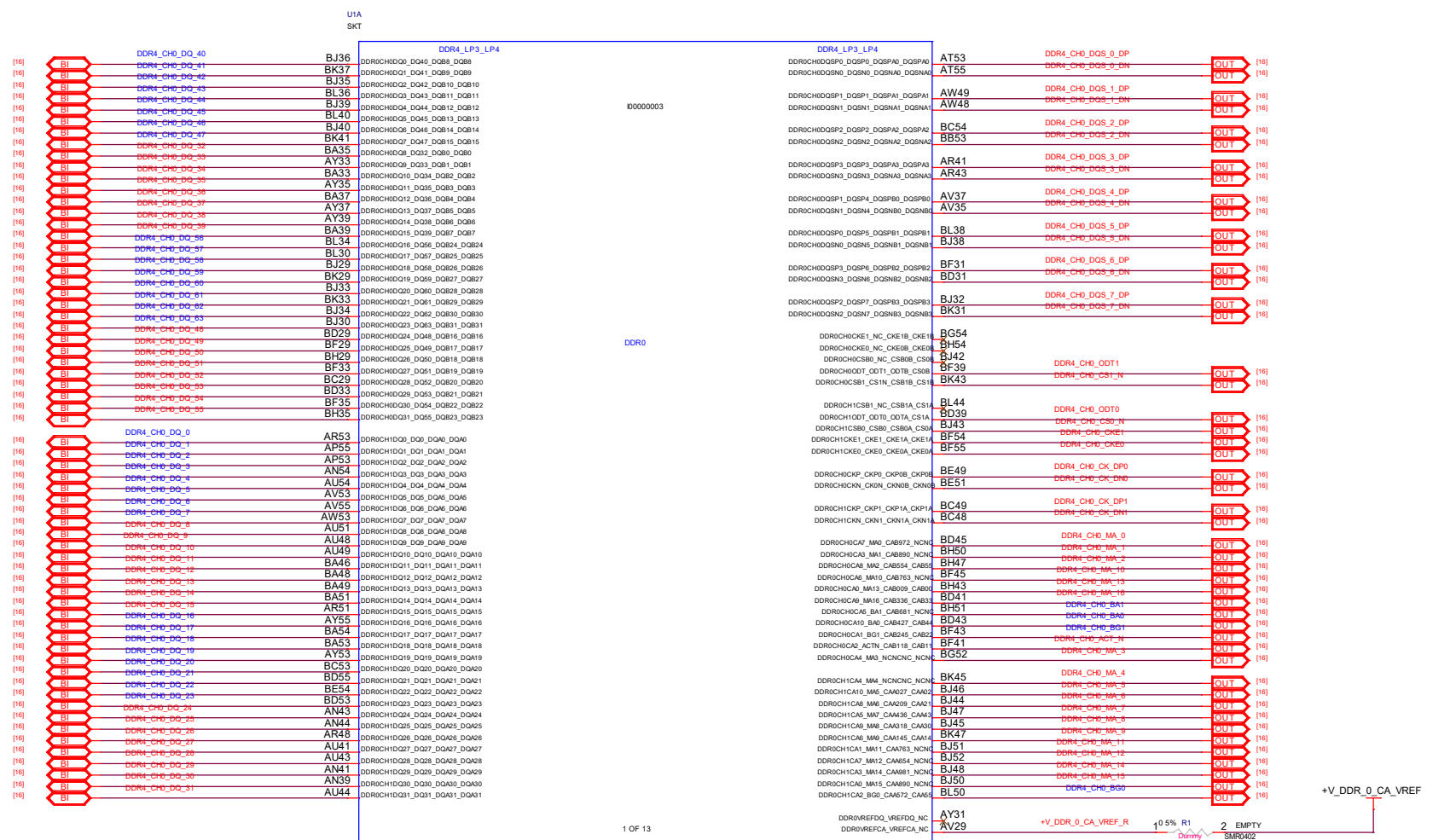
2019-05-29

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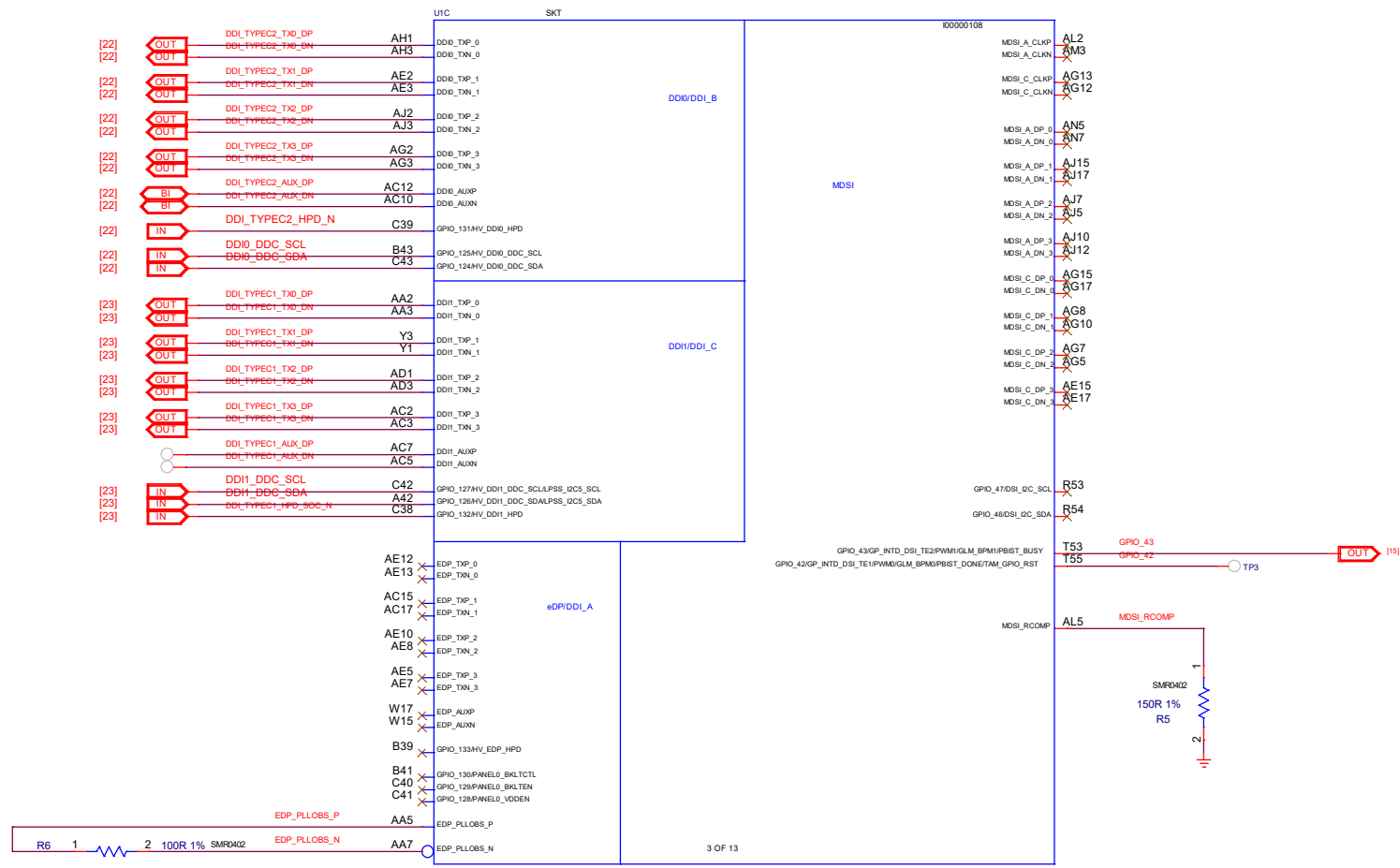
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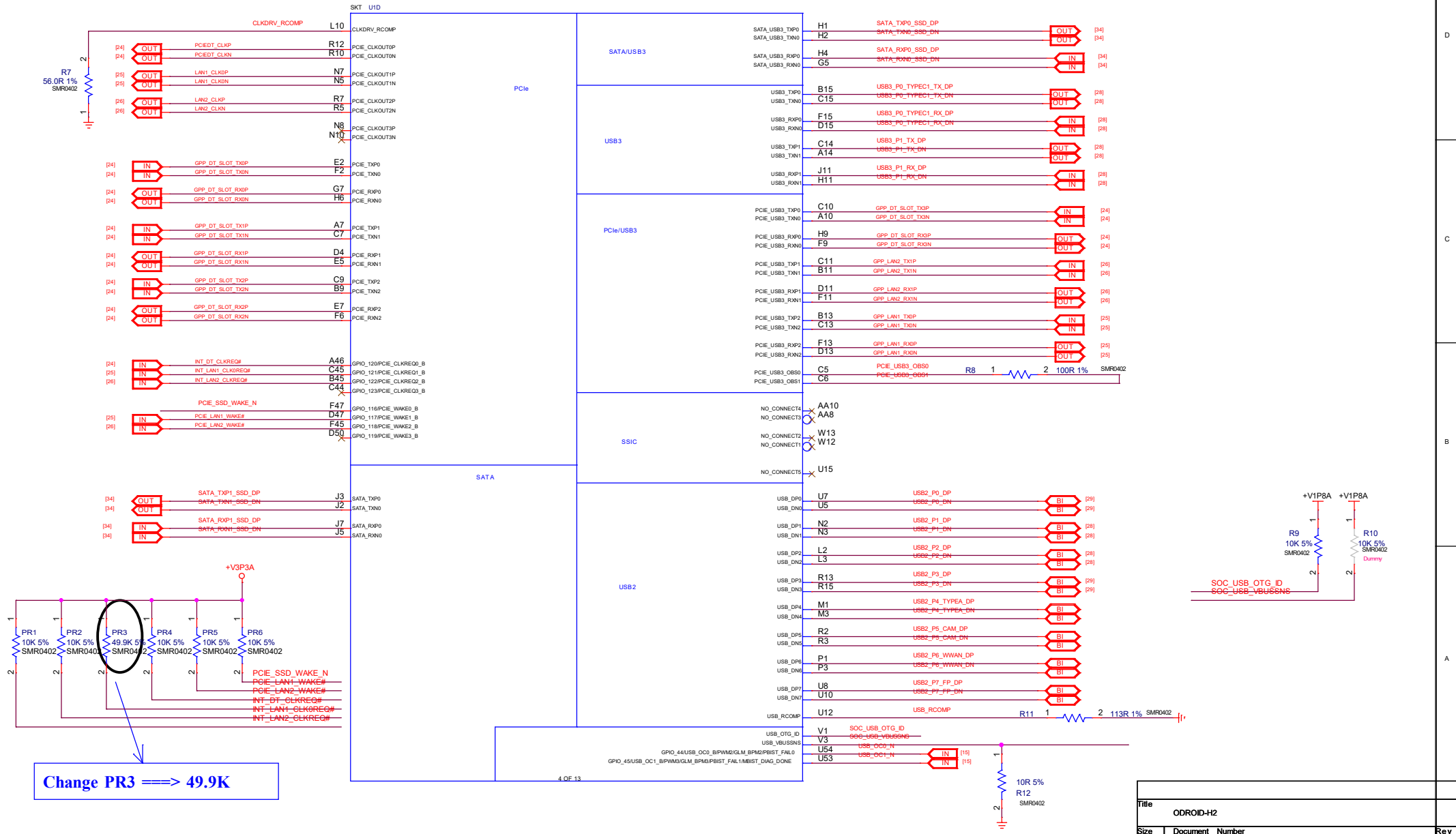


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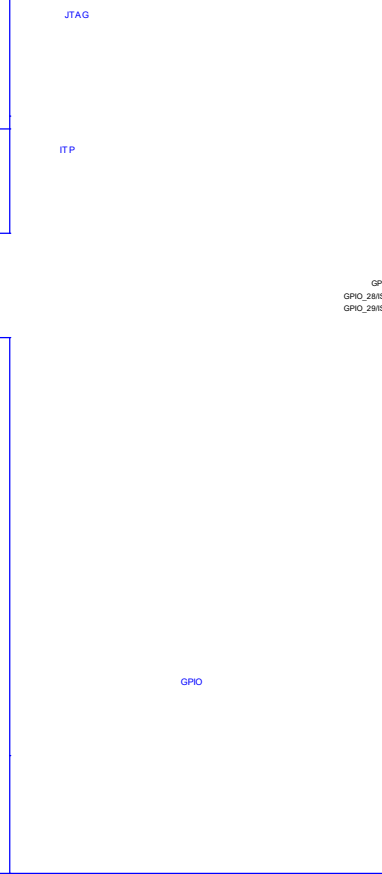
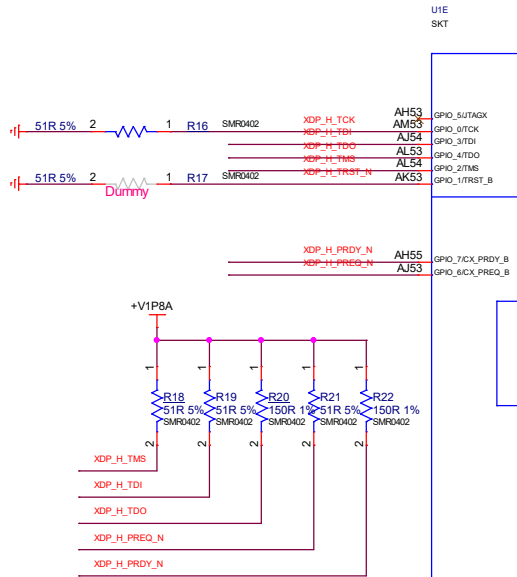


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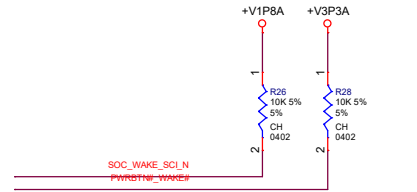
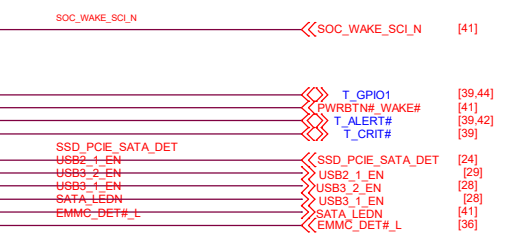
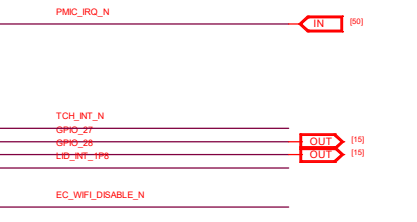
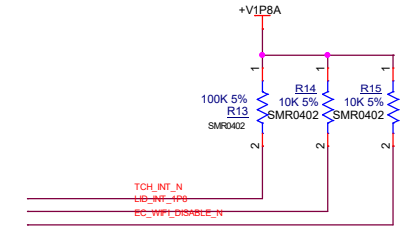
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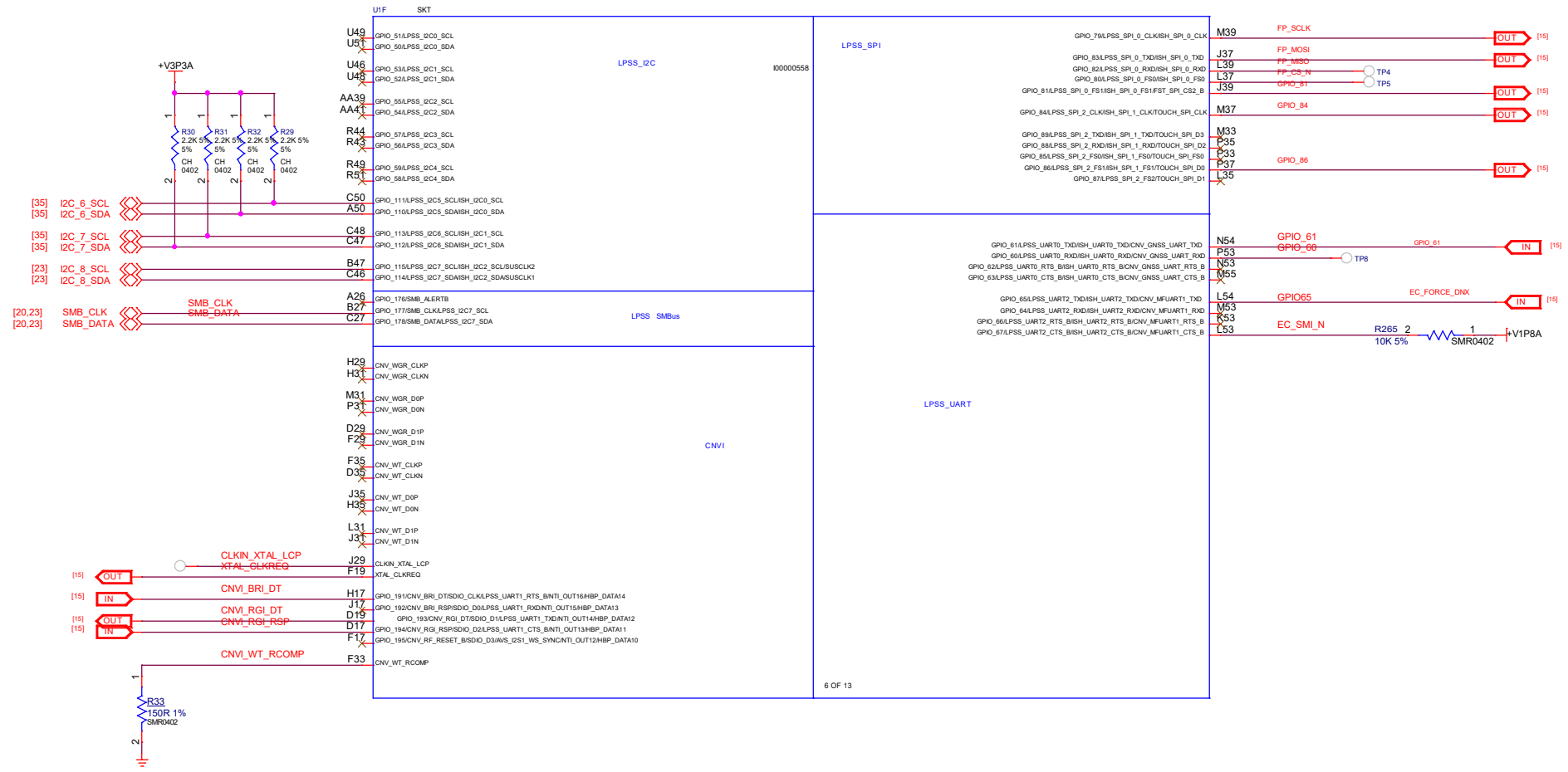
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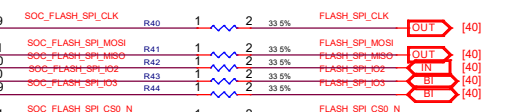
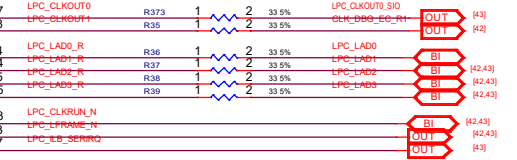
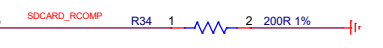
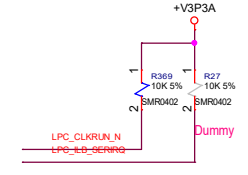
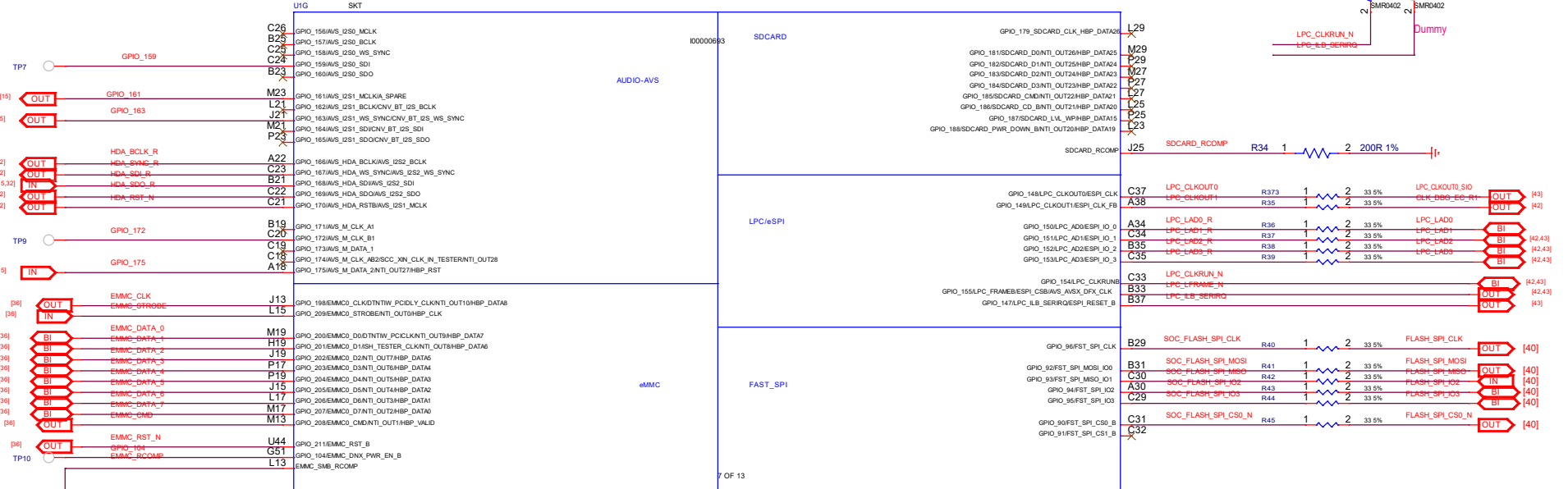
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| AG53 | GPIO_8ICNV_DEBUG_09ICNV_DEBUG_00TRACE_0_CLK_VNNTRACE_0_CLK_AONTRACE_CNV_CLK_AONNTI_N10GLM_PL1TESTDBG |
| AG54 | GPIO_9ICNV_DEBUG_10ICNV_DEBUG_01TRACE_0_DATA_VNNTRACE_0_DATA_AONTRACE_CNV_DATA_AONNTI_N11GLM_TICKLE |
| AE54 | GPIO_10ICNV_DEBUG_11ICNV_DEBUG_02TRACE_0_DATA_VNNTRACE_0_DATA_AONTRACE_CNV_DATA_AONNTI_N2 |
| AE53 | GPIO_11ICNV_DEBUG_12ICNV_DEBUG_03TRACE_0_DATA_VNNTRACE_0_DATA_AONTRACE_CNV_DATA_AONNTI_N3 |
| AE55 | GPIO_12ICNV_DEBUG_13ICNV_DEBUG_04TRACE_0_DATA_VNNTRACE_0_DATA_AONTRACE_CNV_DATA_AONNTI_N4 |
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| AE53 | GPIO_16ICNV_DEBUG_17ICNV_DEBUG_08TRACE_0_DATA_VNNTRACE_0_DATA_AONTRACE_CNV_DATA_AONNTI_N8 |
| AA49 | GPIO_17ICNV_MFUART0_RXDCNV_DEBUG_00TRACE_1_CLK_VNNTRACE_1_CLK_AONTRACE_CNV_CLK_AONNTI_N14 |
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| AC46 | GPIO_19ICNV_MFUART0_RXDCNV_DEBUG_02TRACE_1_DATA_VNNTRACE_1_DATA_AONTRACE_CNV_DATA_AONNTI_N11 |
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| AC49 | GPIO_22ICNV_MFUART2_TXDCNV_DEBUG_05TRACE_1_DATA_VNNTRACE_1_DATA_AONTRACE_CNV_DATA_AONNTI_N14 |
| AE51 | GPIO_23ICNV_GNSS_PA_BLMKNCNV_DEBUG_06PMC_STDBITTRACE_1_DATA_VNNTRACE_1_DATA_AONNTI_N15BTM_CMD |
| AE51 | GPIO_24ICNV_GNSS_FTACNV_DEBUG_07PMC_PMI000TRACE_1_DATA_VNNTRACE_1_DATA_AONNTI_N16BTM_RST |
| AA46 | GPIO_25ICNV_GNSS_SYSCNV_DEBUG_08PMC_RESET_BITTRACE_1_DATA_VNNTRACE_1_DATA_AONNTI_N17BTM_CLK |
| AE41 | GPIO_26ISH_GPIO_0LPSS_UART1_RXDISH_UART1_RXDCNV_BT_UART_RXDTRACE_2_CLK_VNNTRACE_2_CLK_AONNTI_N18BTM_DATA_IN_0 |
| AE39 | GPIO_27 |
| AE46 | GPIO_28 |
| AE44 | GPIO_29 |
| AC41 | GPIO_30 |
| AC39 | GPIO_31 |
| AC44 | GPIO_32 |
| AC43 | GPIO_33 |
| AA44 | GPIO_34 |
| AA54 | GPIO_35 |
| AE53 | GPIO_36 |
| Y55 | GPIO_37 |
| Y53 | GPIO_38 |
| W54 | GPIO_39 |
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| Y53 | GPIO_41 |
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| M43 | GPIO_137ISH_GPIO_13 |
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| H43 | GPIO_139LPSS_UART3_TXDISH_UART0_TXDISH_GPIO_15SATA_GP1 |
| J43 | GPIO_140LPSS_UART3_RTS_BISH_UART0_RTS_BICNV_UART0_RTS_BITTRACE_2_DATA_VNNTRACE_2_DATA_AONNTI_N27BTM_DATA_IN_9 |
| D43 | GPIO_141LPSS_UART3_CTS_BASH_UART0_CTS_B0GCLKDBG_PMC_0SATA_DEVSLEP1 |
| F43 | GPIO_142LPSS_SPL1_CLKISH_SPL1_0_CLKCISE_HWM_STALLSATA_LEDN |
| H41 | GPIO_143LPSS_SPL1_FSDISH_SPL1_0_FSDISEC_TCK |
| F59 | GPIO_144LPSS_SPL1_FSHISH_SPL1_0_FSHISEC_TDPANEL1_VDDEN |
| L41 | GPIO_145LPSS_SPL1_RDISH_SPL1_0_RDISEC_INSPANEL1_BILTEN |
| L41 | GPIO_146LPSS_SPL1_TXDISH_SPL1_0_TXDISEC_TDPANEL1_BKLTCTL |
| F27 | GPIO_210NTI_OUT18WBP_DATA18 |
| U43 | GPIO_212 |
| U41 | GPIO_213 |
| U39 | GPIO_214 |



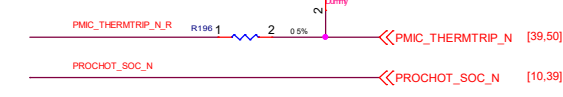
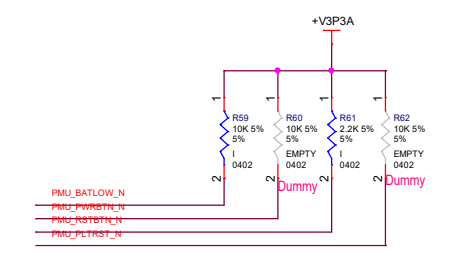
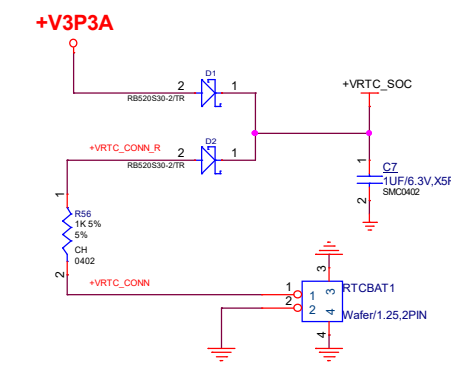
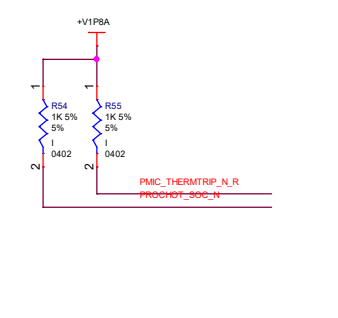
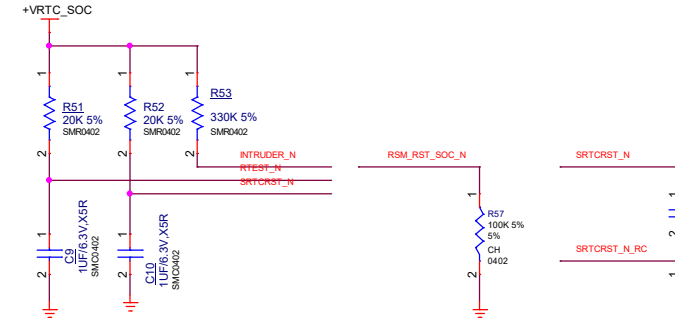
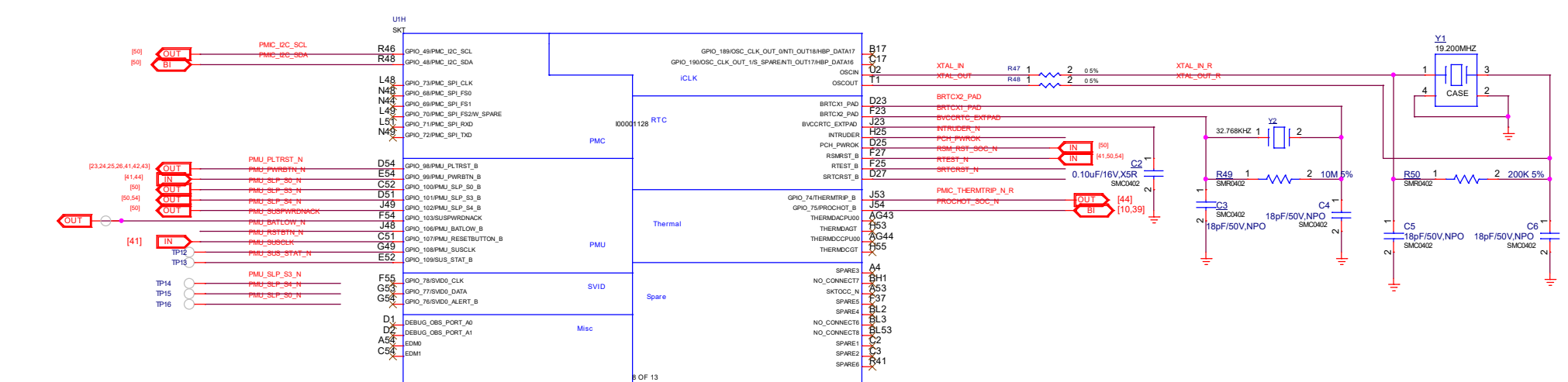
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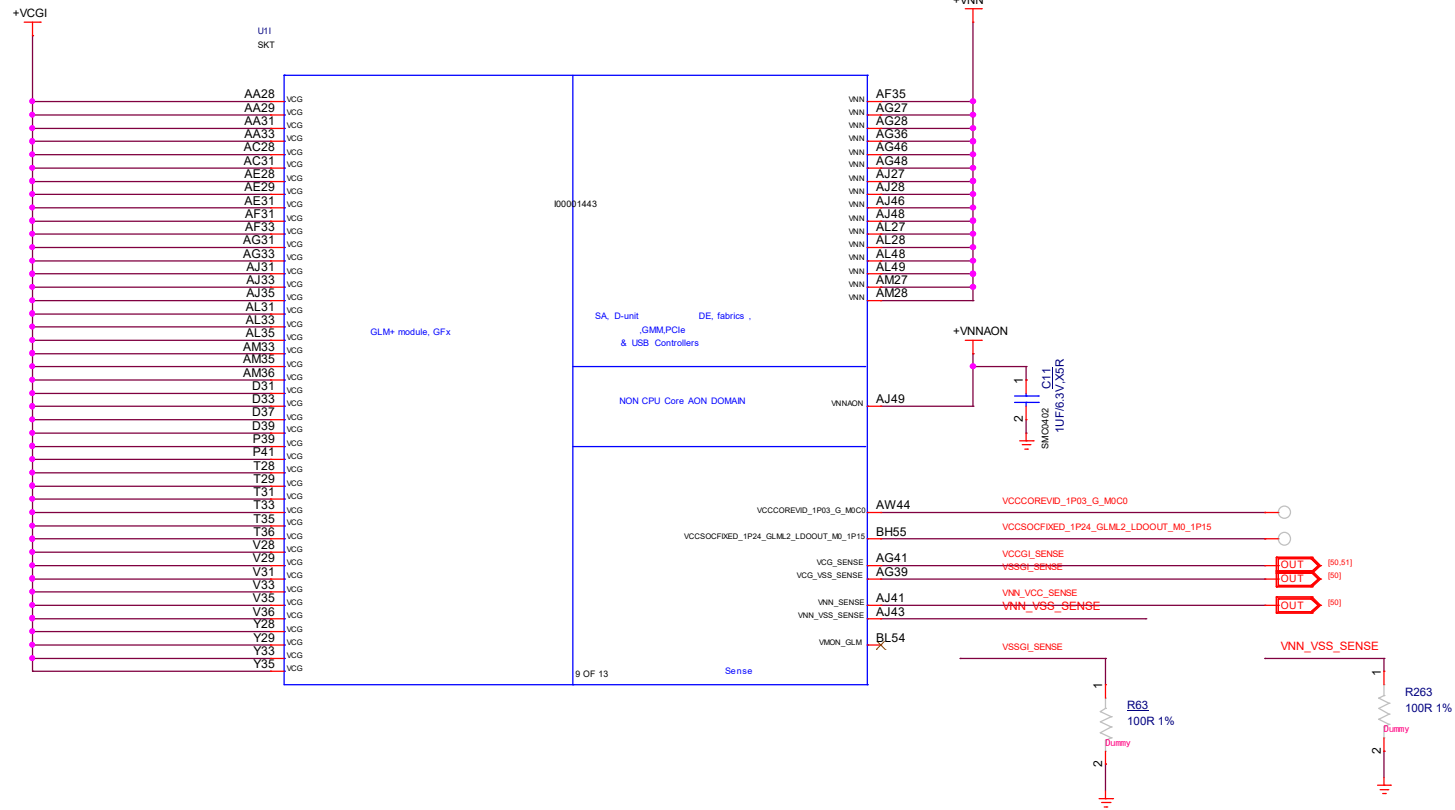
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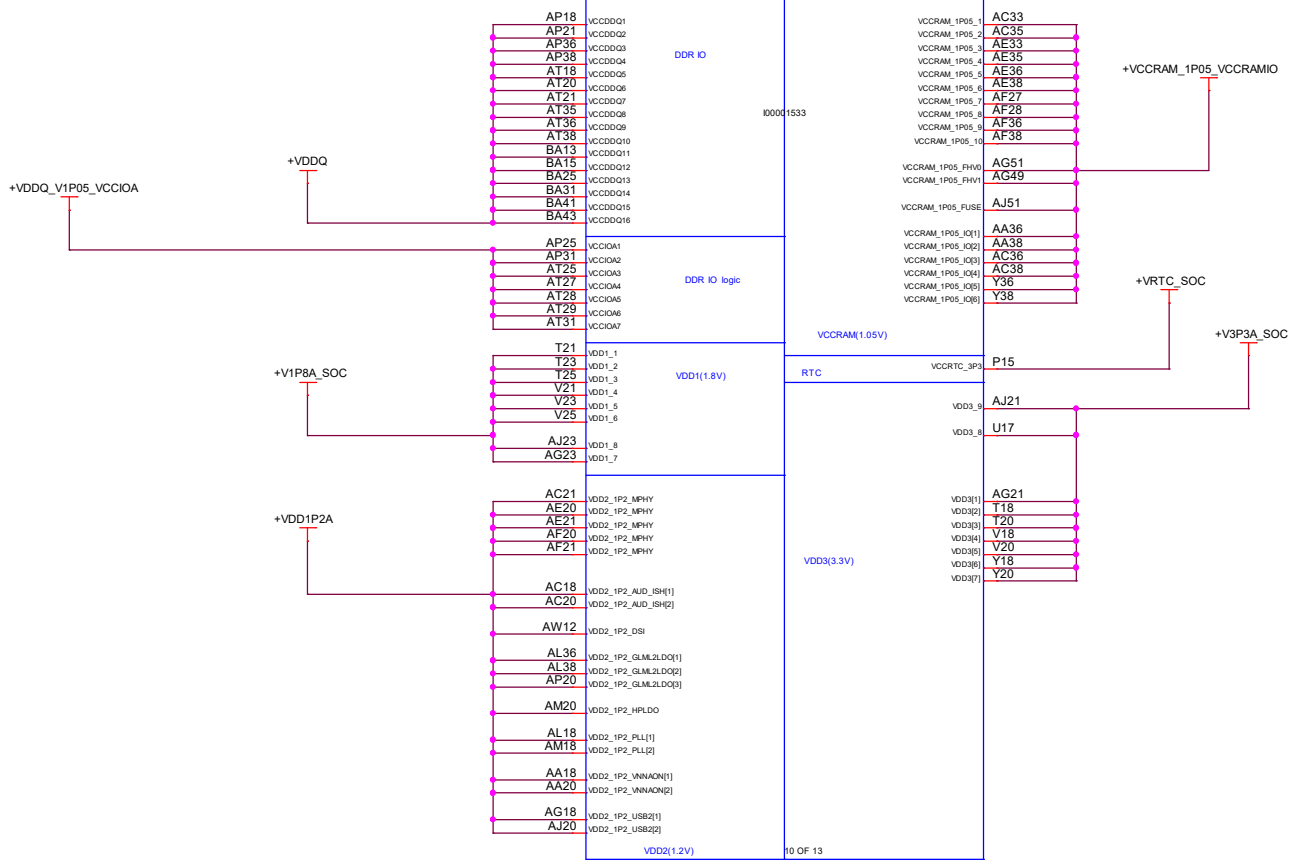
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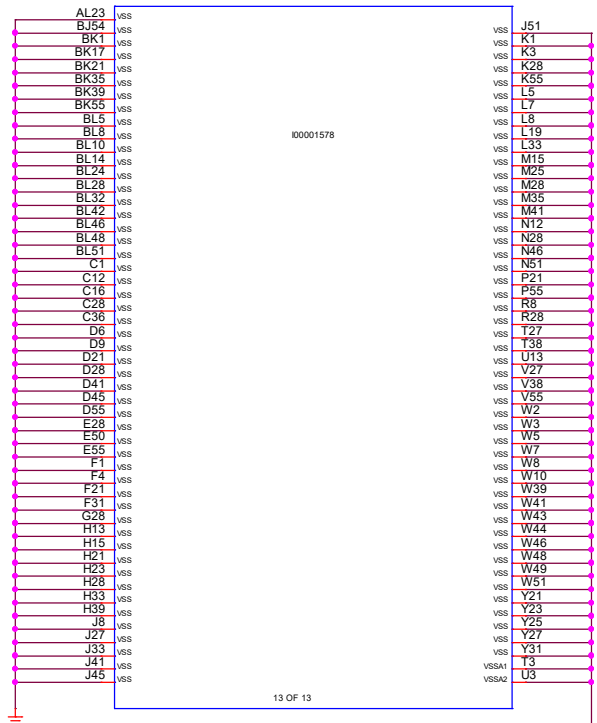
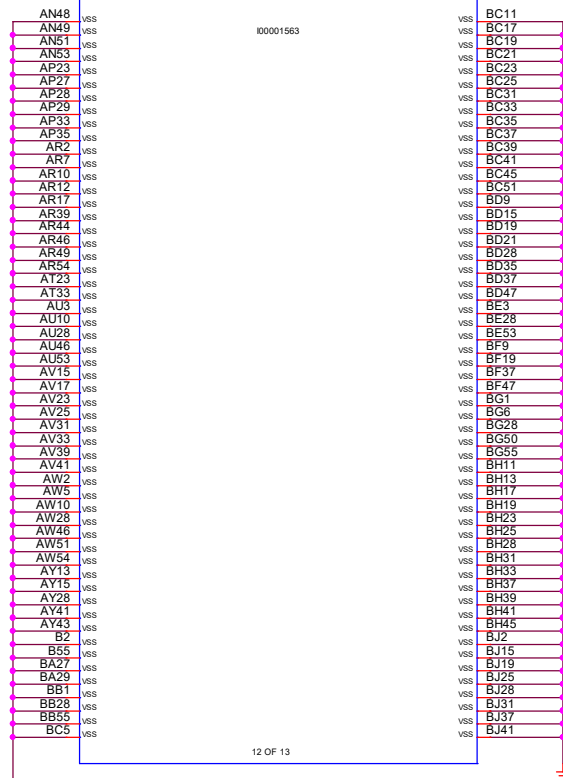
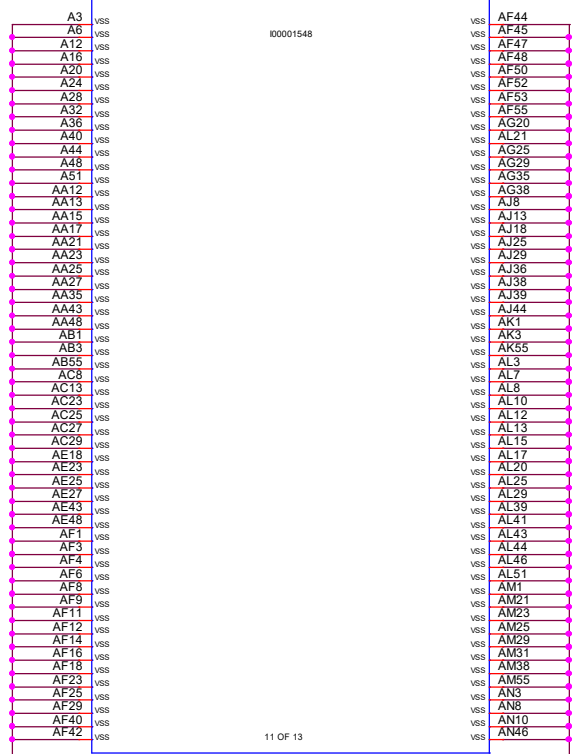


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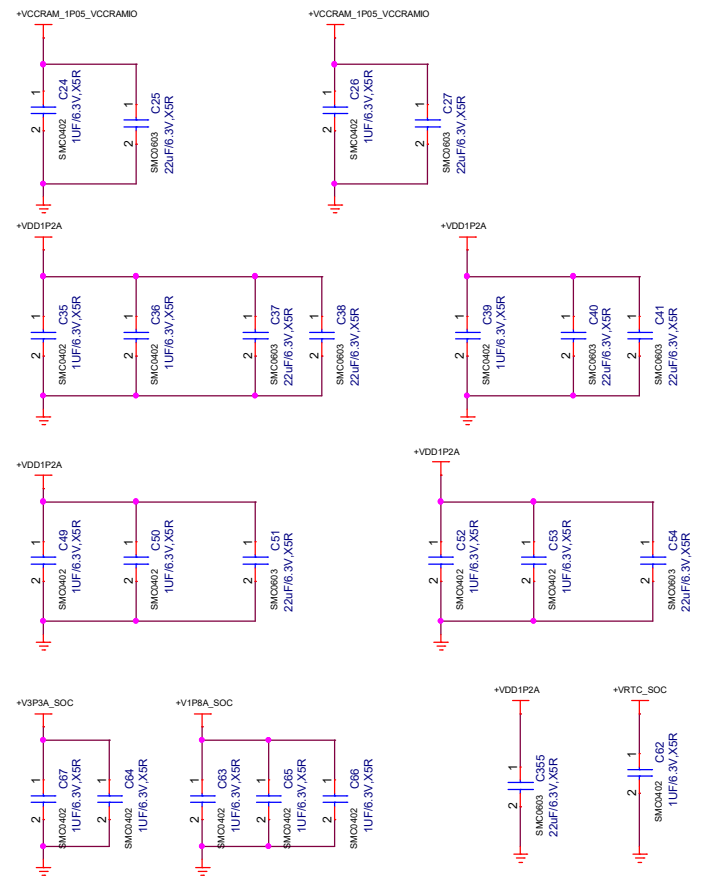
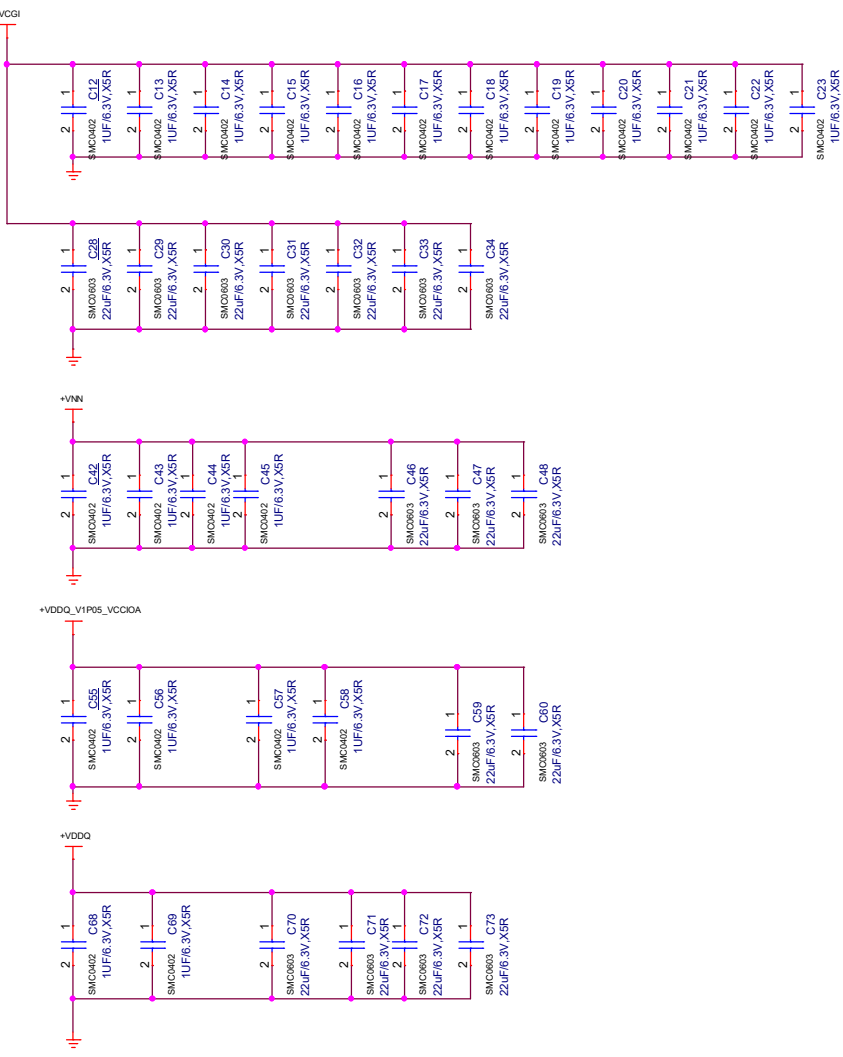
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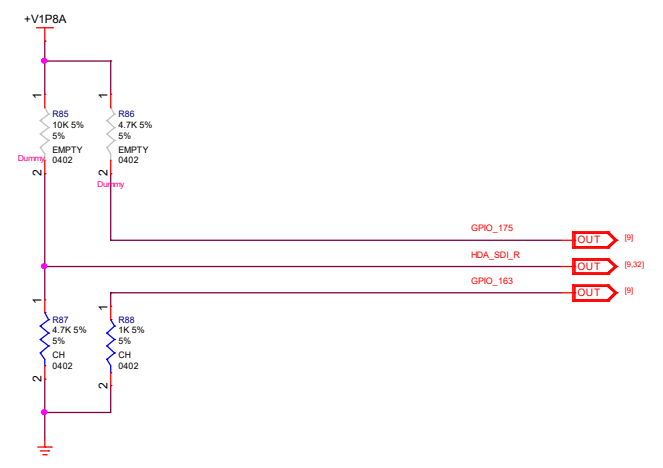
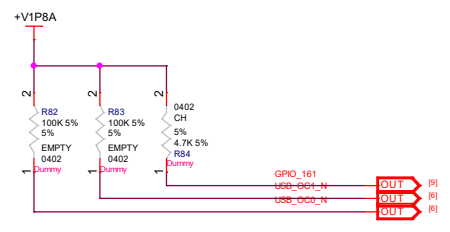
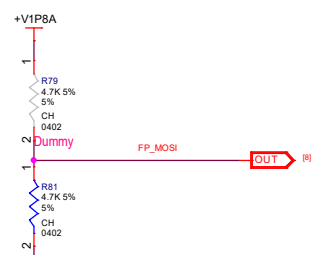
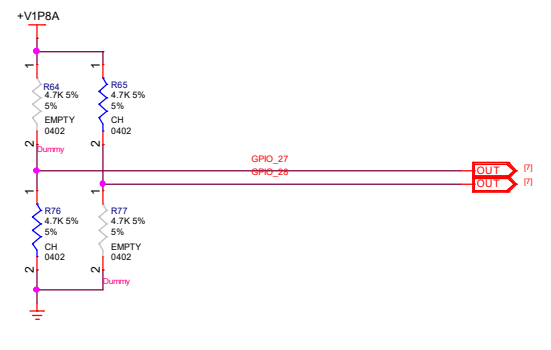
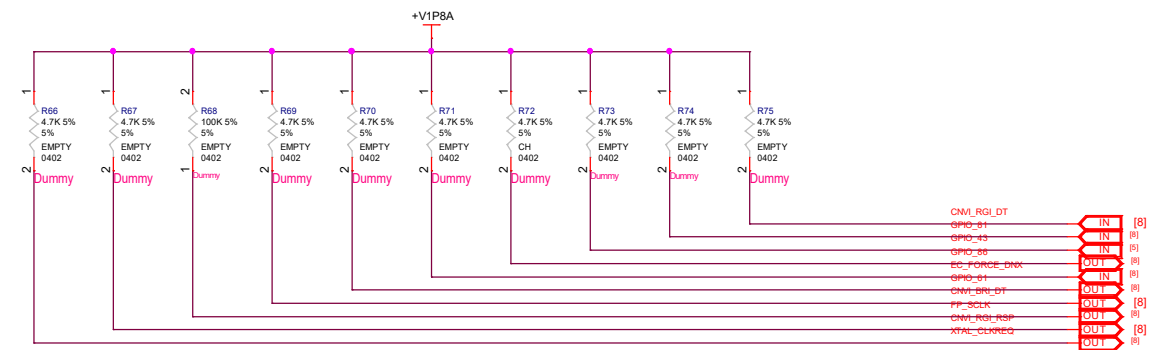
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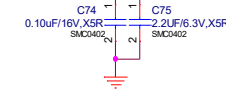
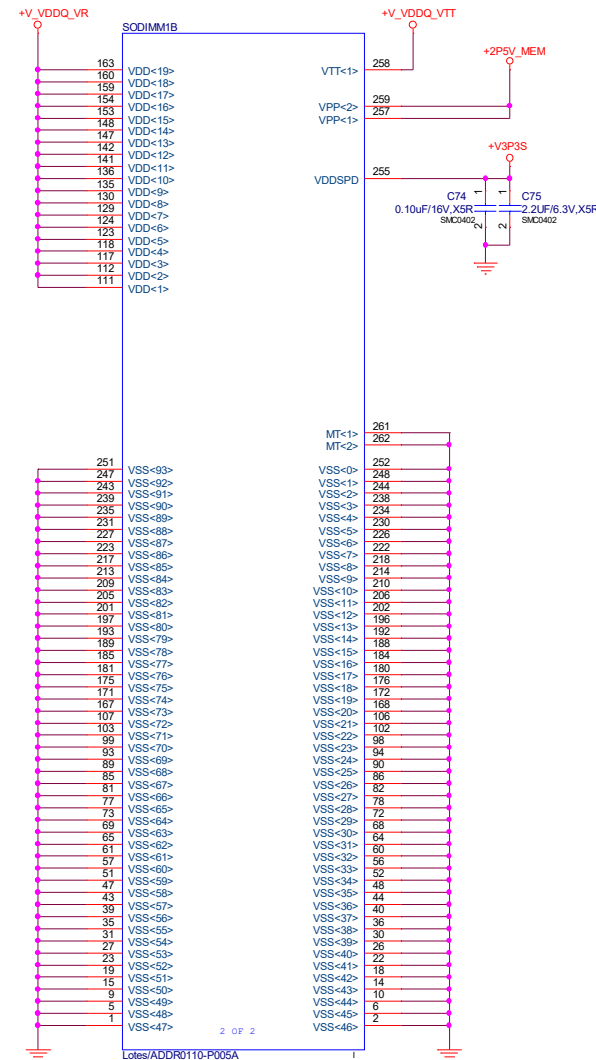
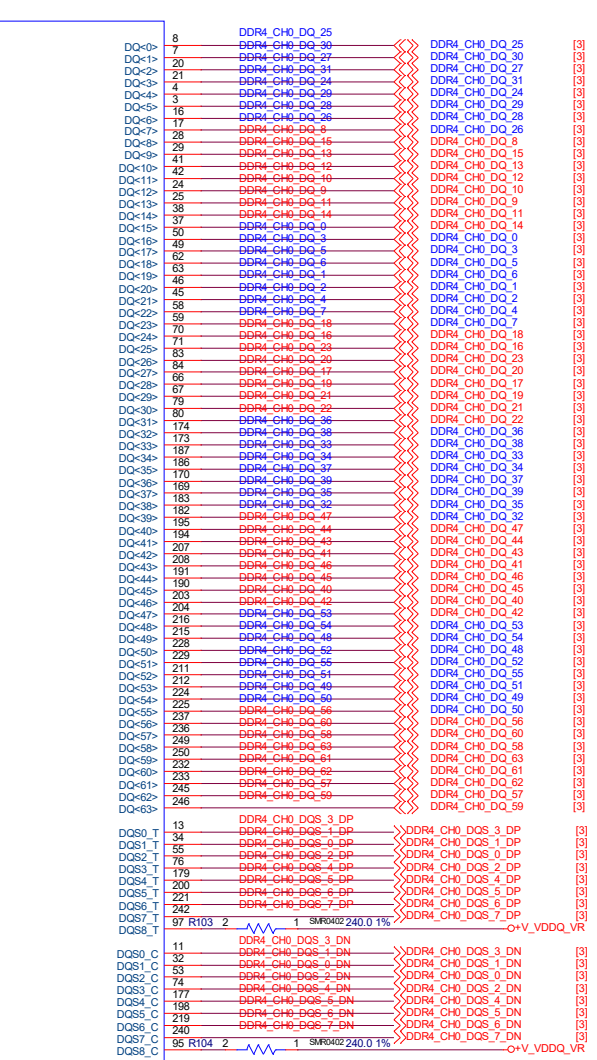
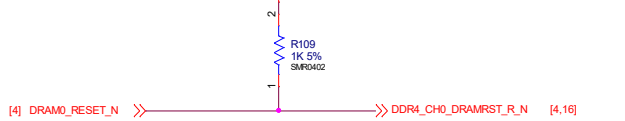
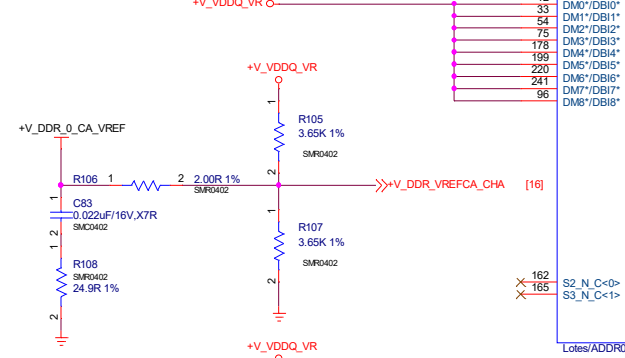
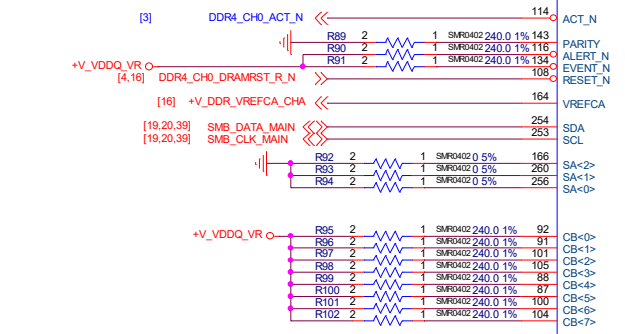
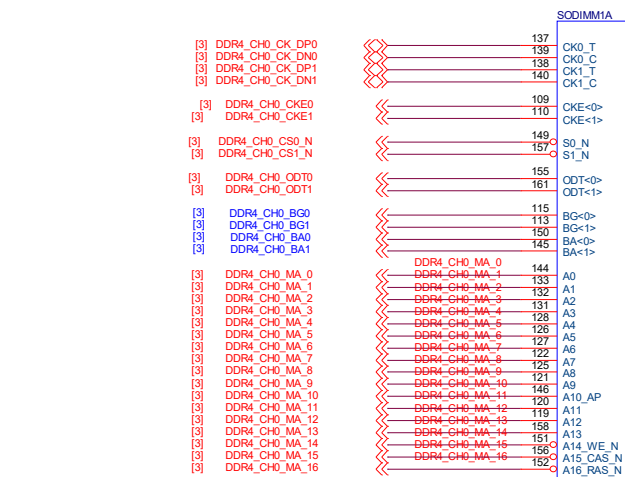
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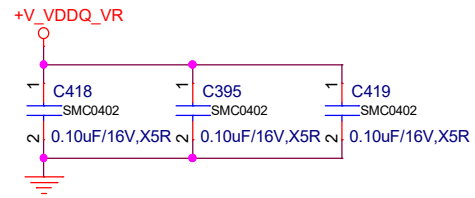
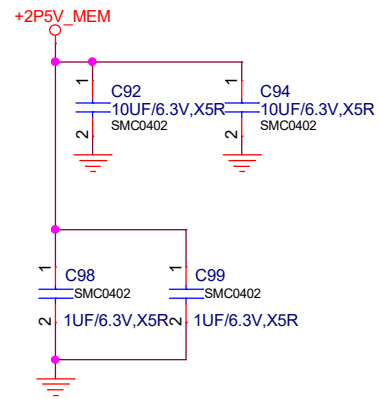
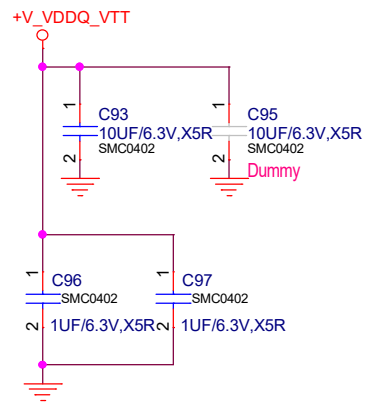
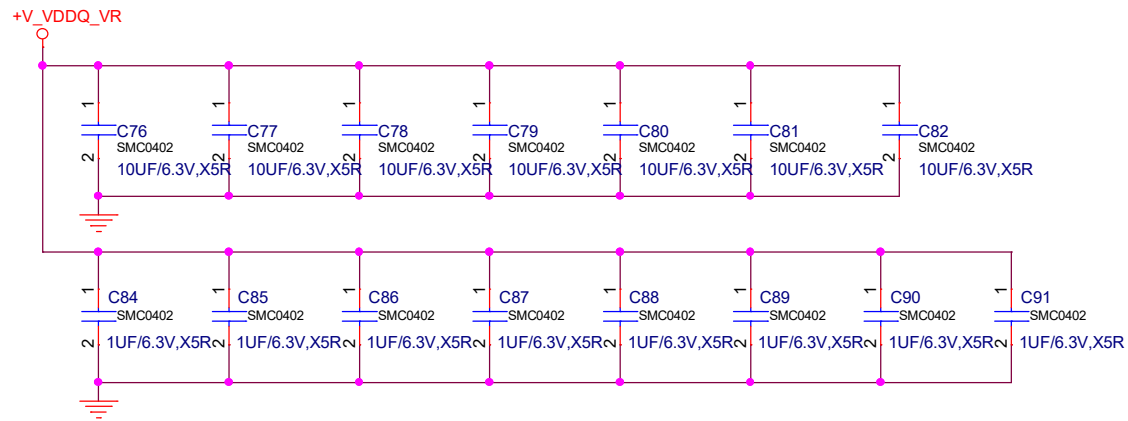
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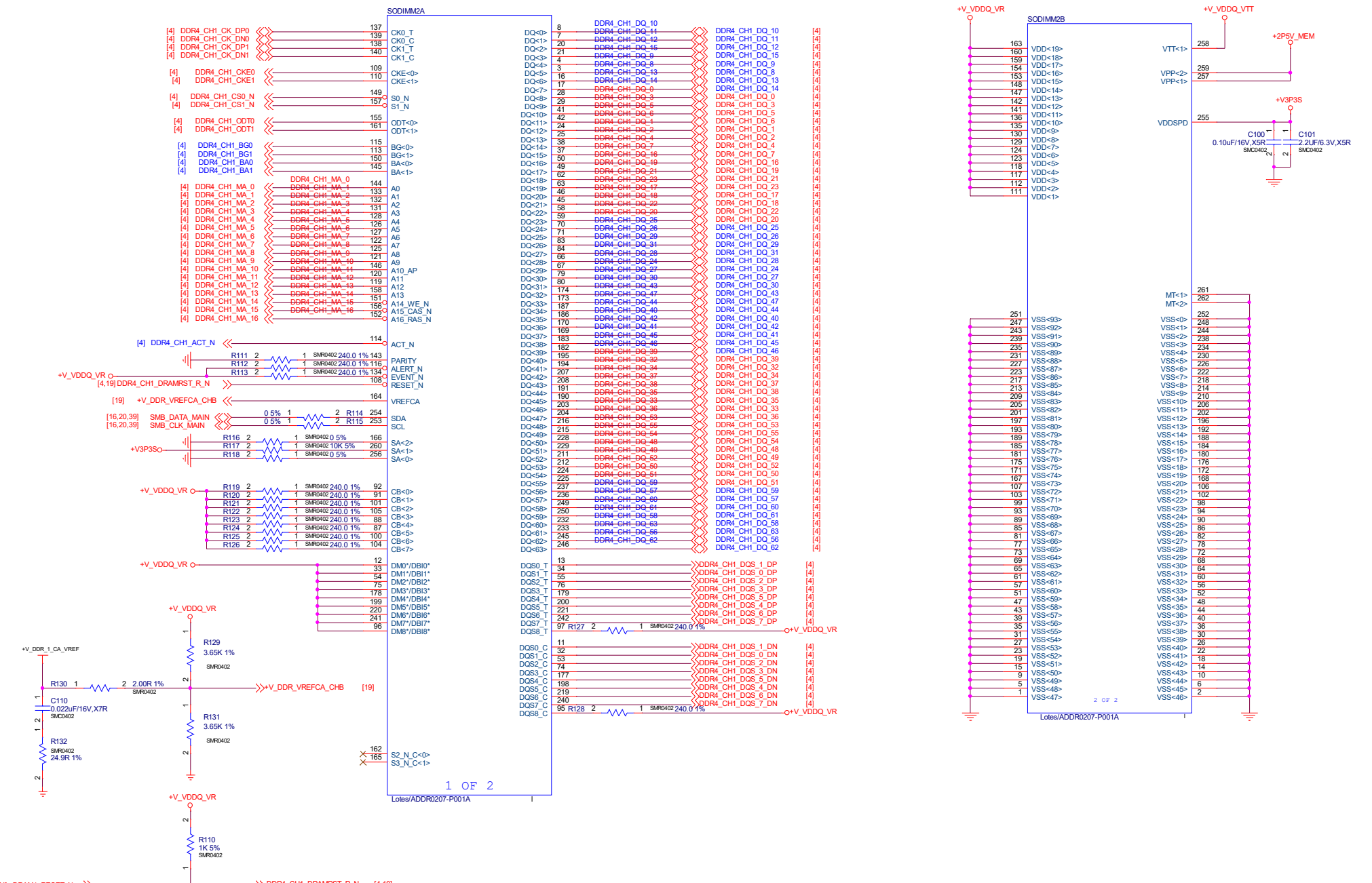
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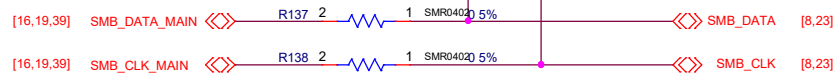
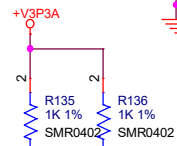
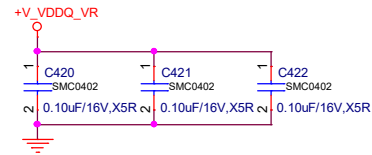
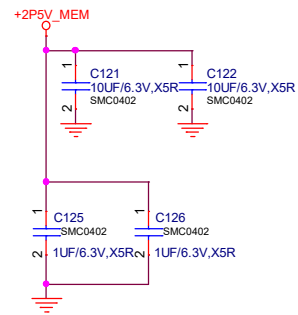
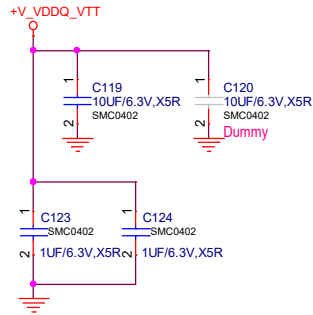
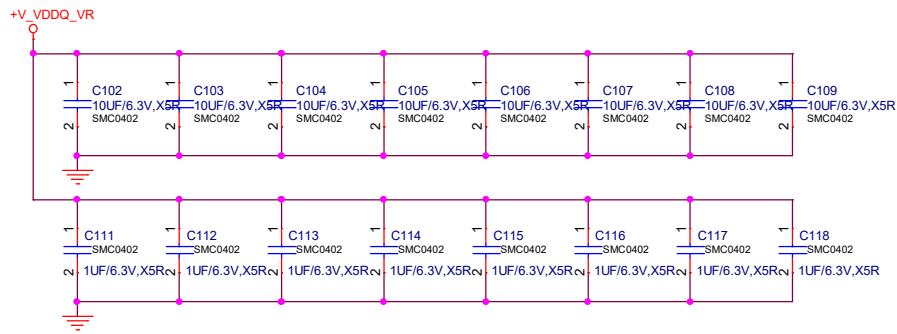
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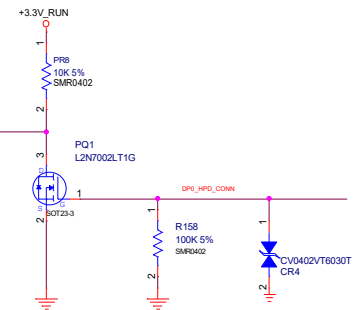
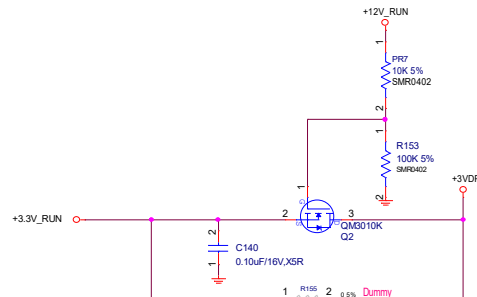
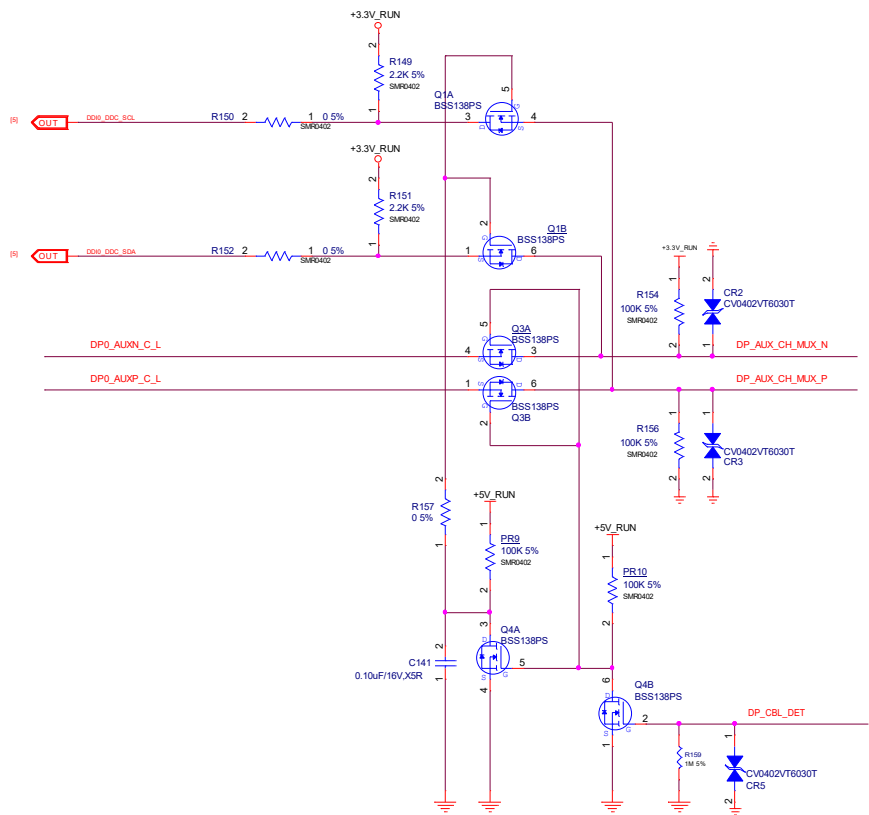
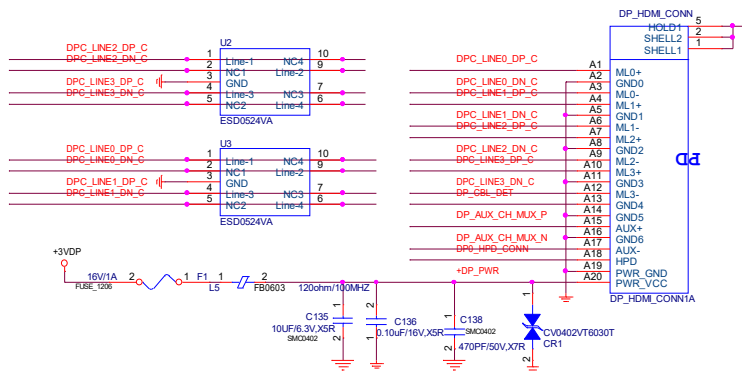
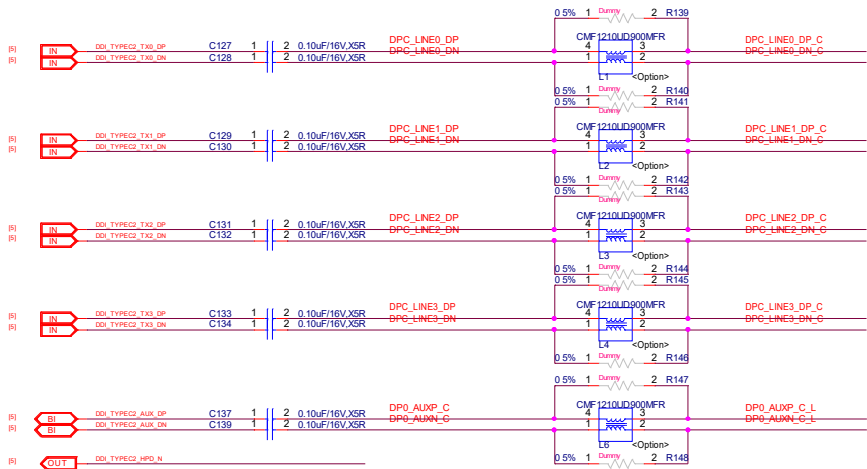


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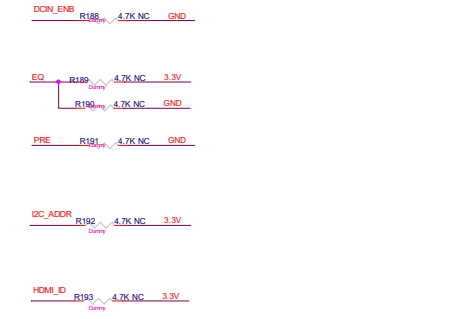
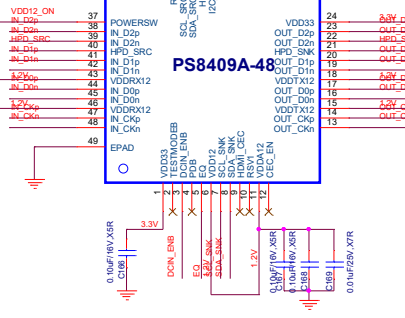
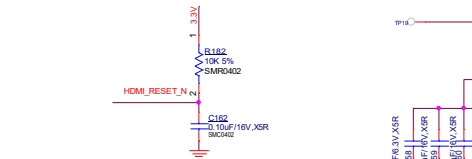
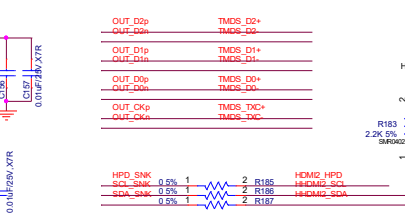
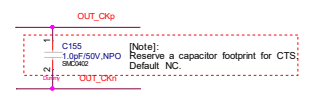
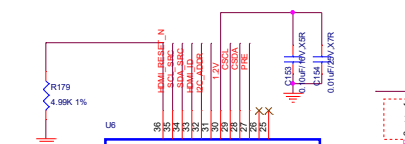
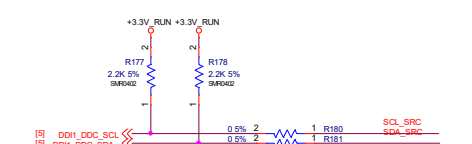
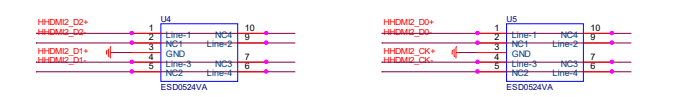
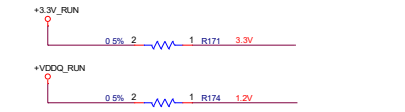
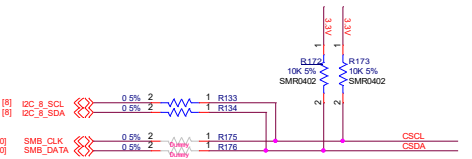
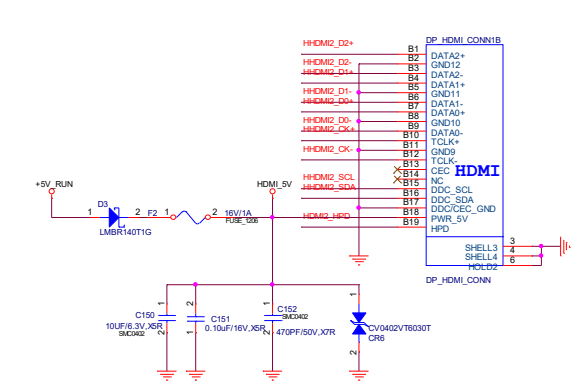
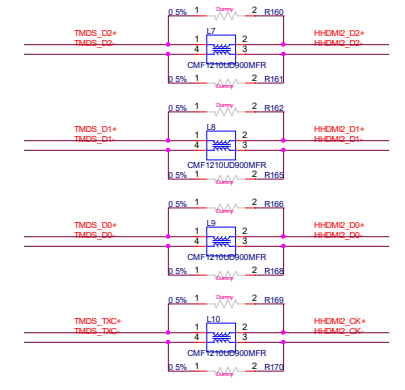
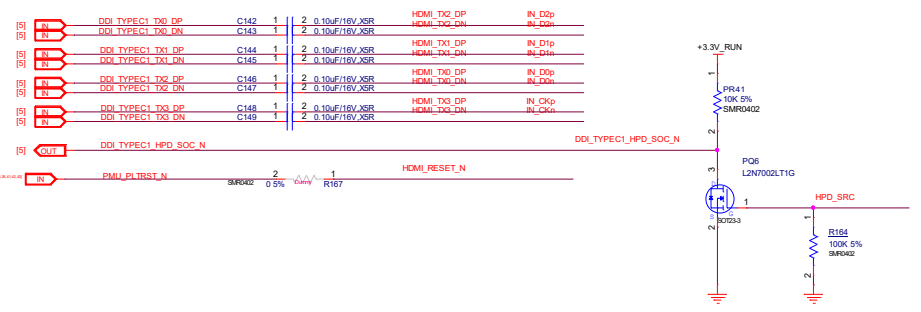




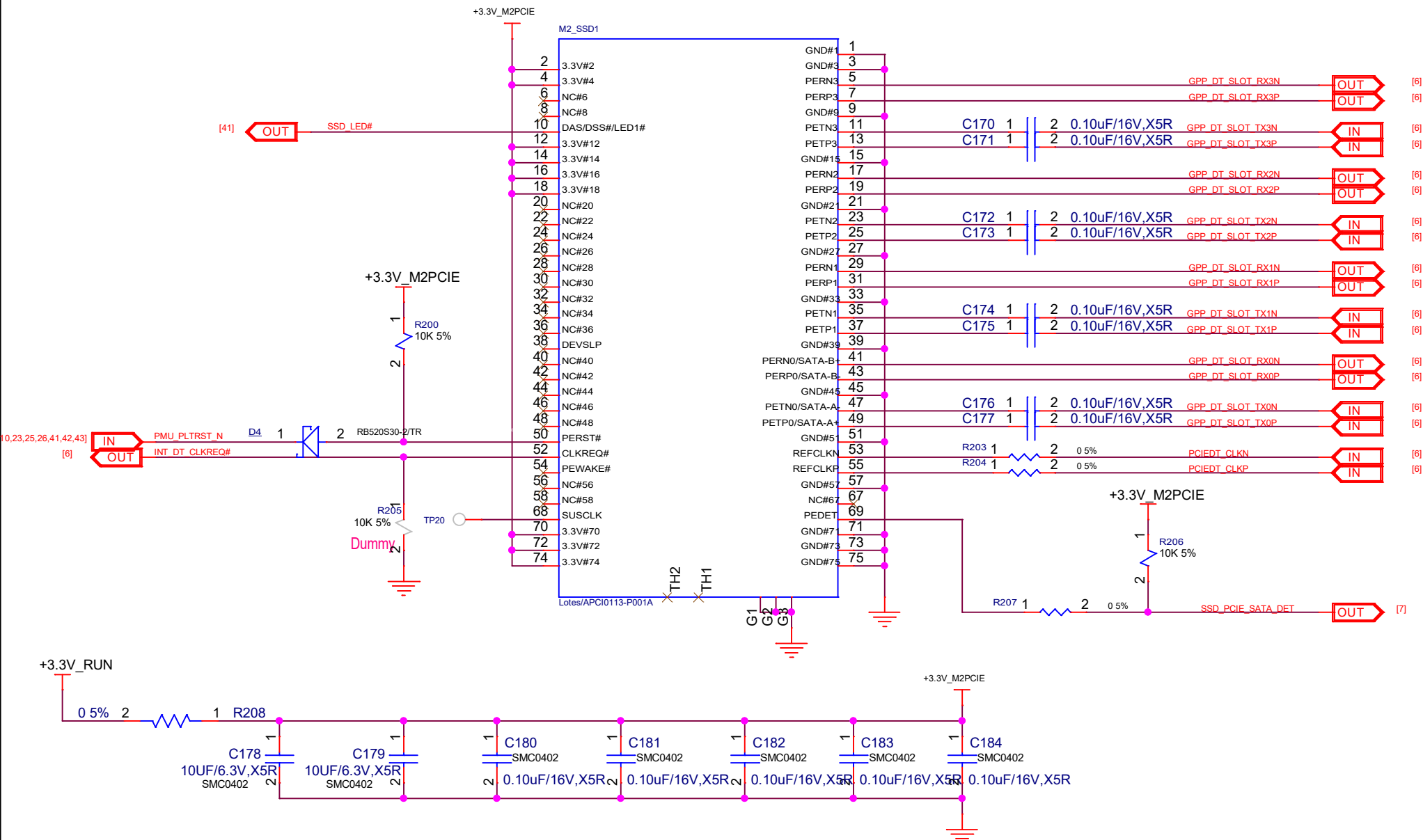
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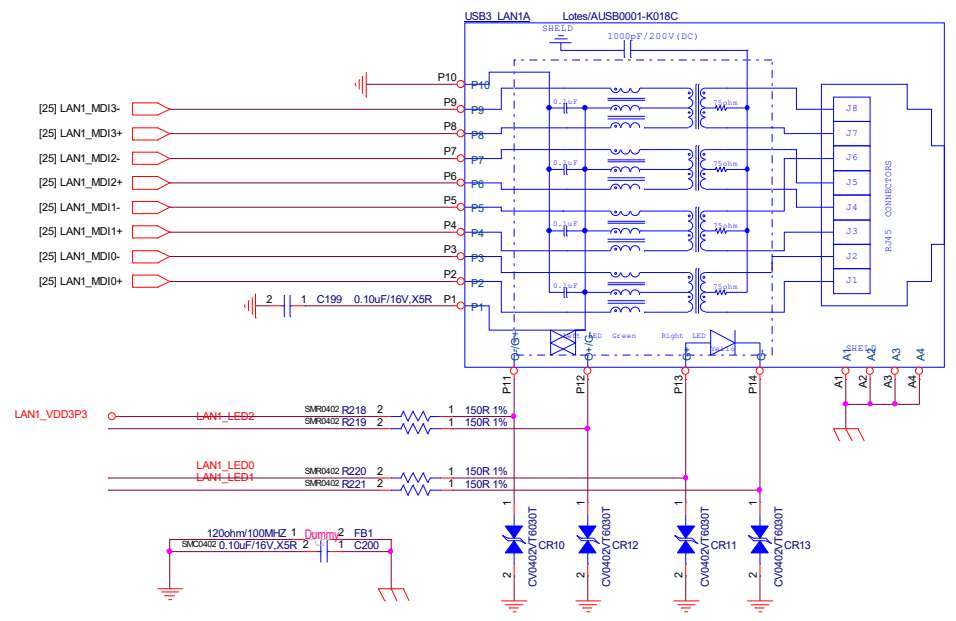
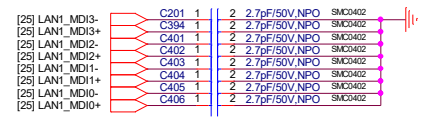
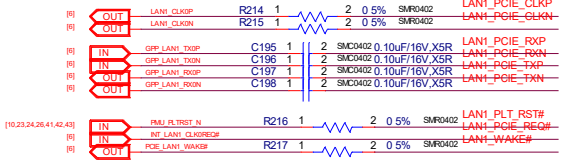
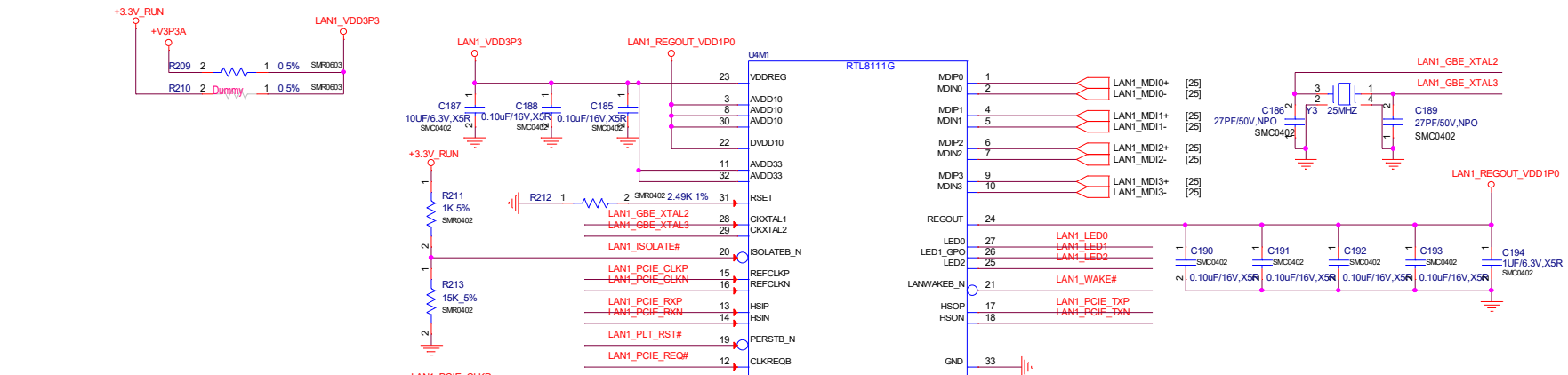
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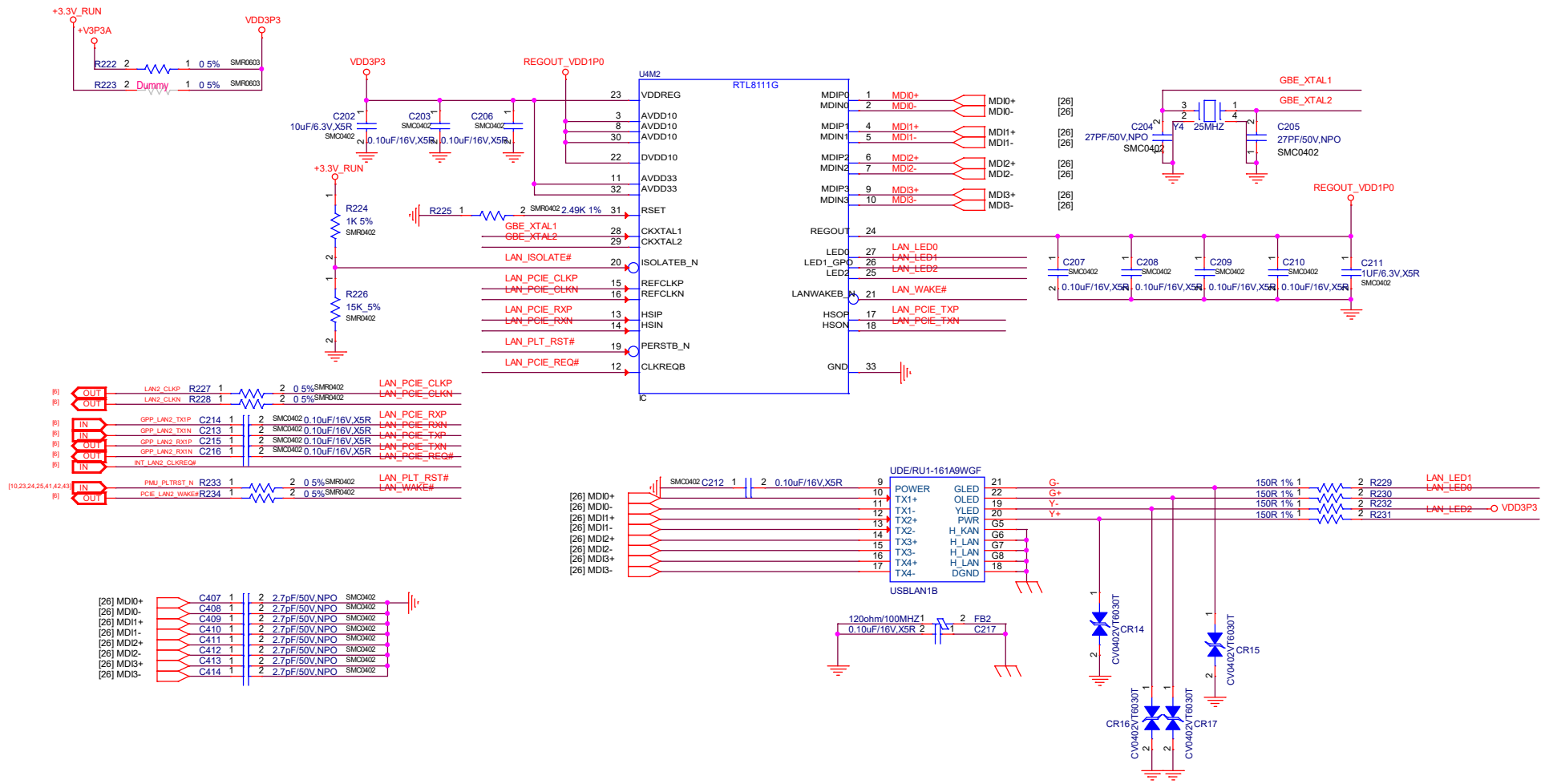
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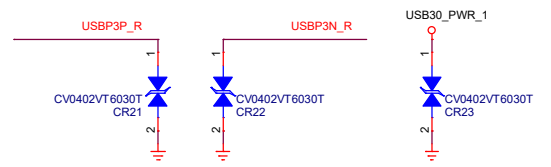
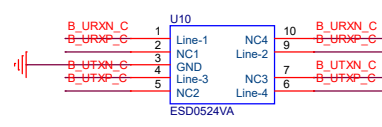
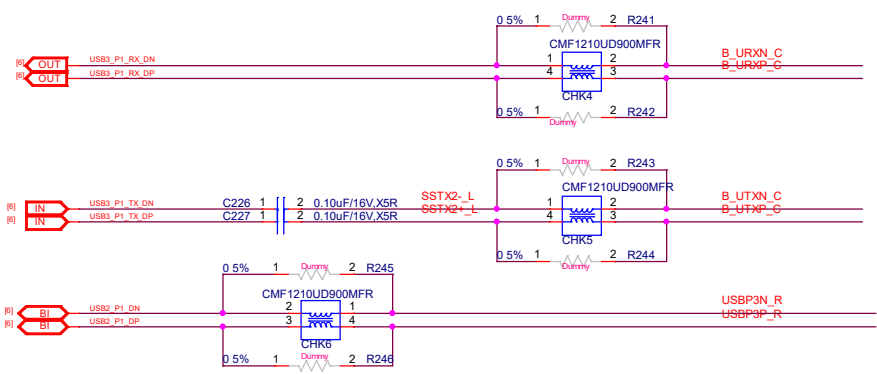
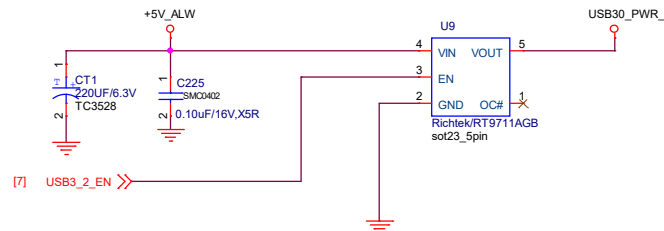
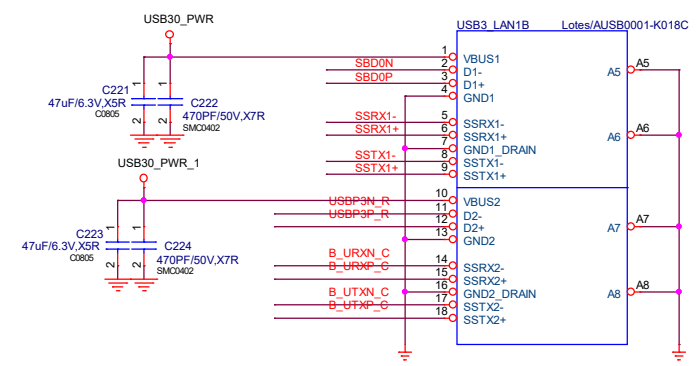
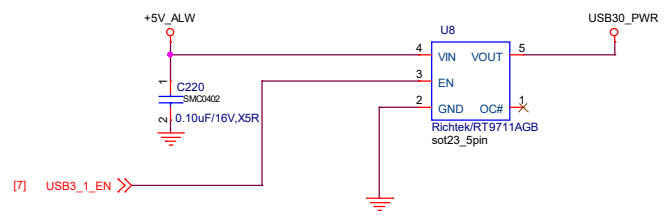
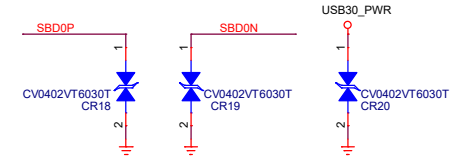
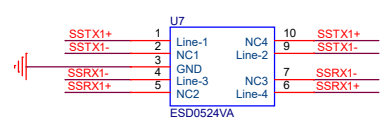
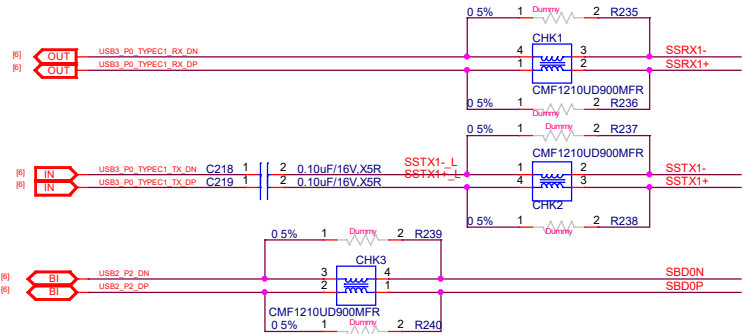


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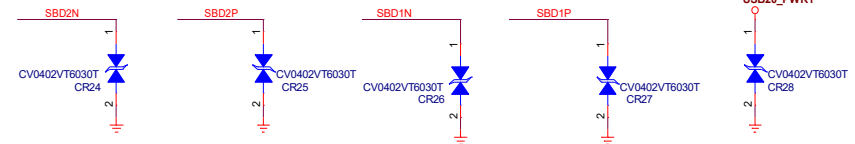
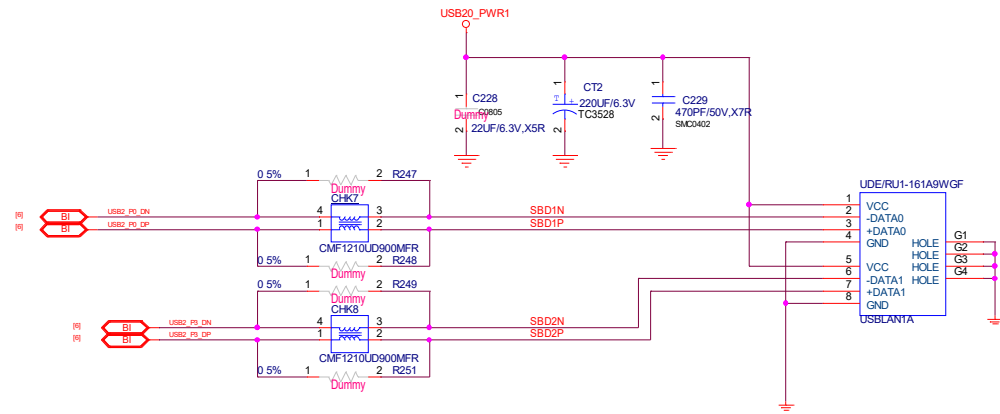
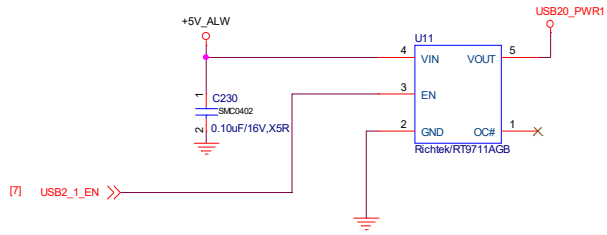


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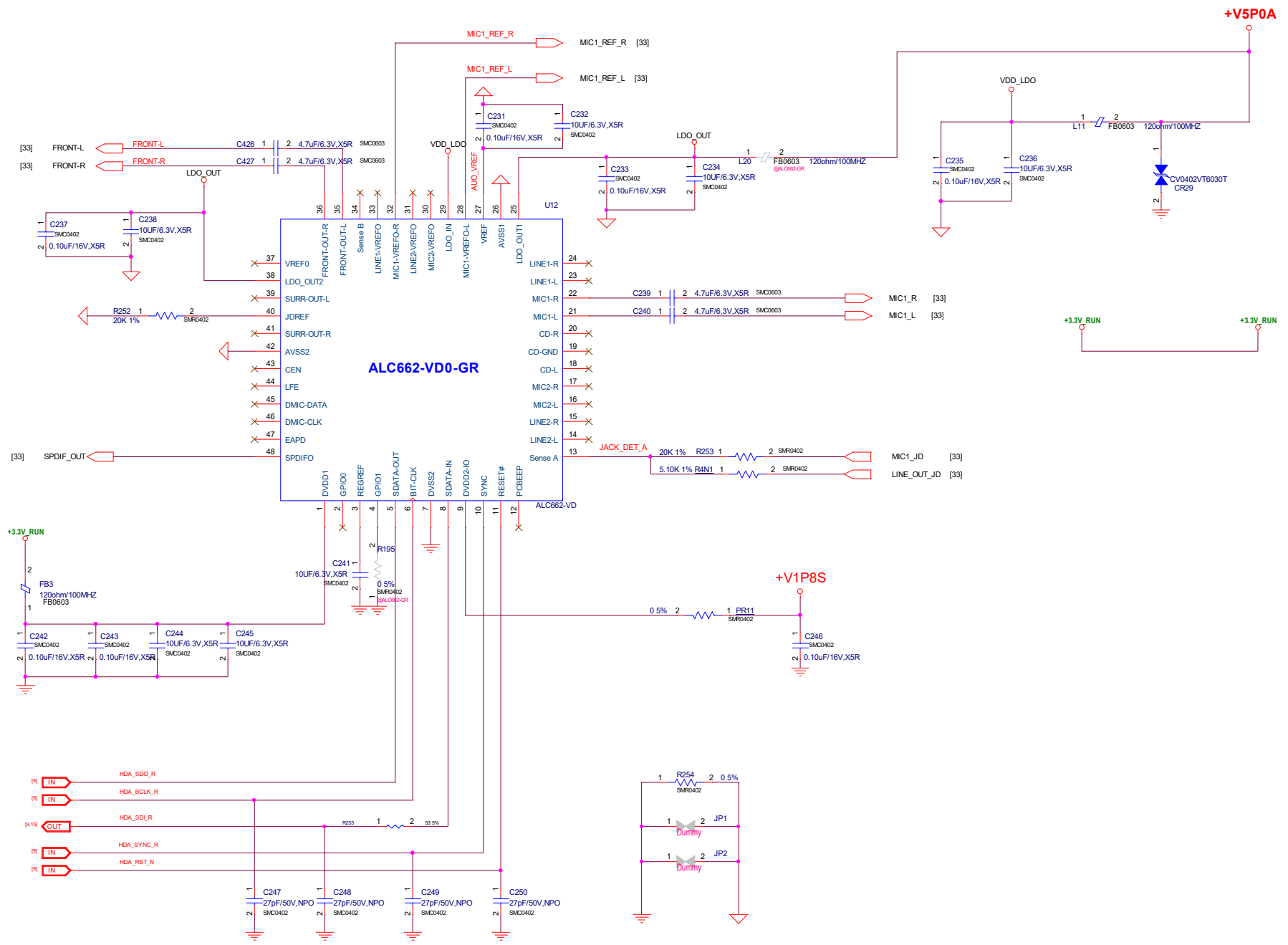




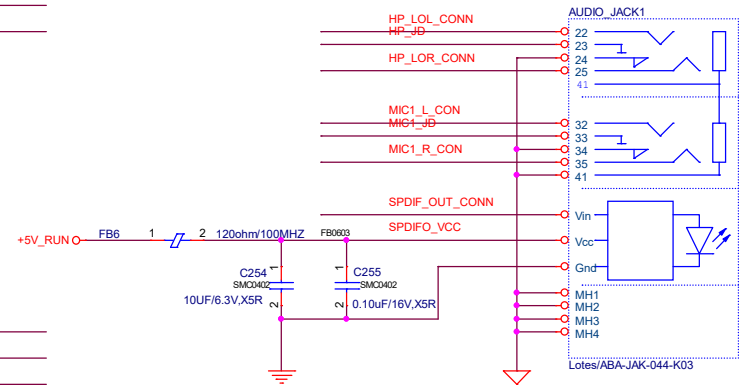
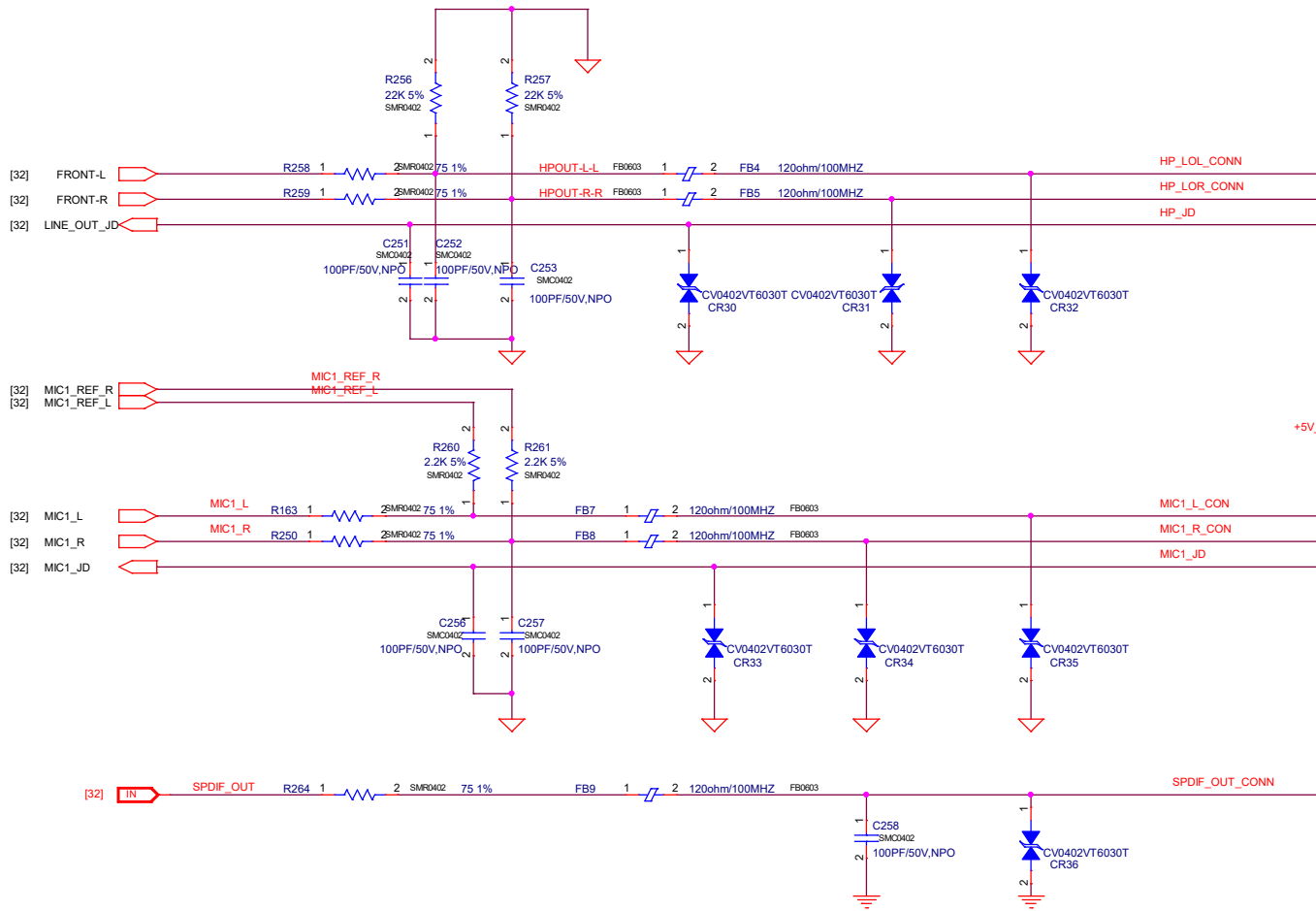
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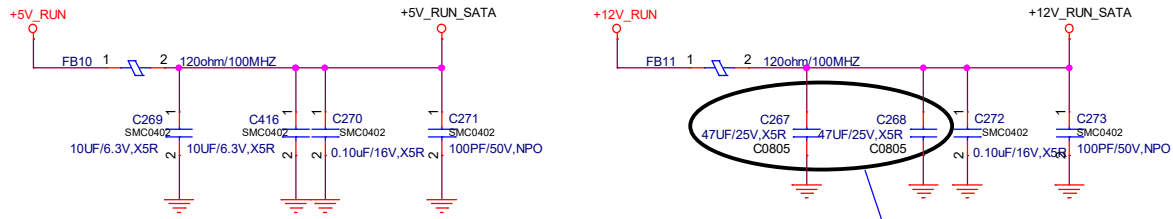
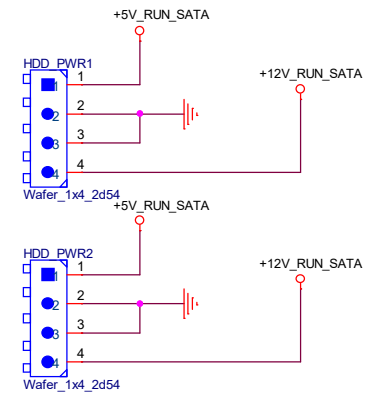
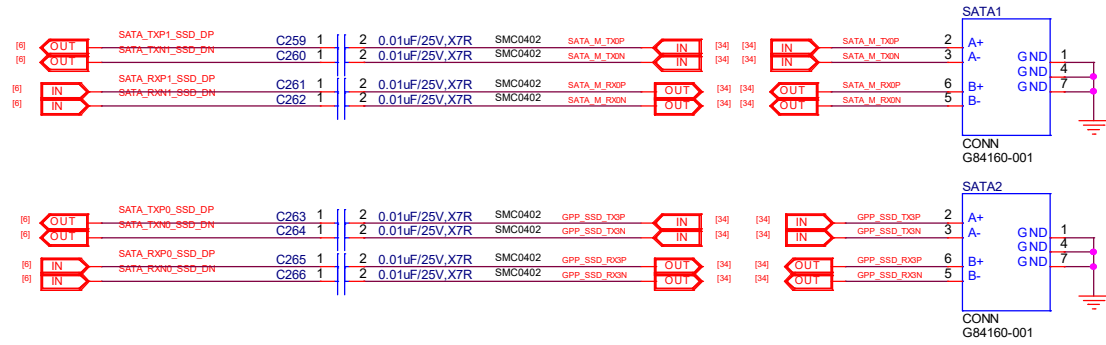
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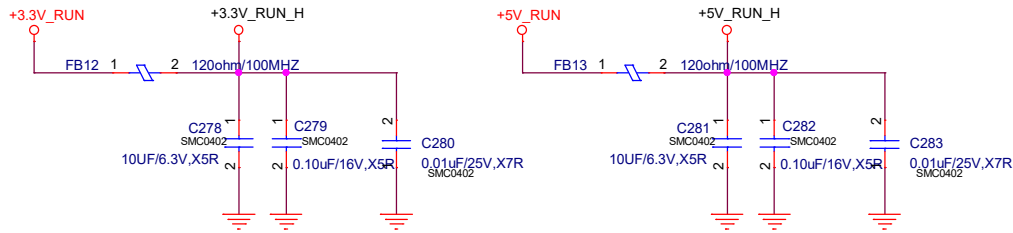
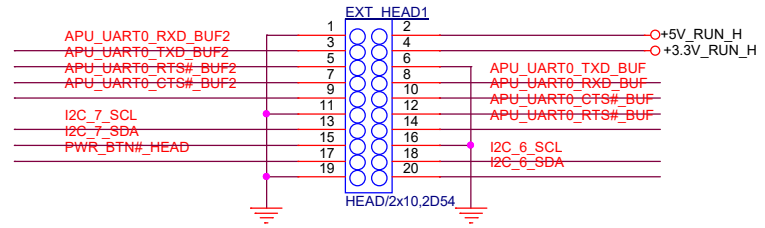
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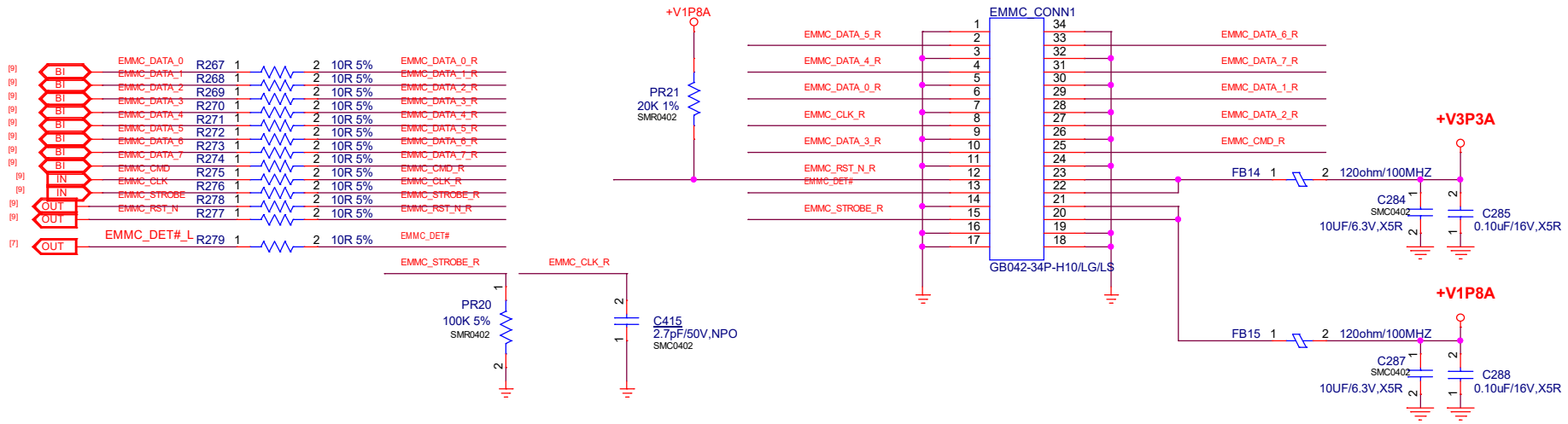


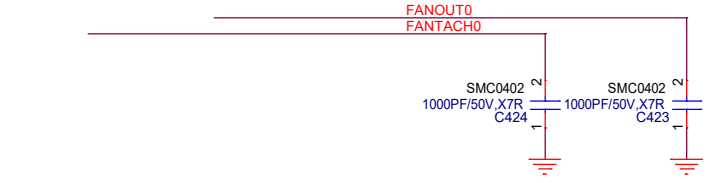
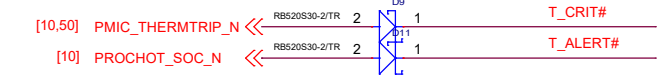
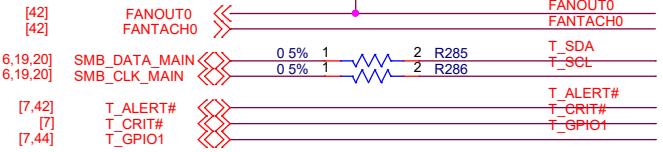
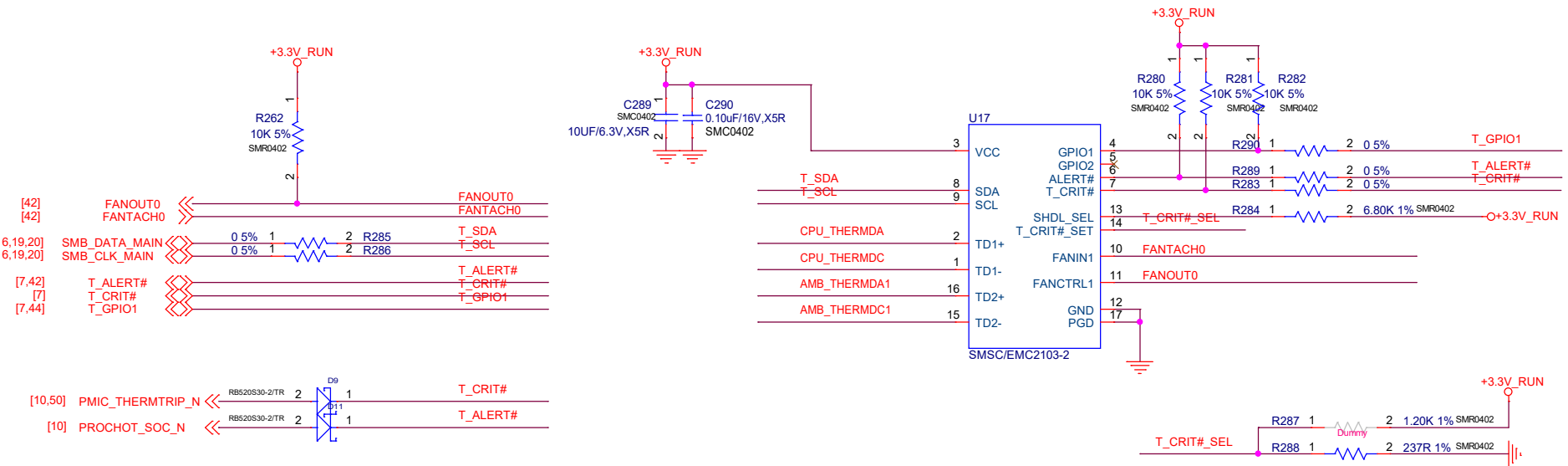
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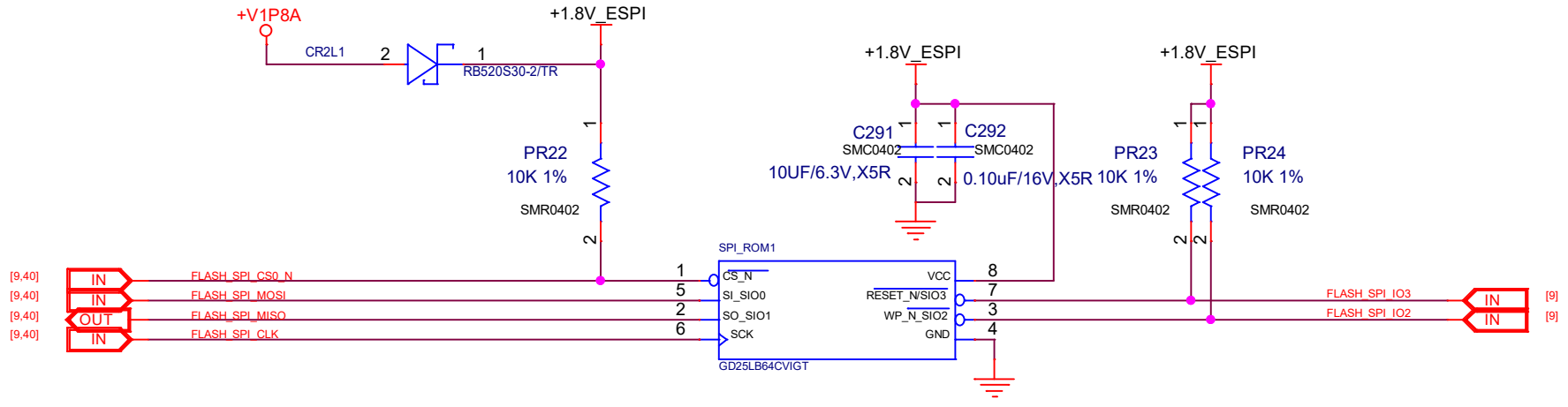


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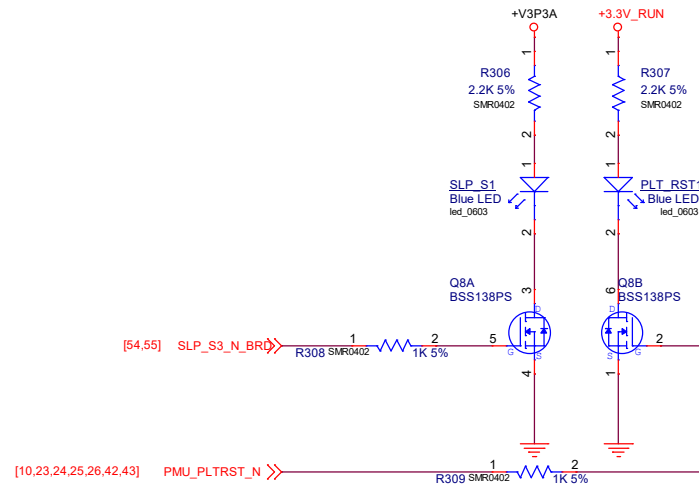
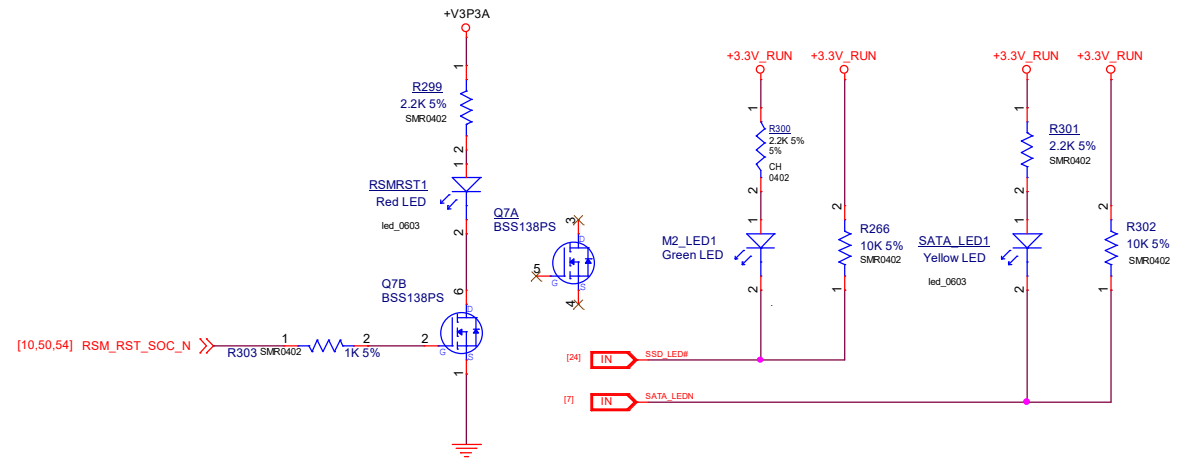
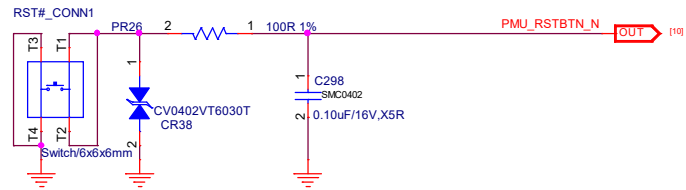
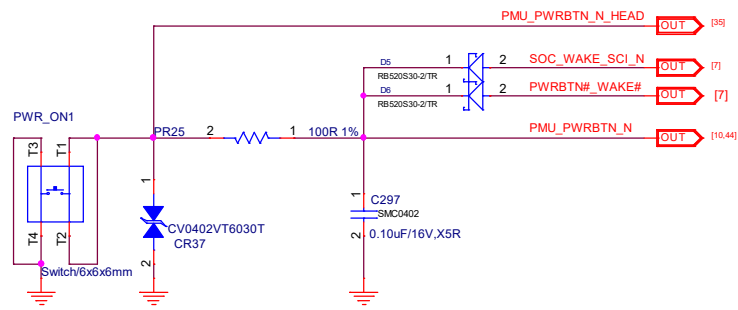


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[9,40]
[9,40]
[9,40]

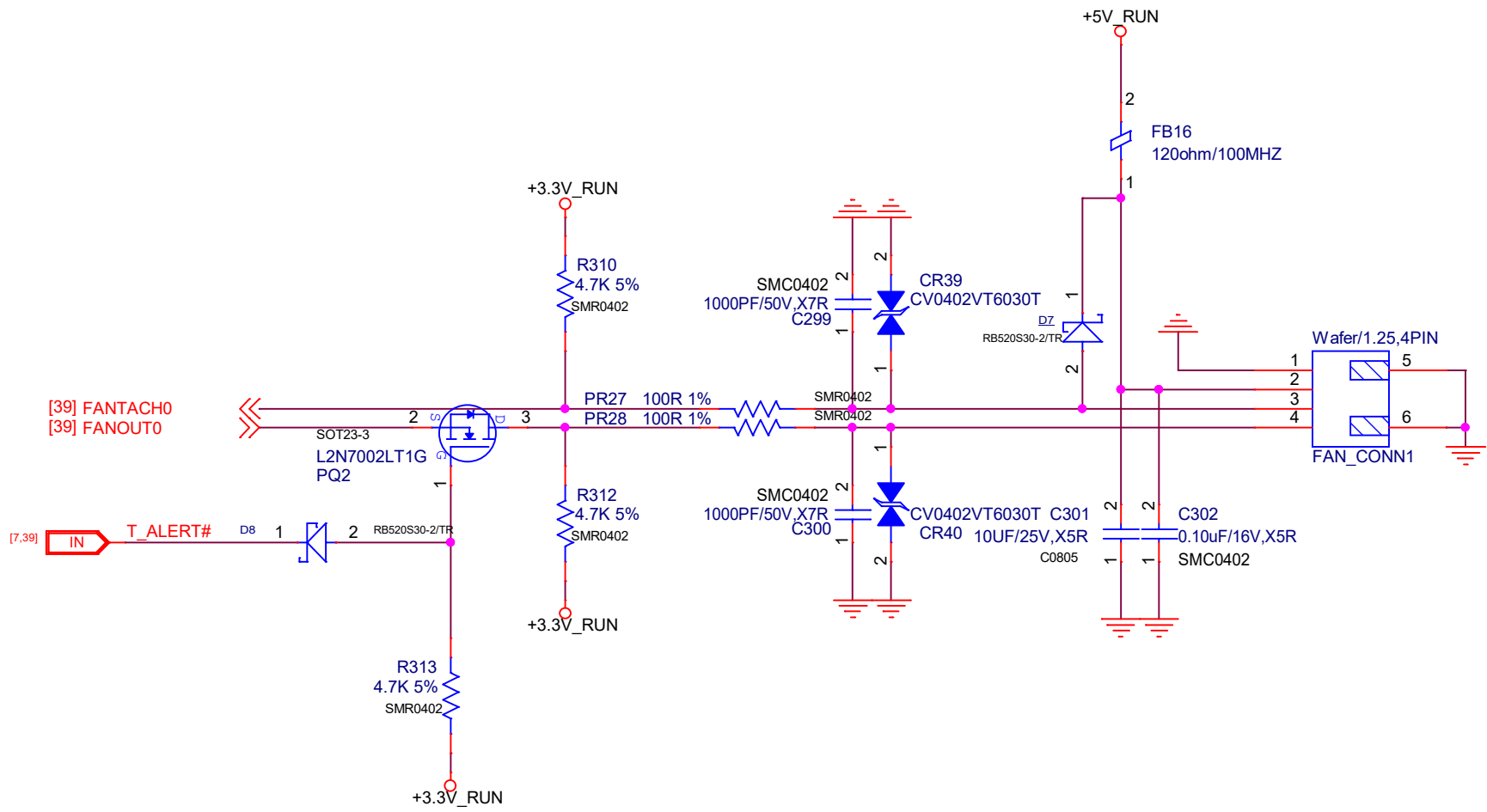
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[9]

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| [9,40] | IN | FLASH_SPI_MOSI | C294 | 1 | 2 | 10PF/50V,NPO | SMC0402 |
| [9,40] | OUT | FLASH_SPI_MISO | C295 | 1 | 2 | 10PF/50V,NPO | SMC0402 |
| [9,40] | IN | FLASH_SPI_CS0_N | C296 | 1 | 2 | 10PF/50V,NPO | SMC0402 |

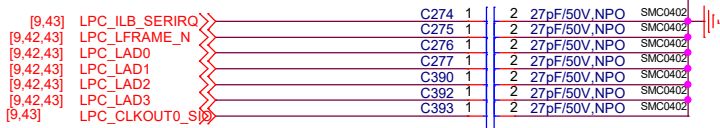
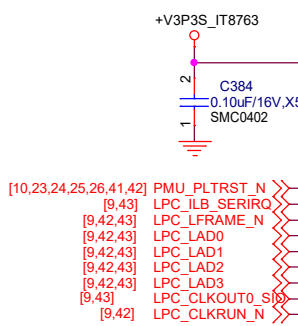
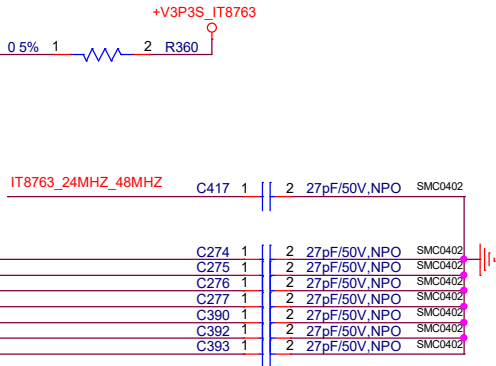
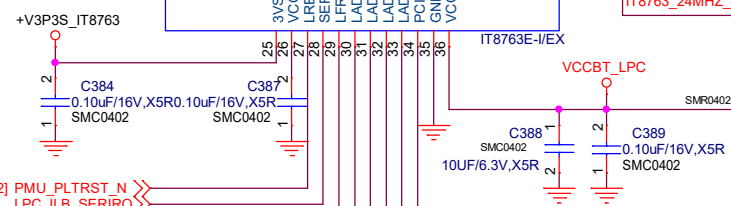
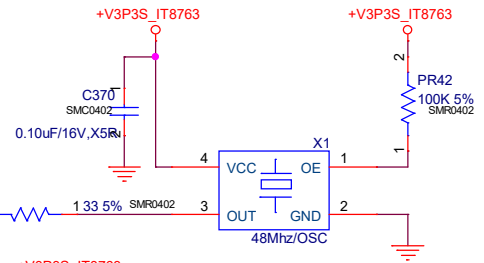
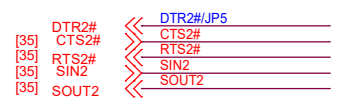
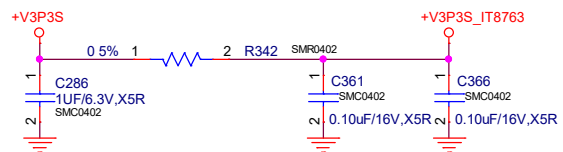
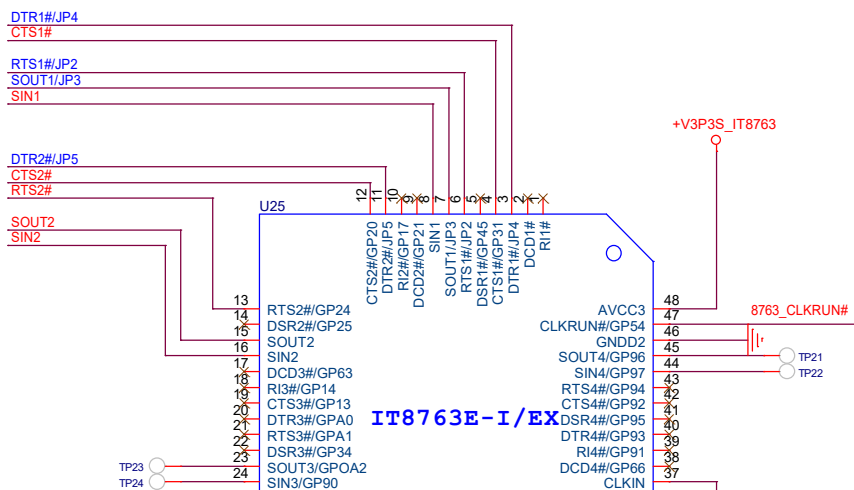
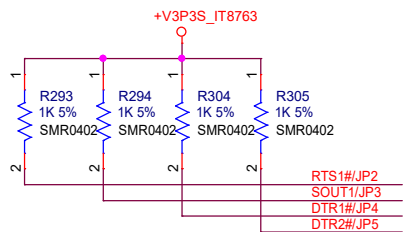
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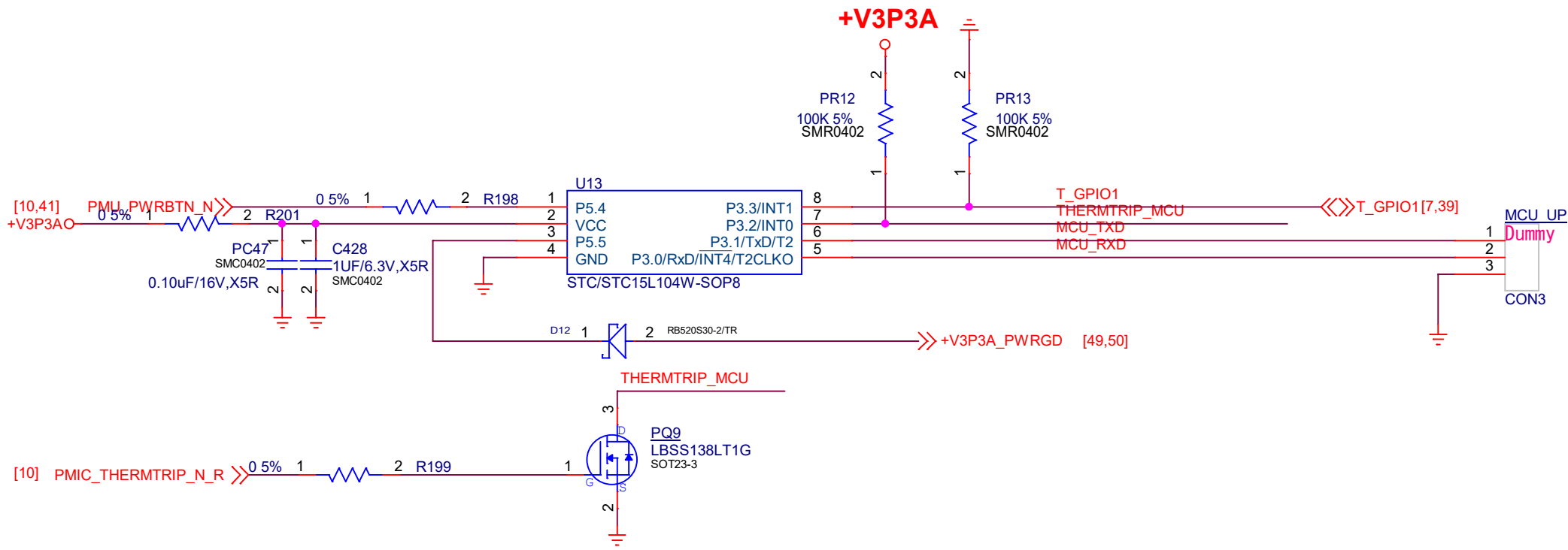
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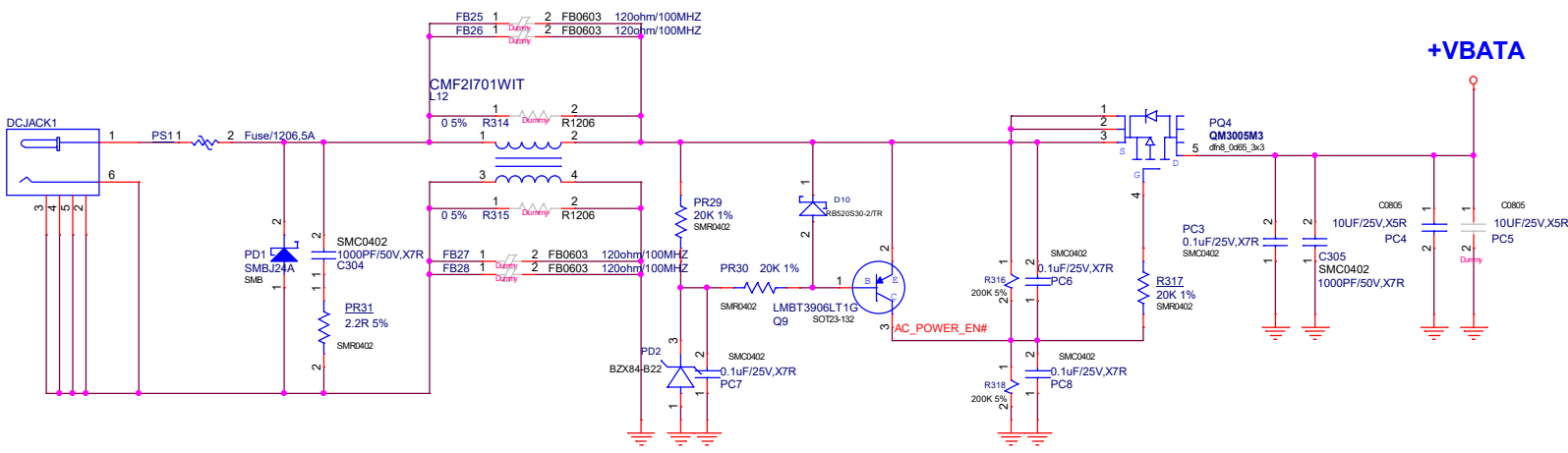
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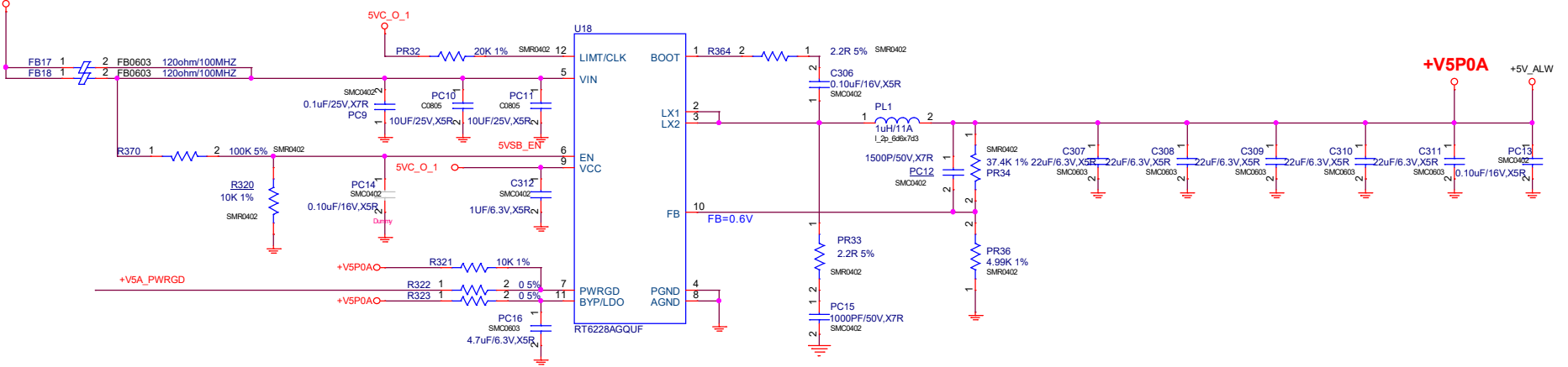
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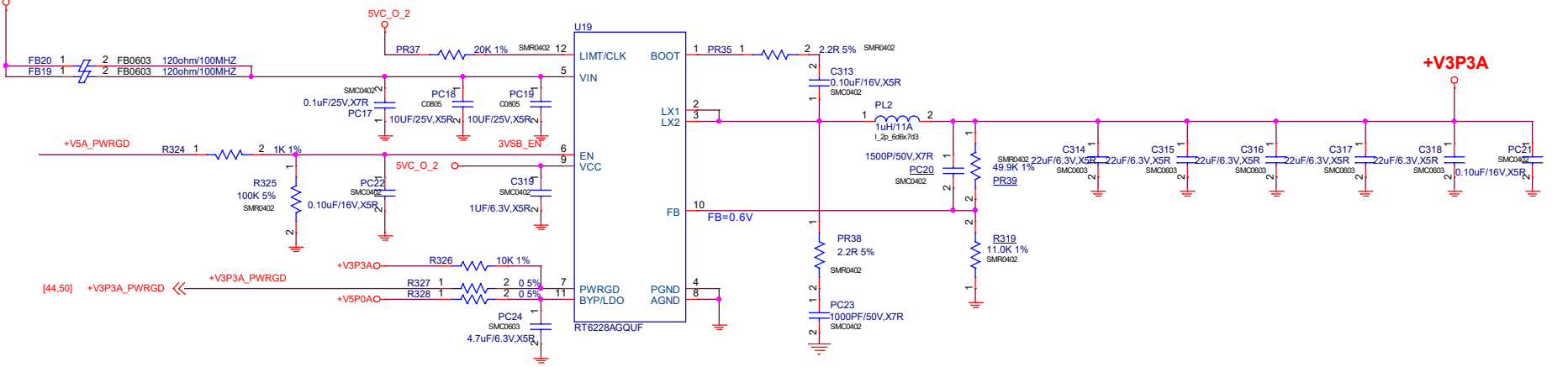
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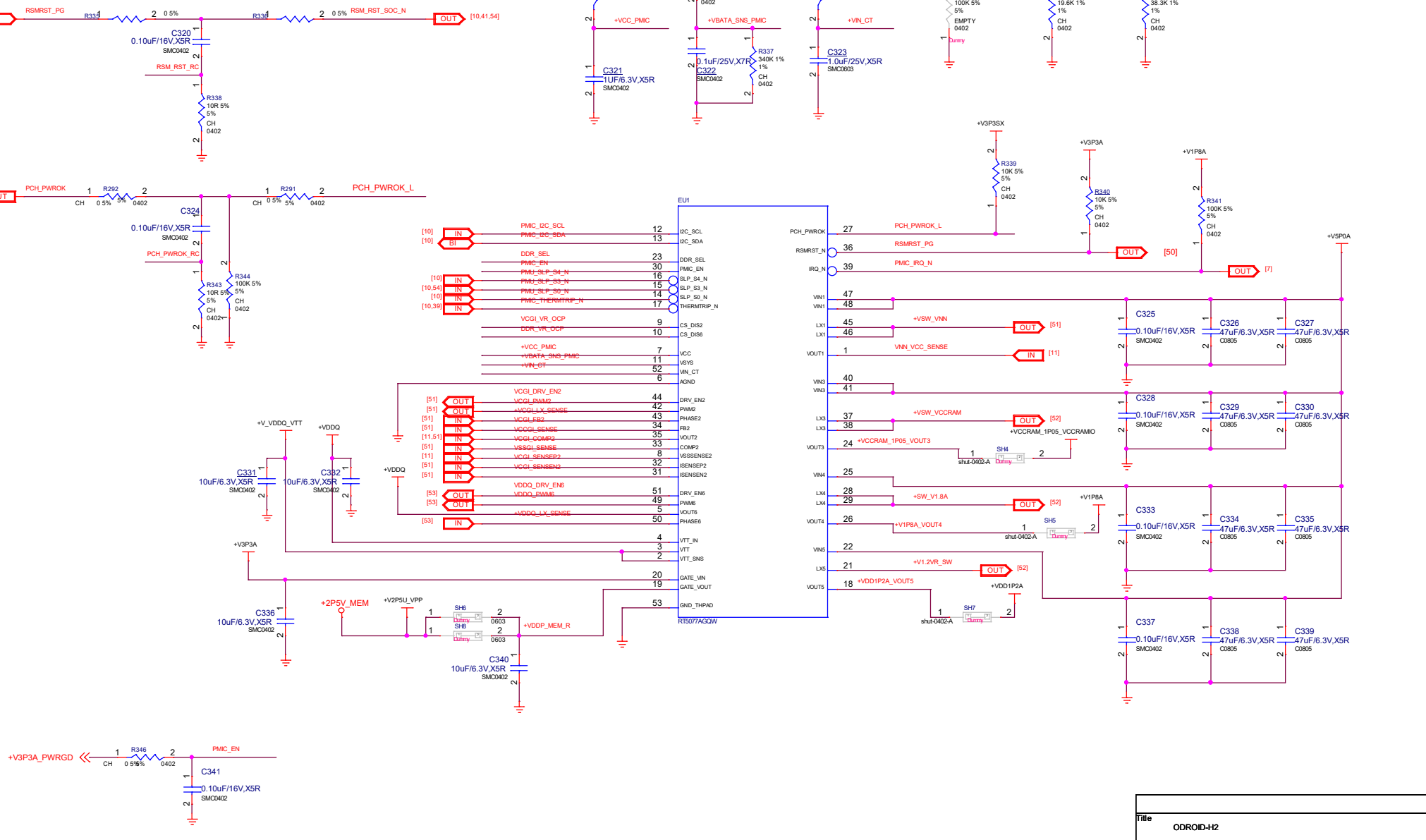
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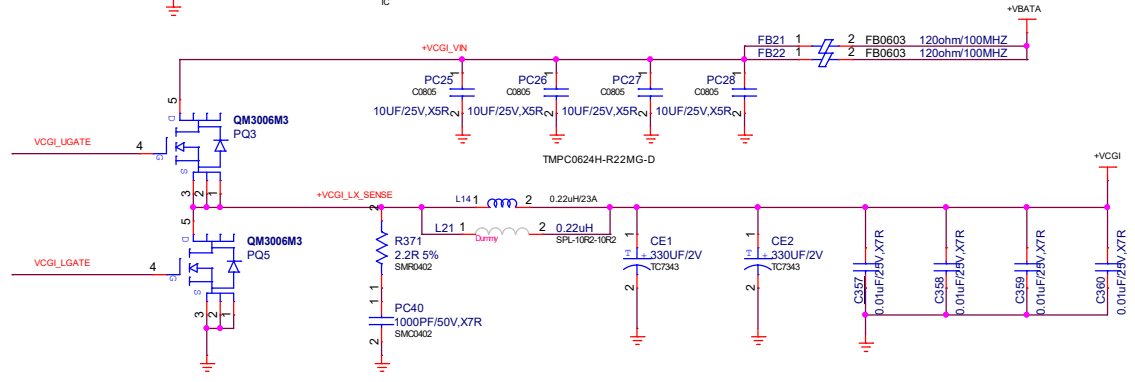
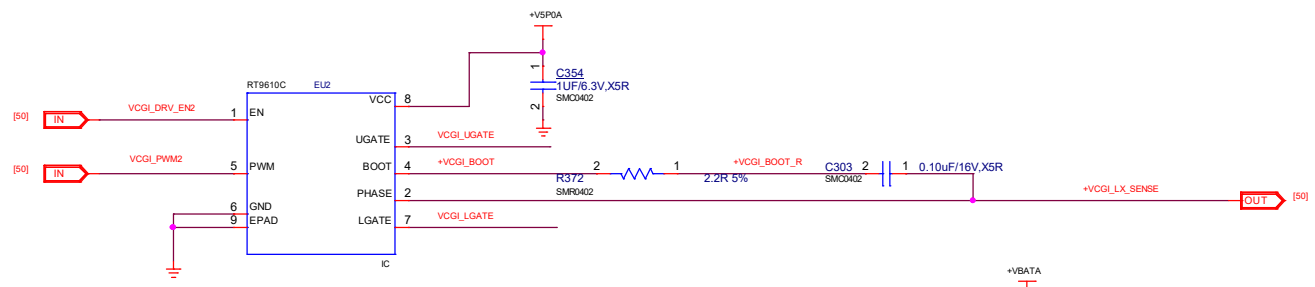
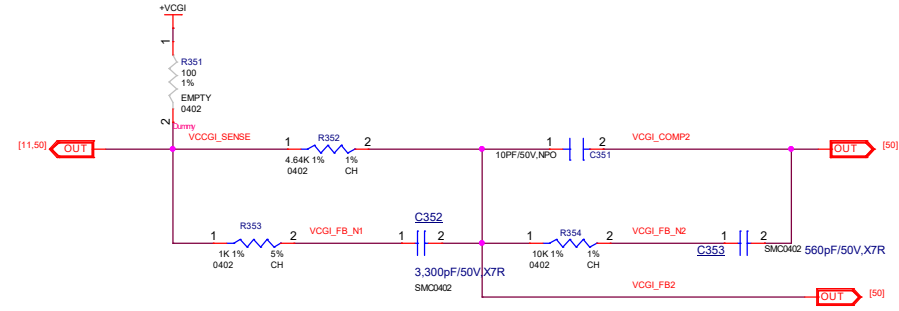
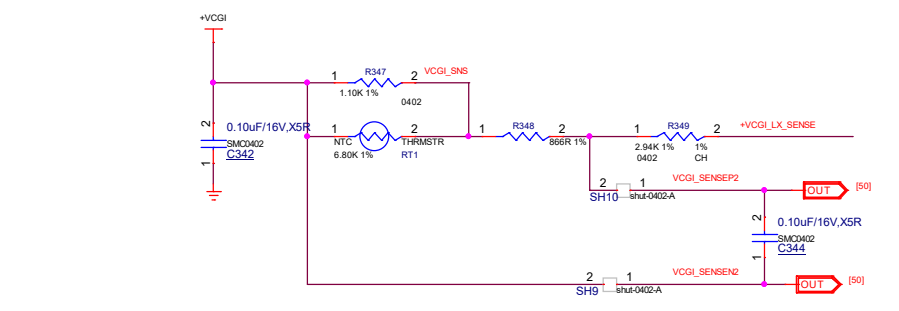
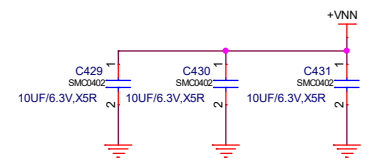
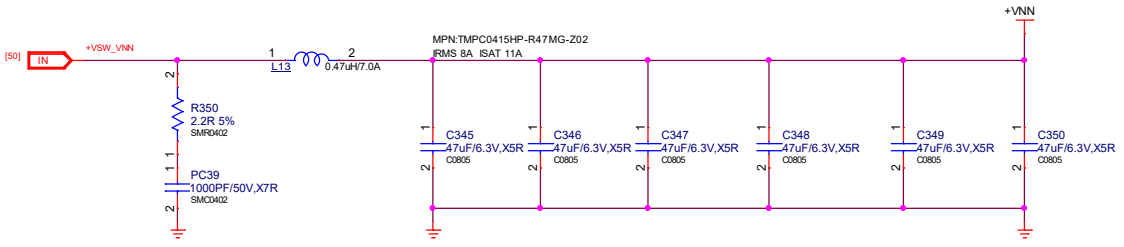
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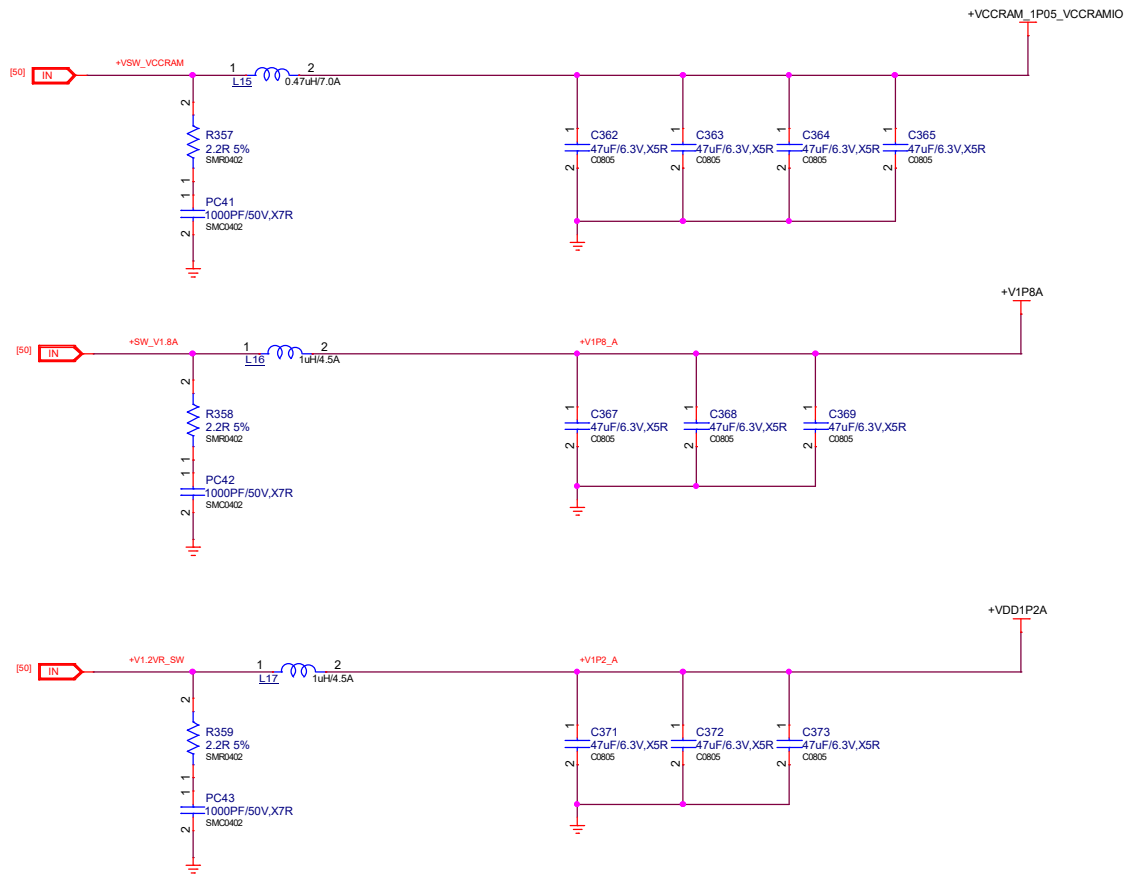
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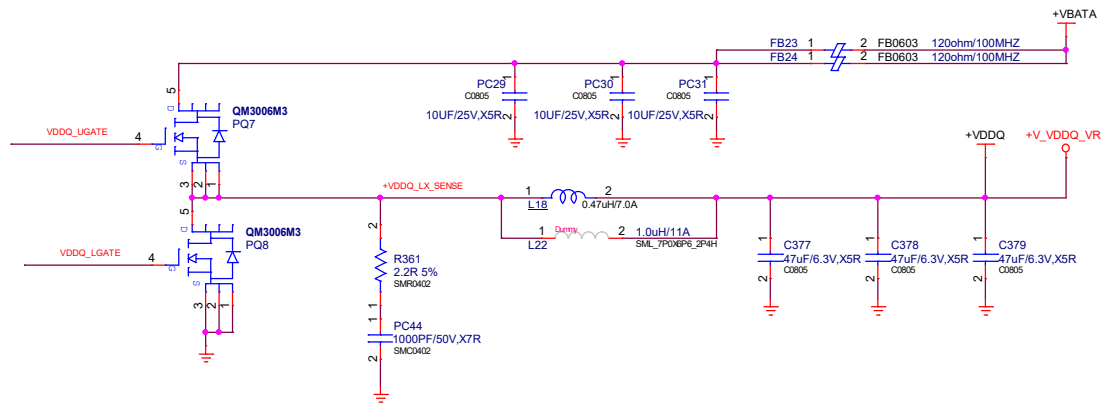
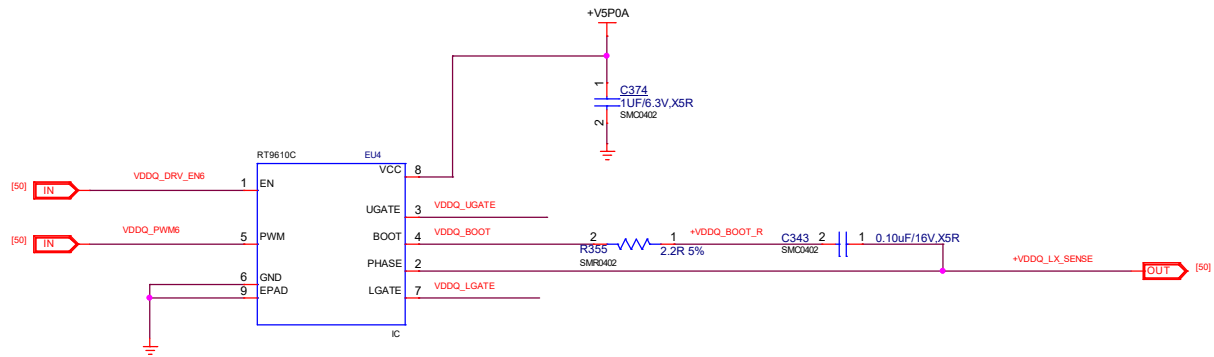
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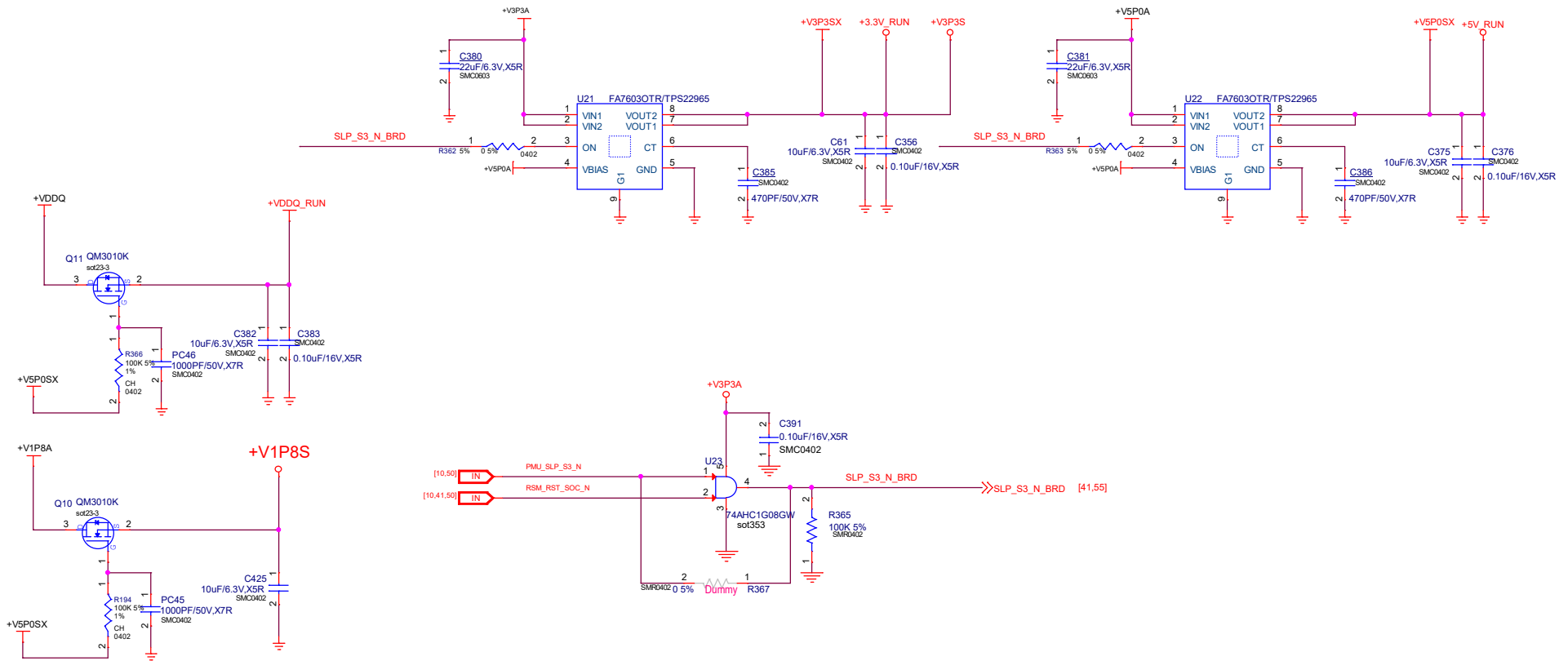
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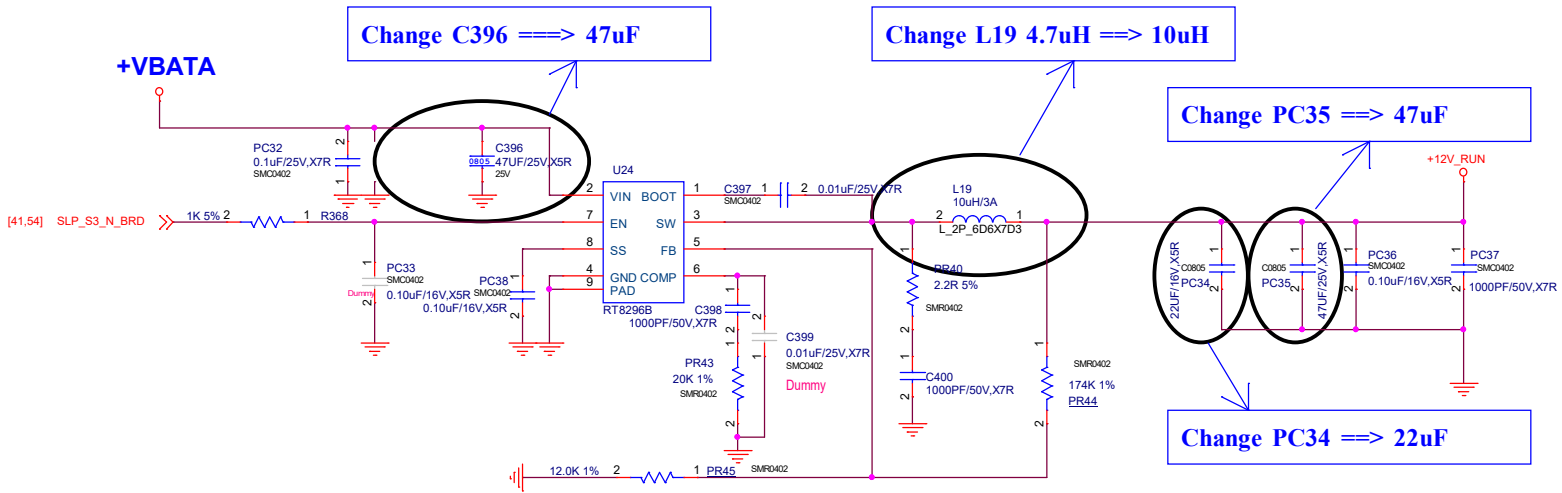
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Change C396 ==> 47uF

Change L19 4.7uH ==> 10uH

Change PC35 ==> 47uF

Change PC34 ==> 22uF

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Explain Content & Change List Revision -B

A) 12V power components changing for high speed HDD.

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L19 ==> 10uH, PC35,C396,C267,C268 ==> 47uF/25V,X5R 1206,
PC34 ==>22uF/16V, 1206

B)CLKREQ Change Pull-up Resistor (Some NVME detection issue).

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CLKREQ pull register PR3 change to 49.9K

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