

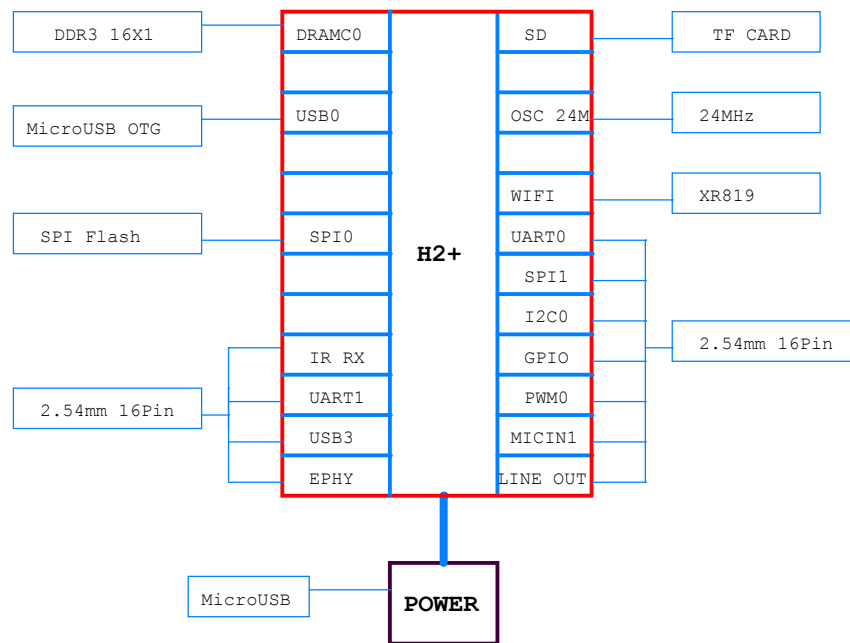
NanoPi Duo

Revision 1706 , V1.0
v1.1 1709 Romoved U5(The DC-DC of AVCC)
Added U12

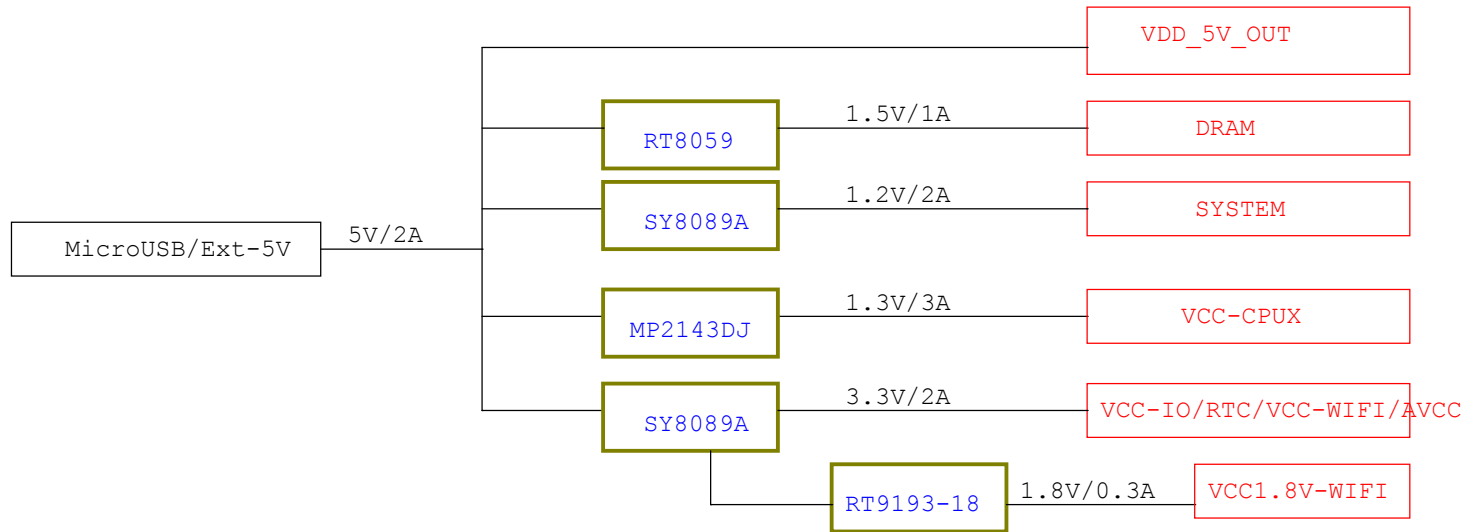


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| NanoPi Duo | | |
| Size A3 | Document Number Title | Rev v1.1 1709 |
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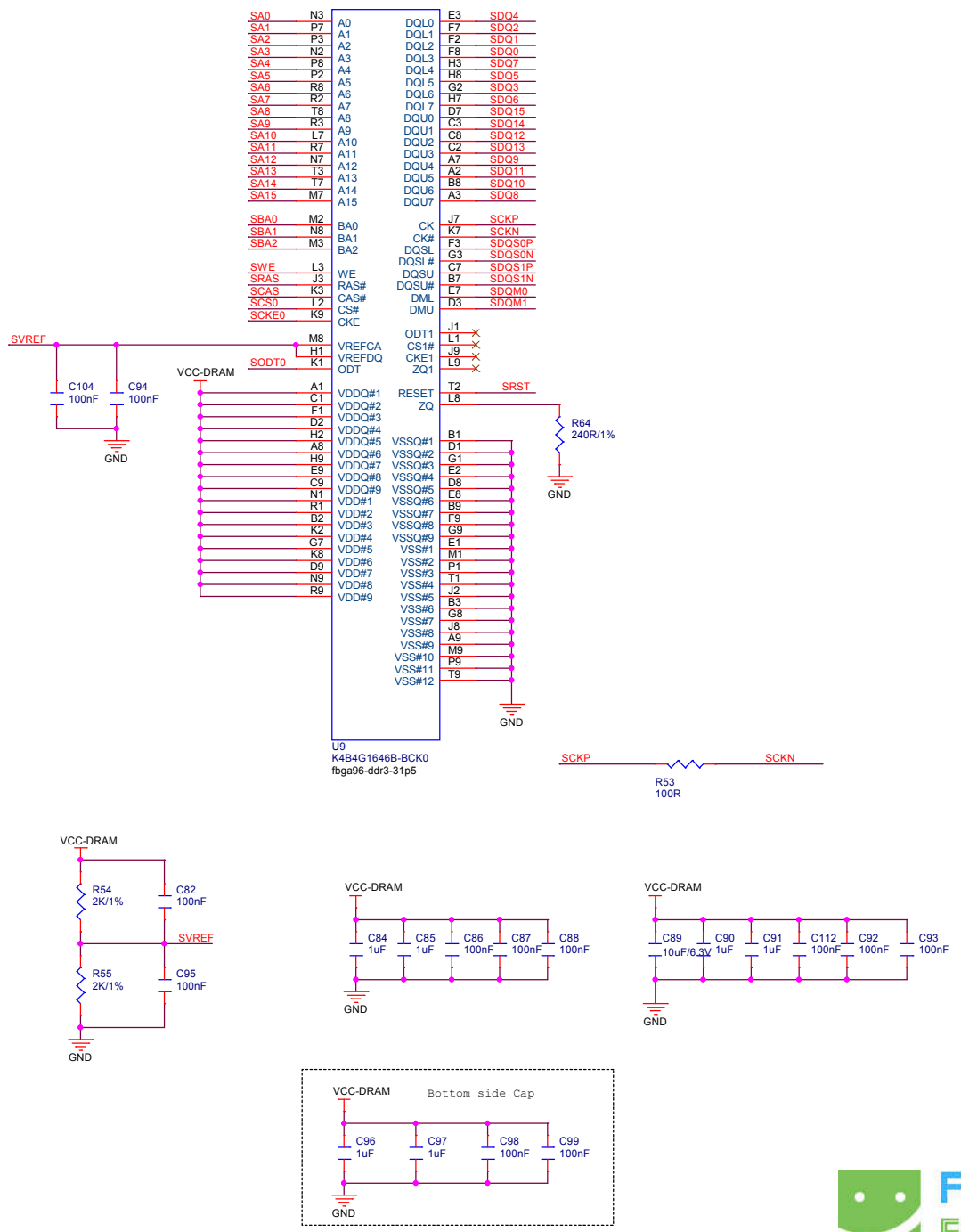
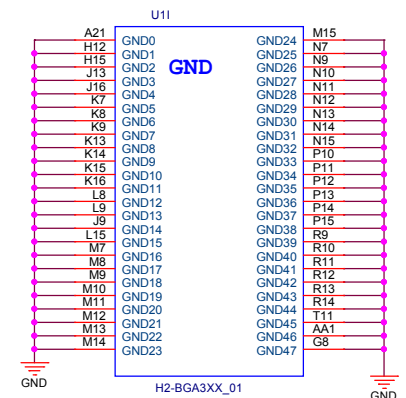
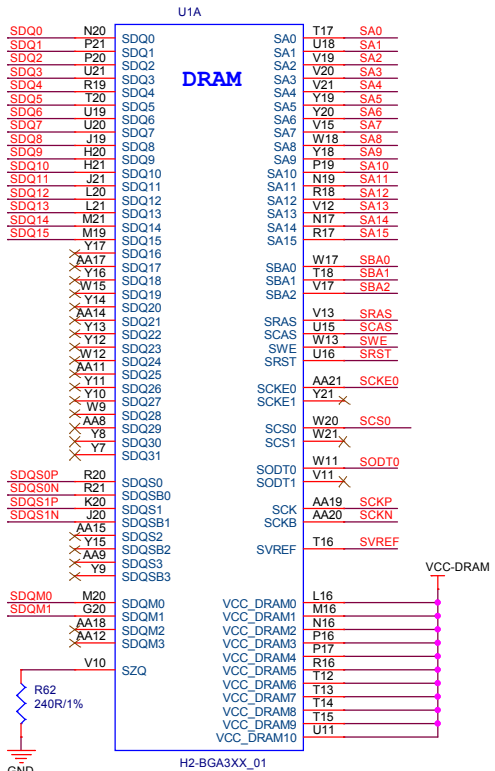
BLOCK



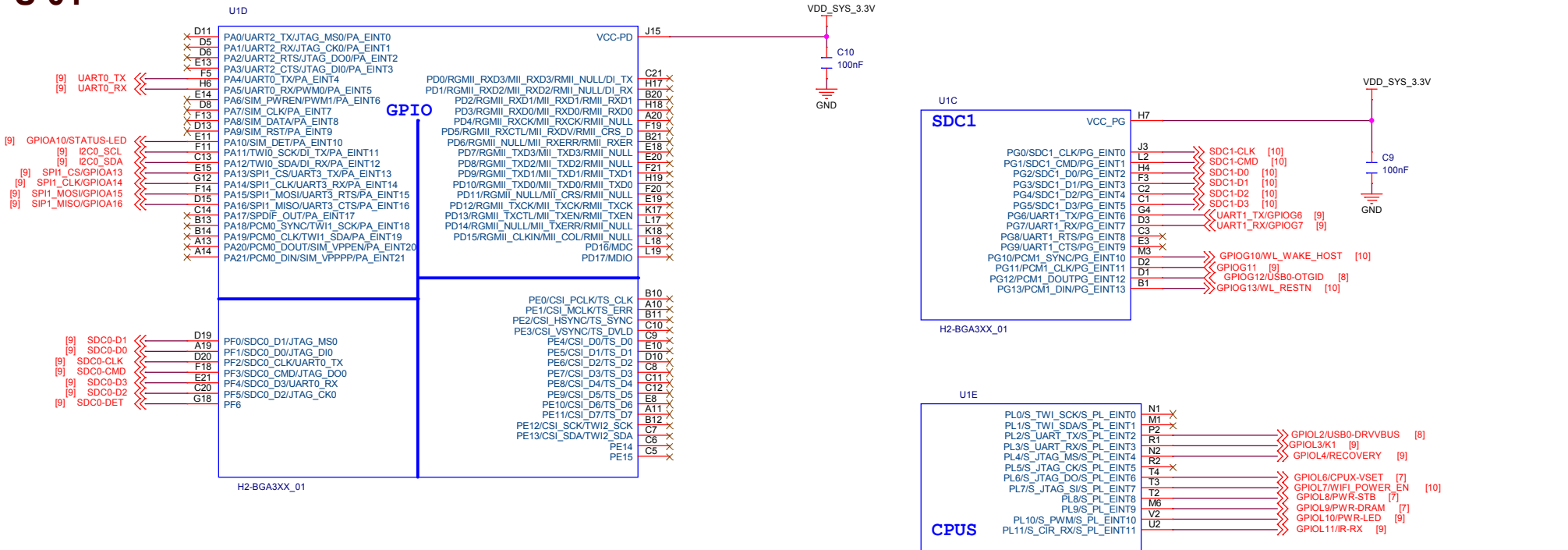
POWER TREE



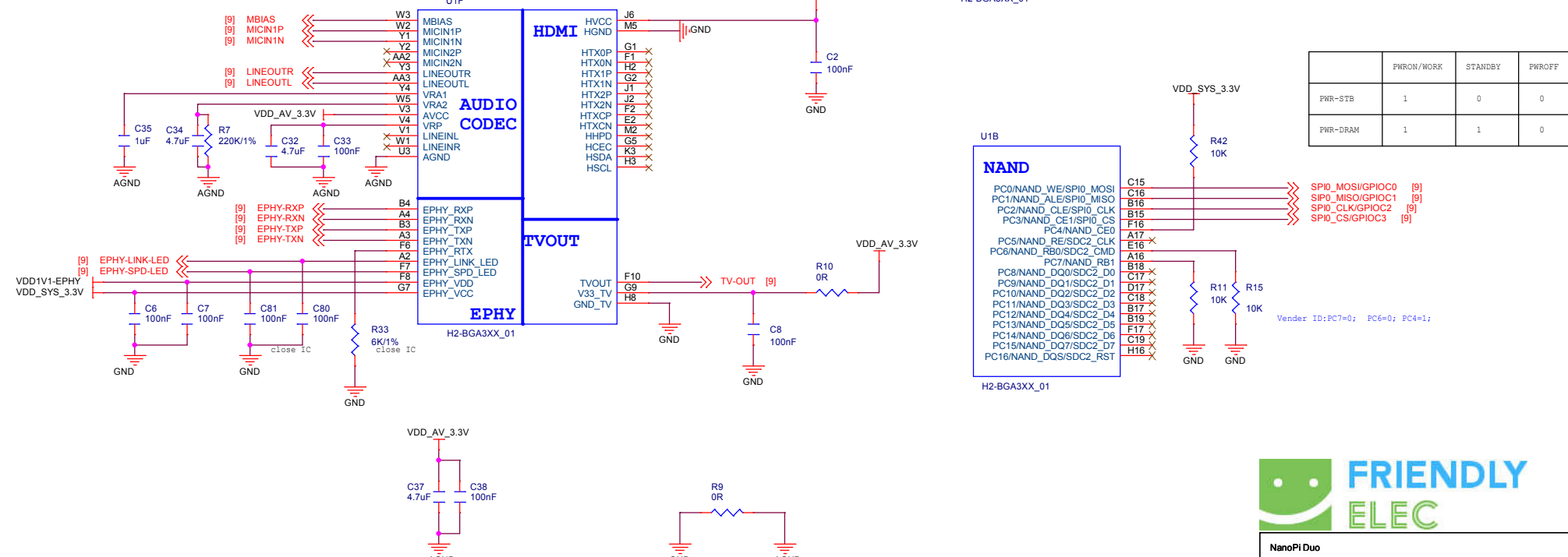
DDR3 16x1



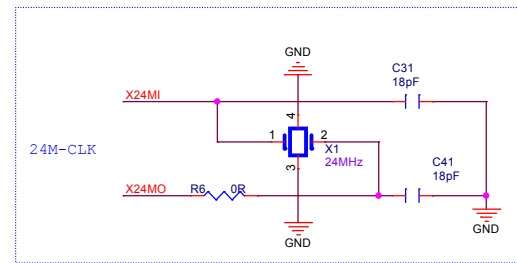
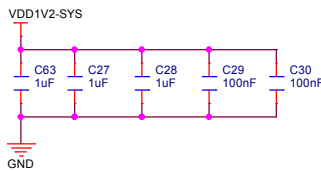
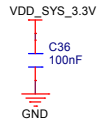
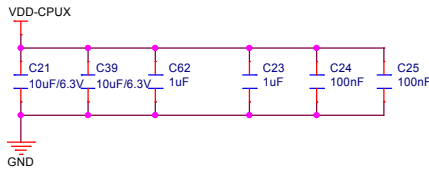
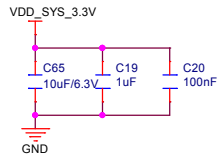
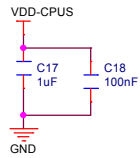
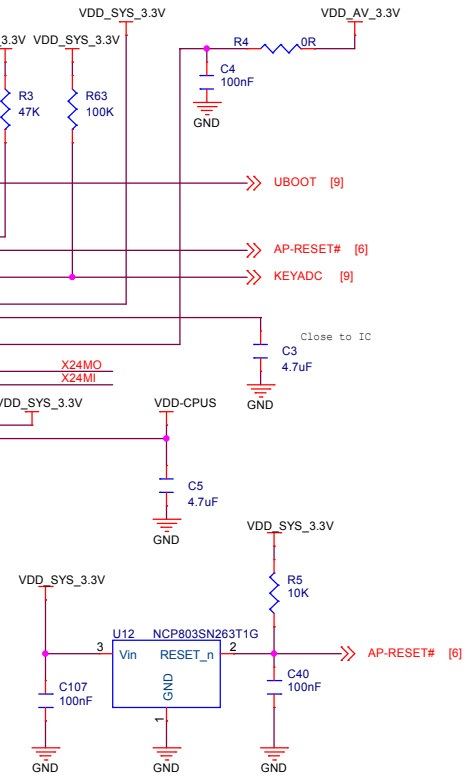
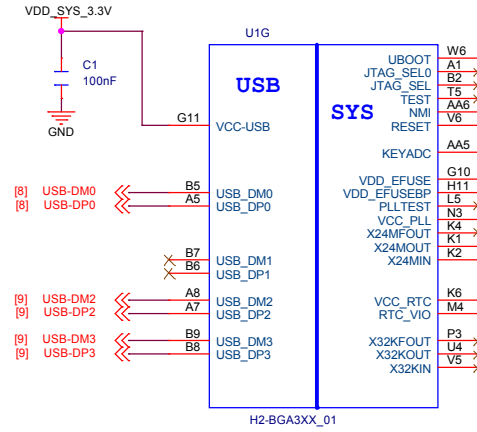
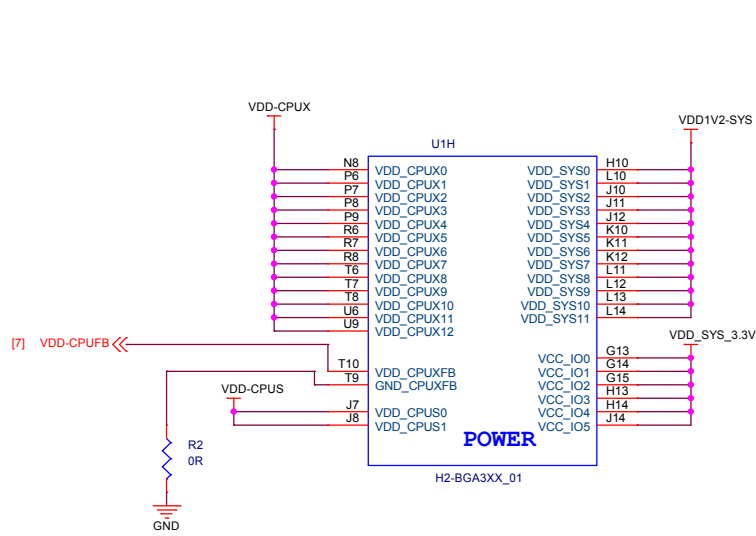
CPU 01



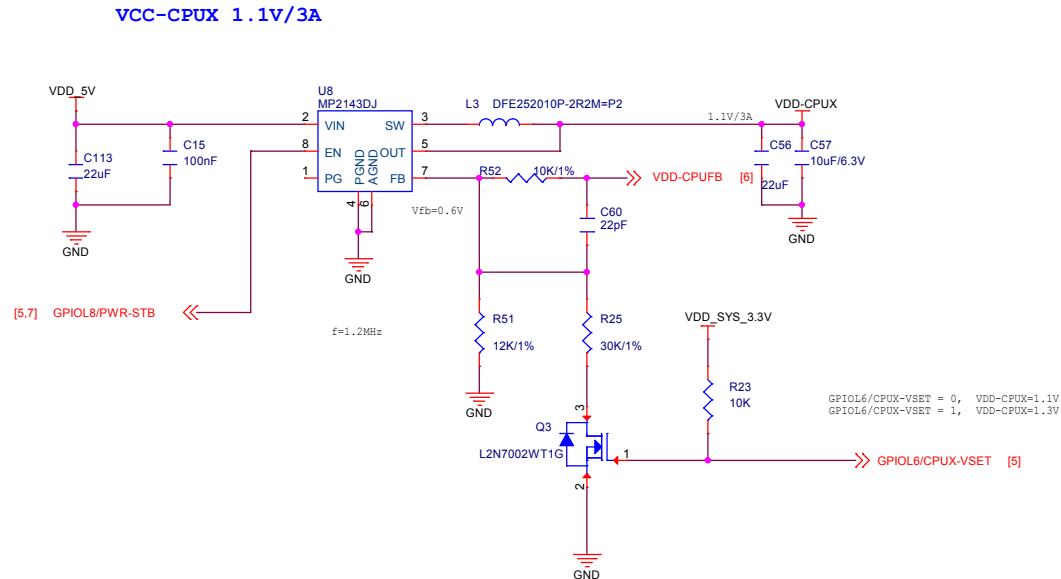
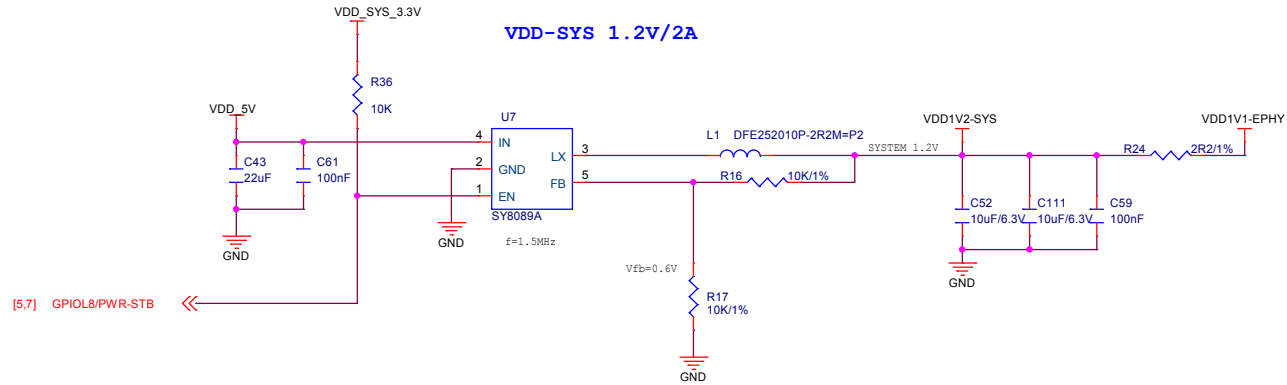
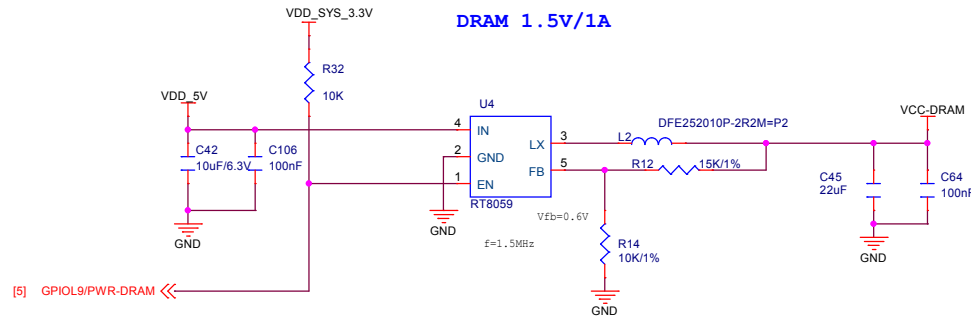
| | PWRON/WORK | STANDBY | PWROFF |
|----------|------------|---------|--------|
| PWR-STB | 1 | 0 | 0 |
| PWR-DRAM | 1 | 1 | 0 |



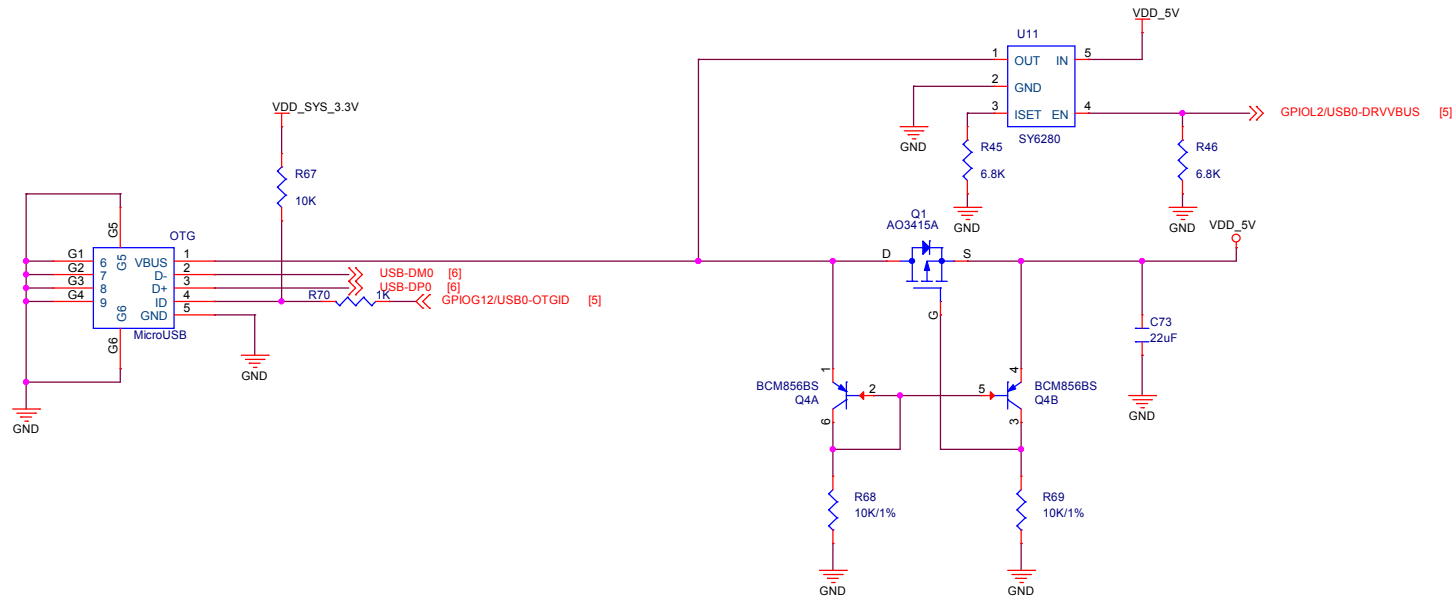
CPU 02



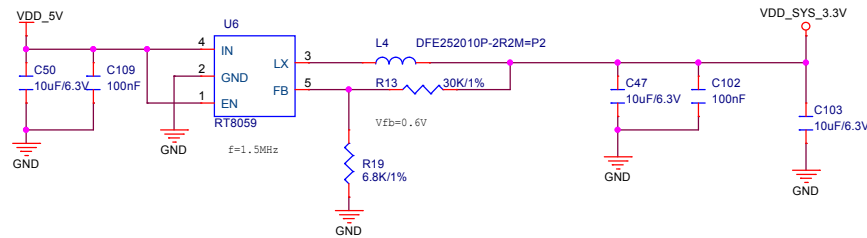
POWER 01



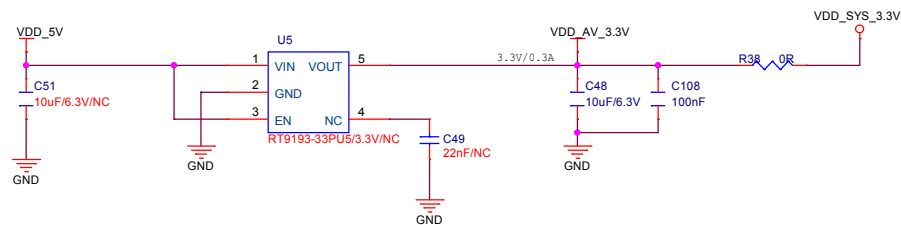
POWER 02



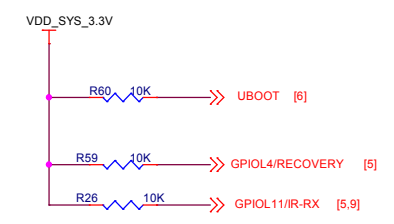
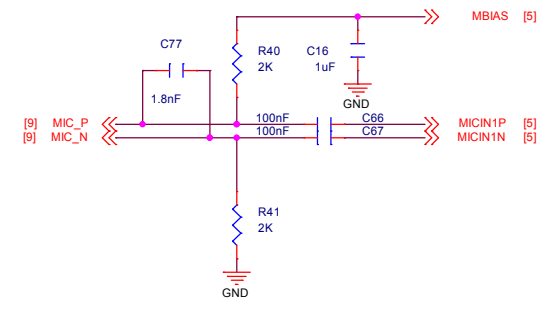
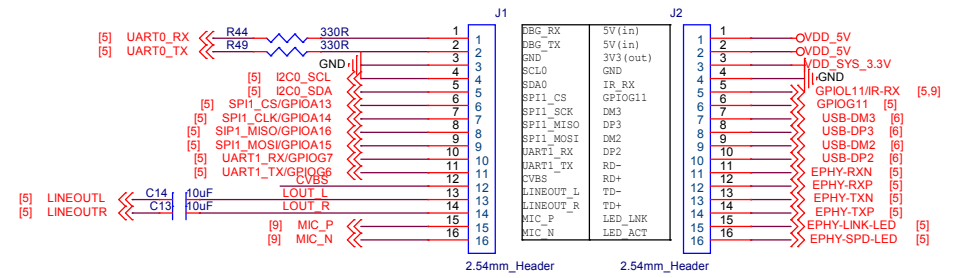
VCC-IO 3.3V/1A



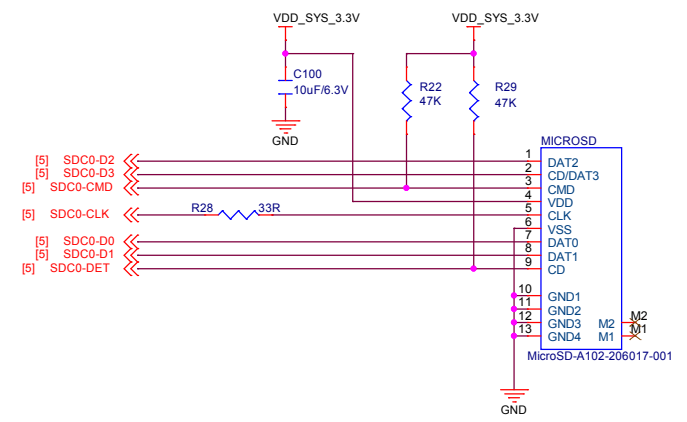
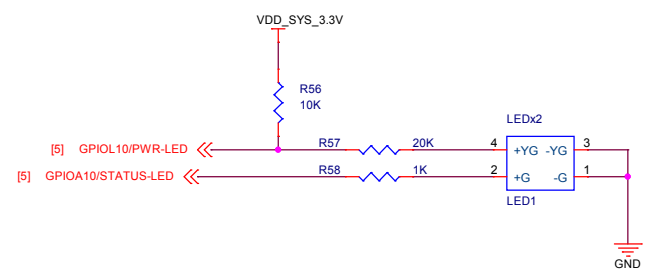
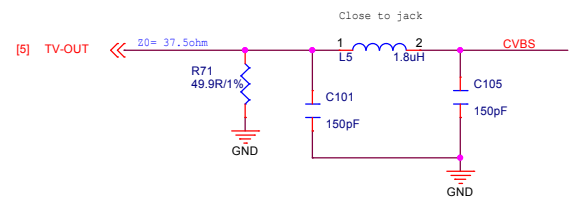
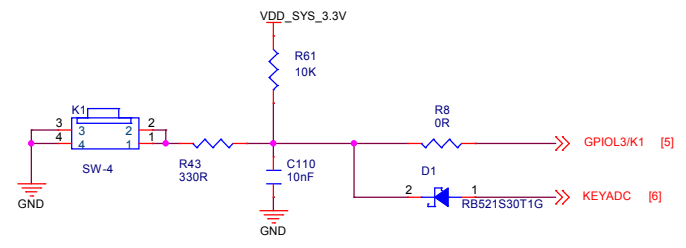
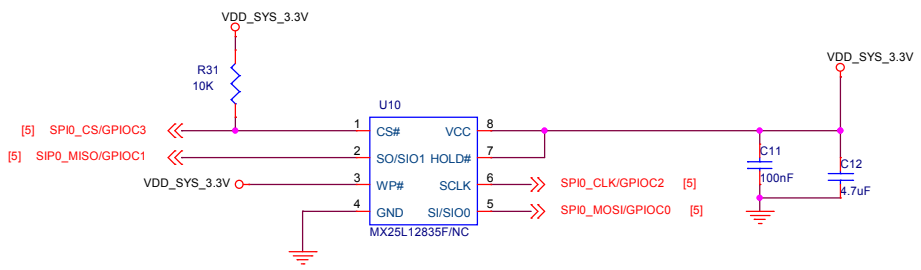
VCC-AVCC 3.3V/300mA



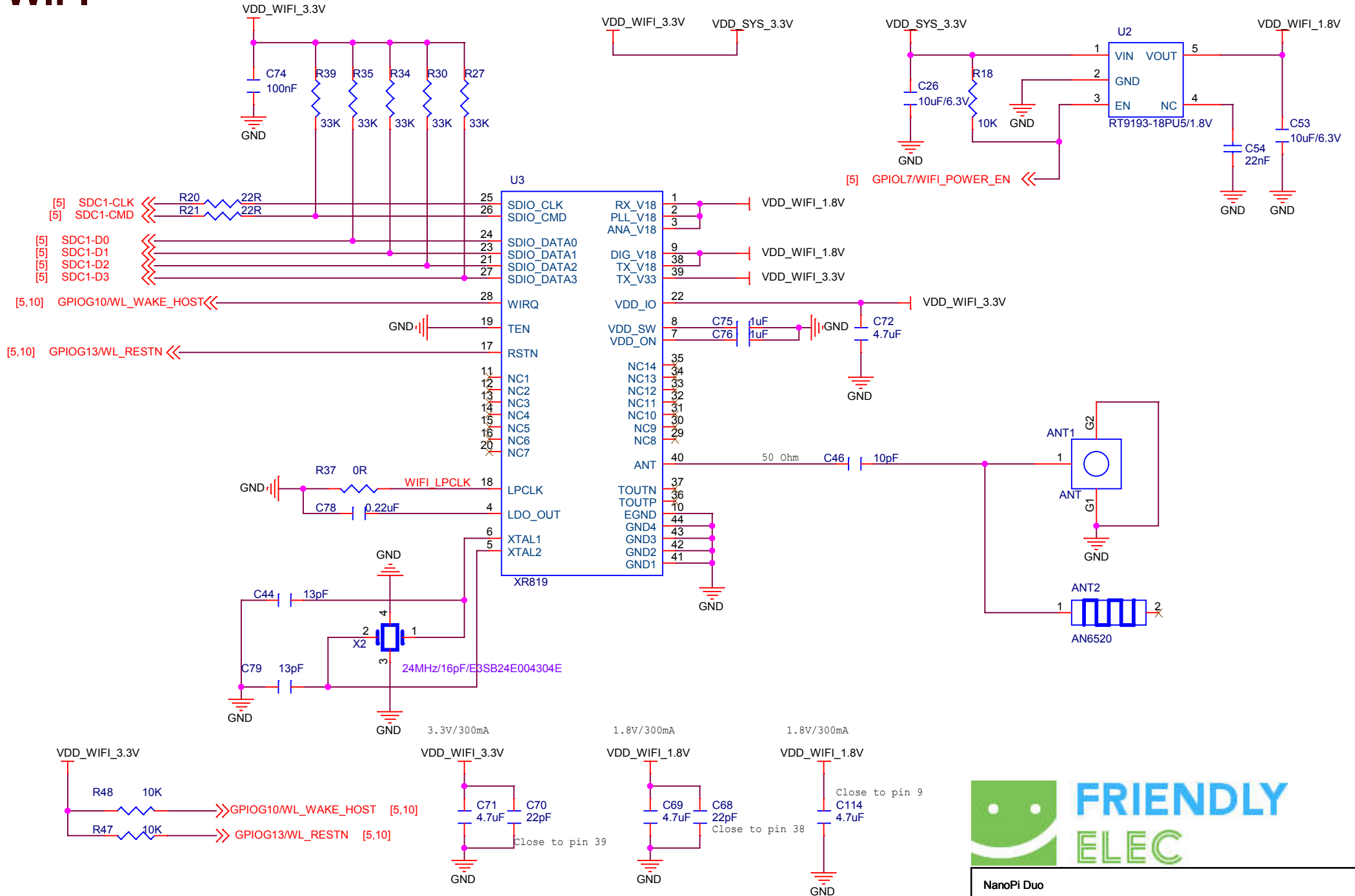
GPIO,LEDs,TF Card



SPI Flash



WIFI



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| NanoPi Duo | | |
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