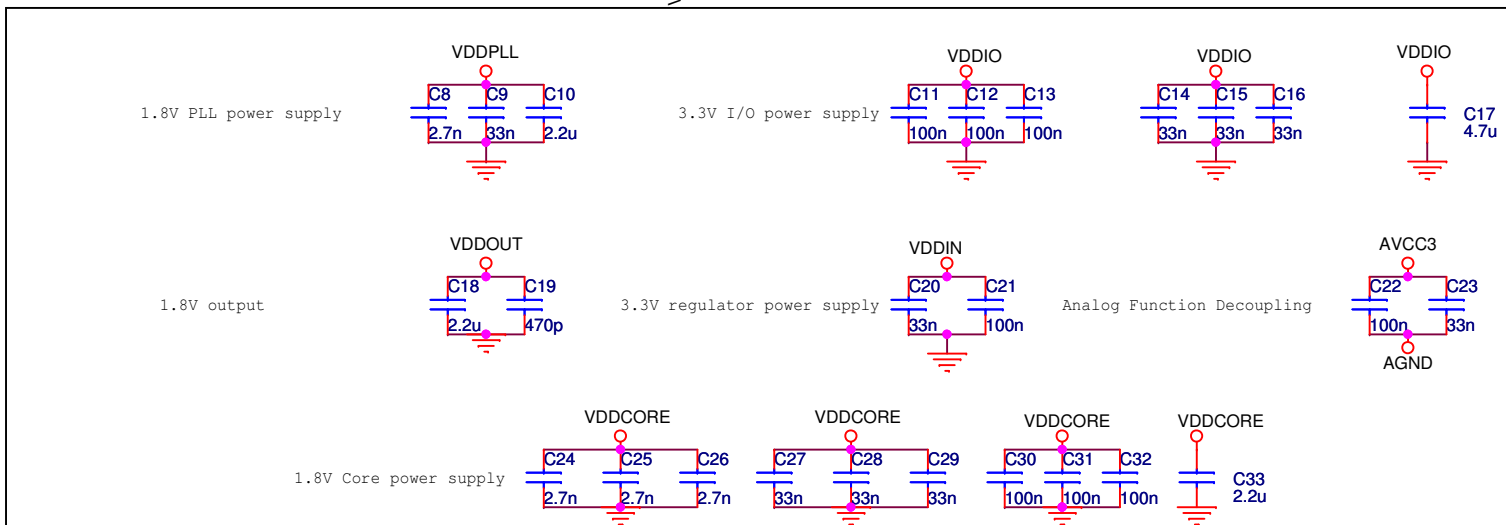
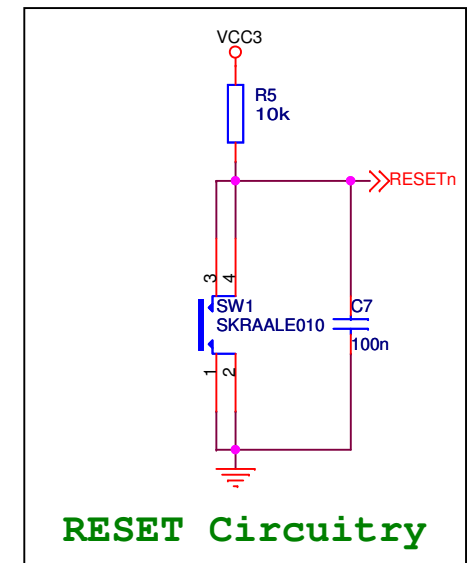
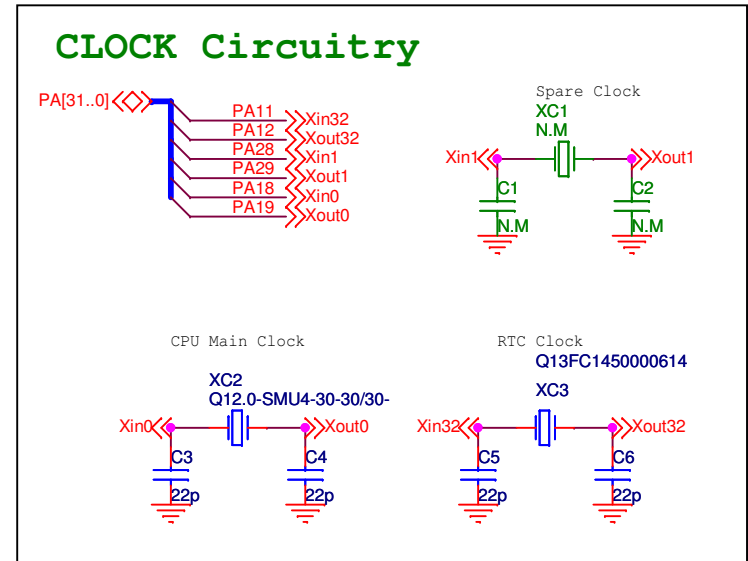
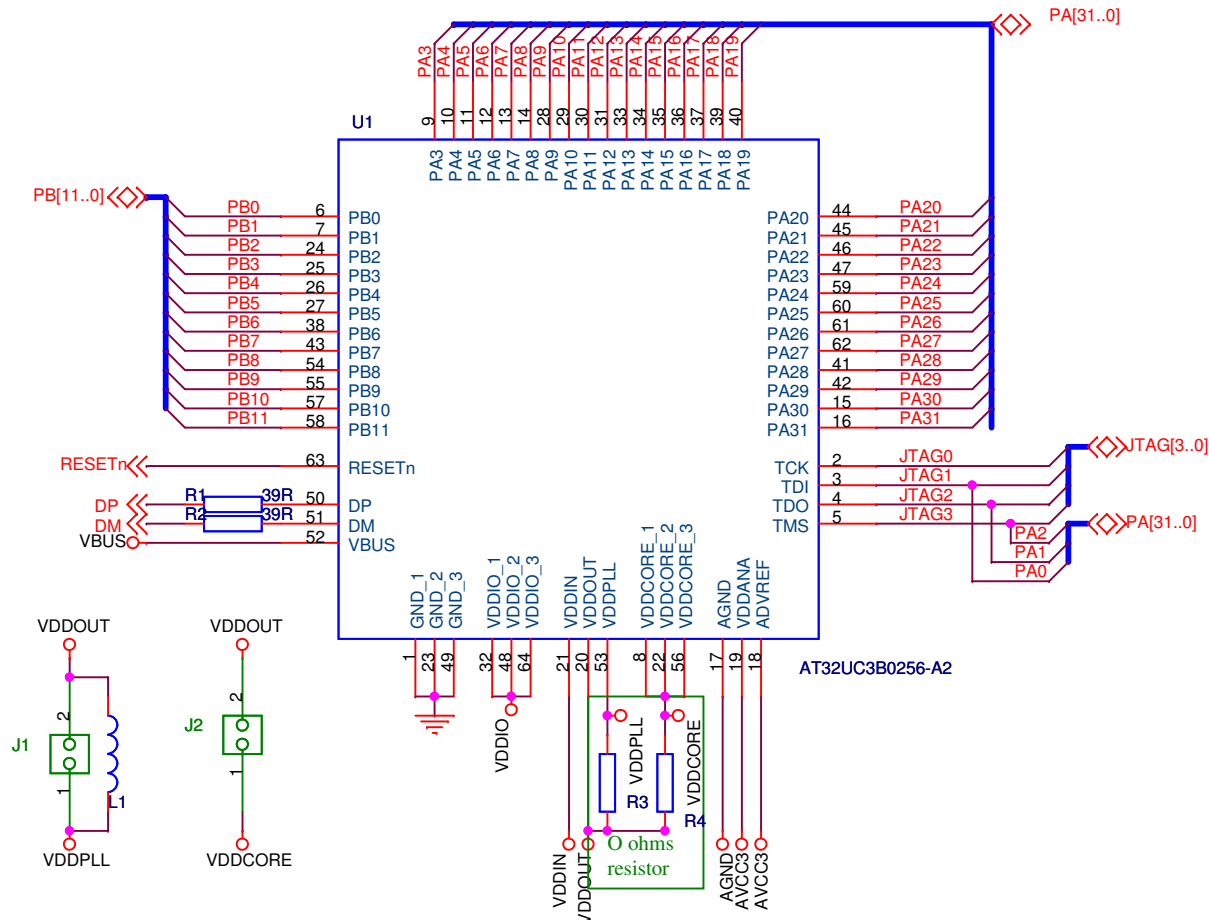


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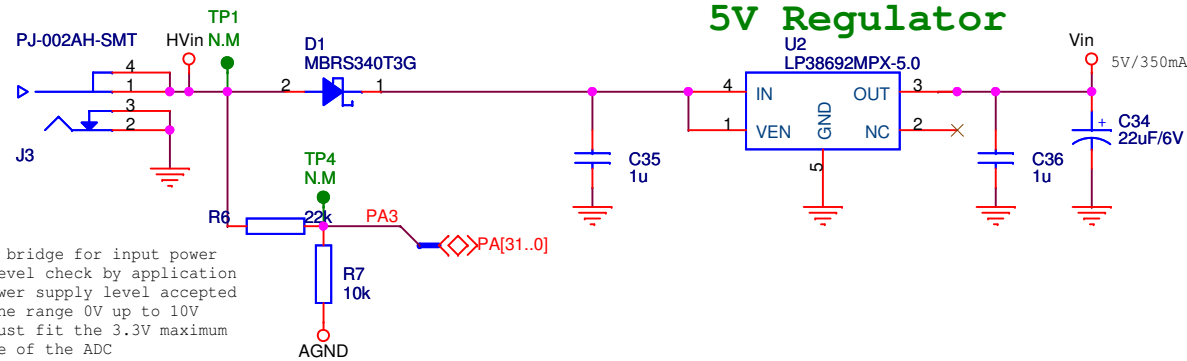
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Processor

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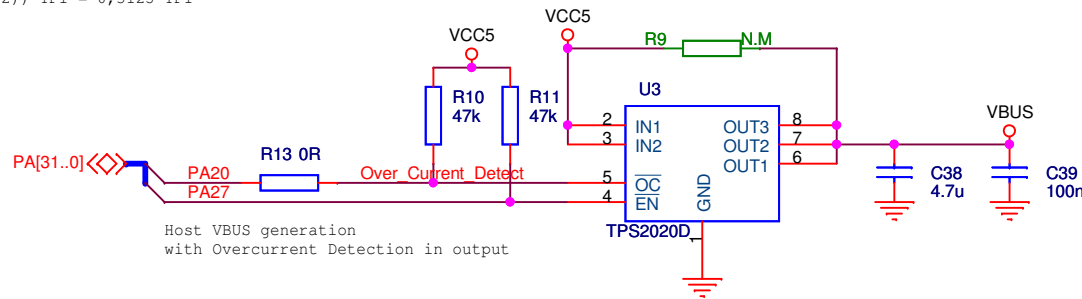
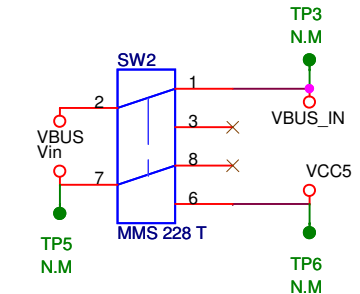
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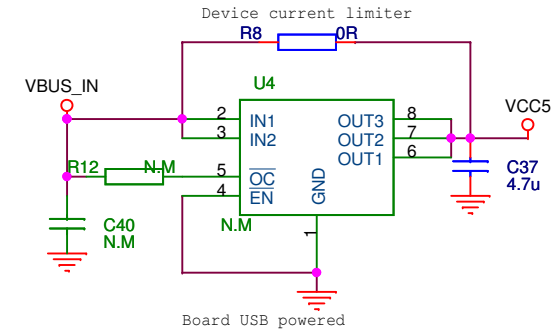


Resistor bridge for input power supply level check by application
 Input power supply level accepted are in the range 0V up to 10V
 Levels must fit the 3.3V maximum reference of the ADC

TP4 = (10/(10+22)) TP1 = 0,3125 TP1

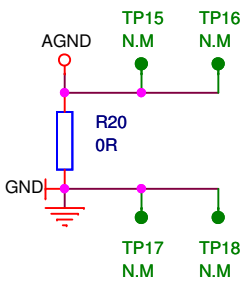
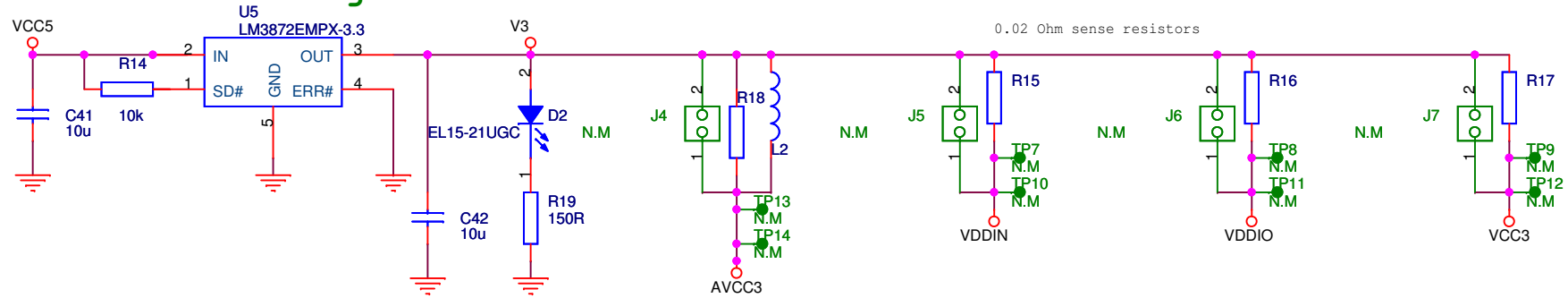


Host VBUS generation with Overcurrent Detection in output



Board USB powered

3.3V Regulator



AVCC3 intends to provide a clean 3.3V power supply for analog blocks
 VDDIO intends to provide a clean 3.3V power supply for MCU buffers
 VDDIN intends to provide a clean 3.3V power supply for MCU internal regulator
 VCC3 intends to provide a clean 3.3V power supply for peripherals

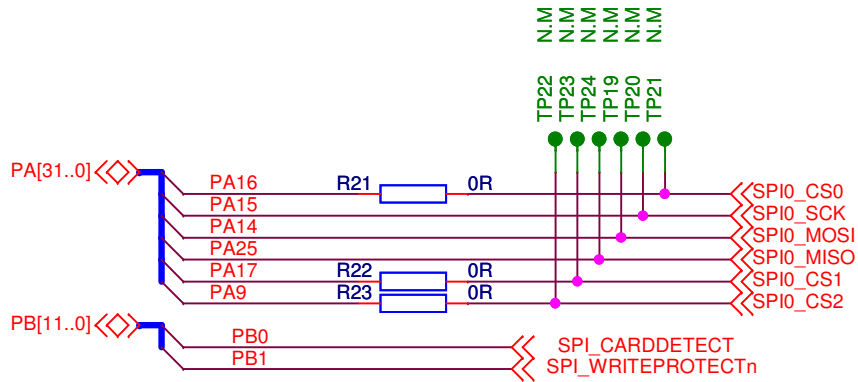
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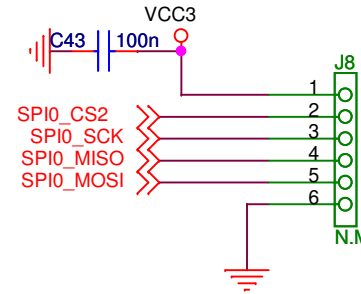


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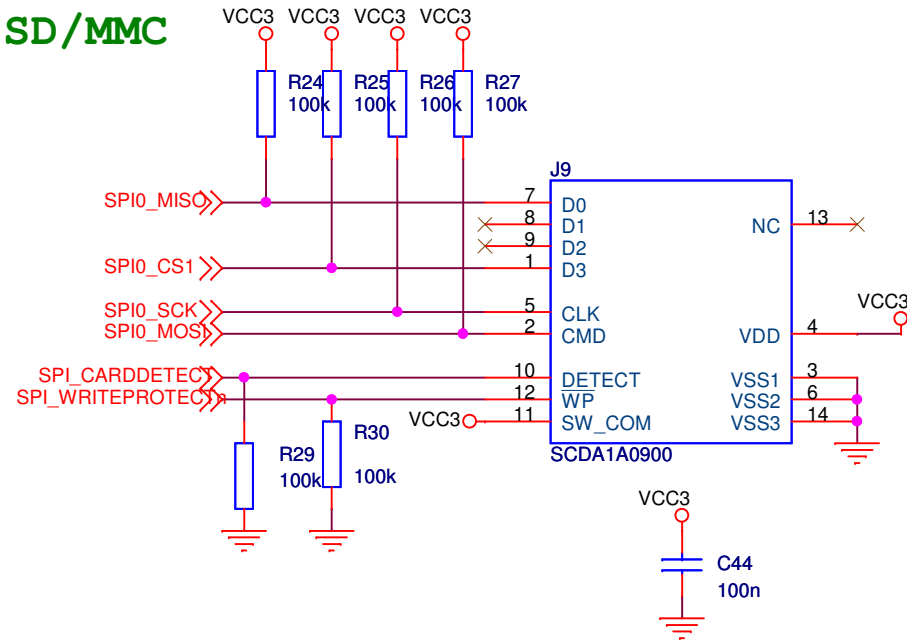
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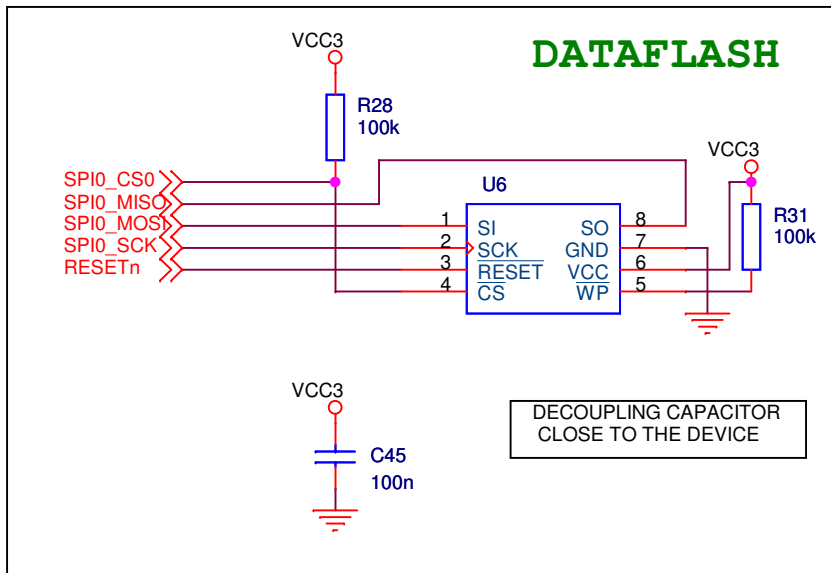
SPI



SD/MMC



DATAFLASH



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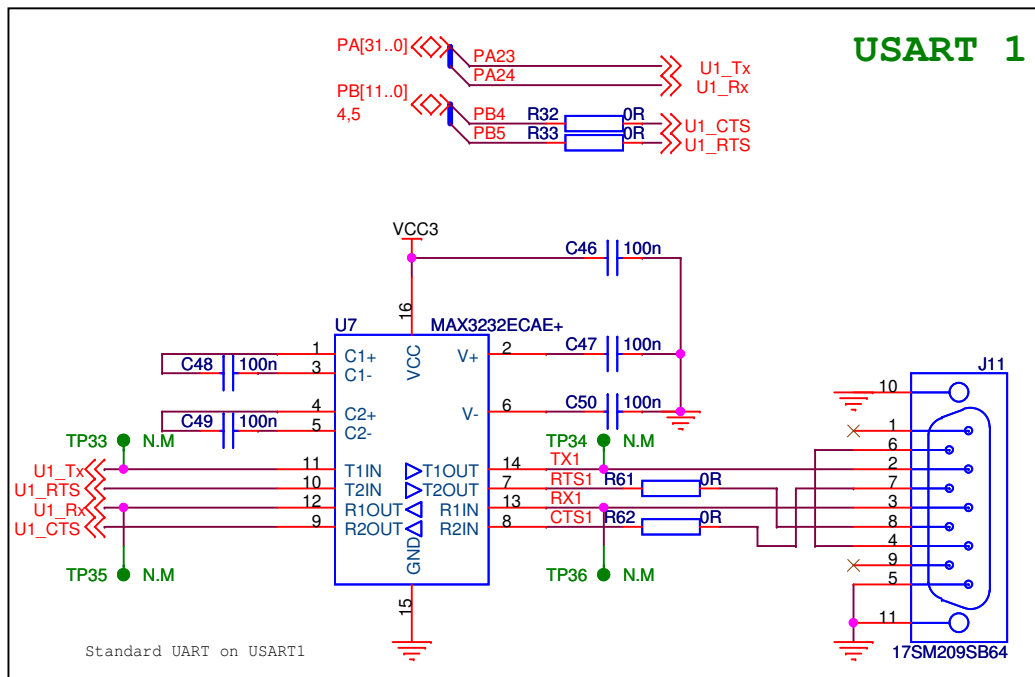
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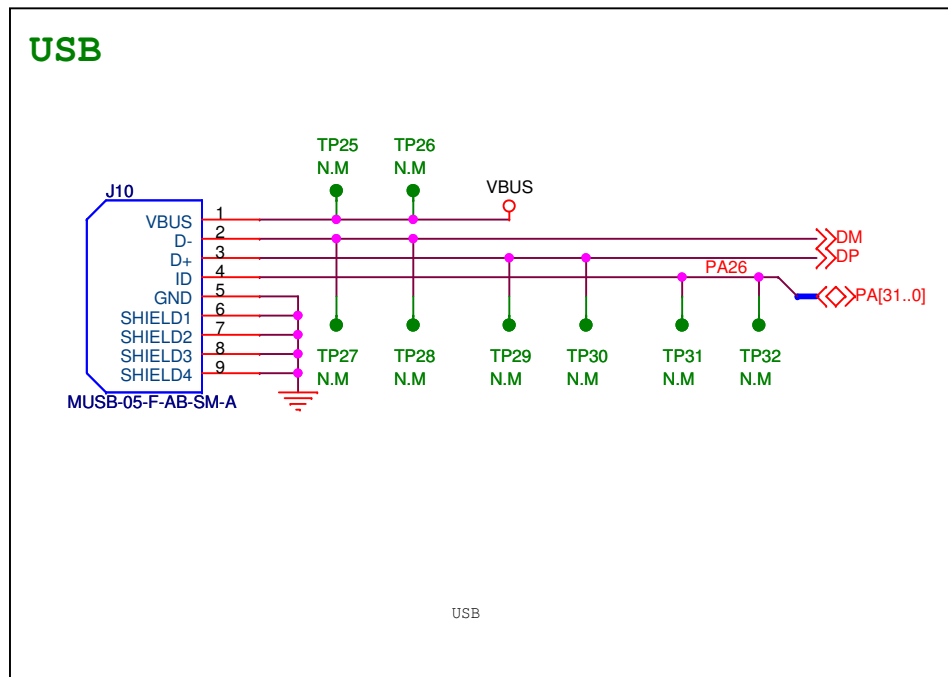
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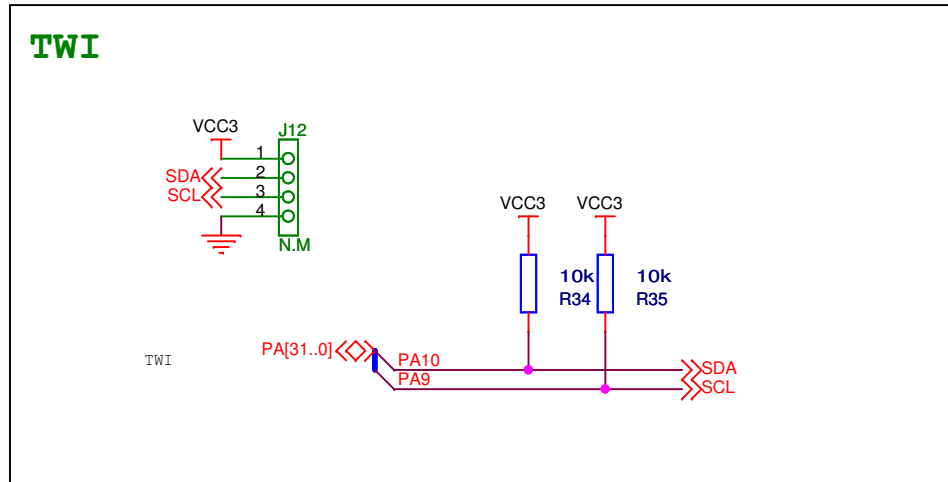
USART 1



USB



TWI

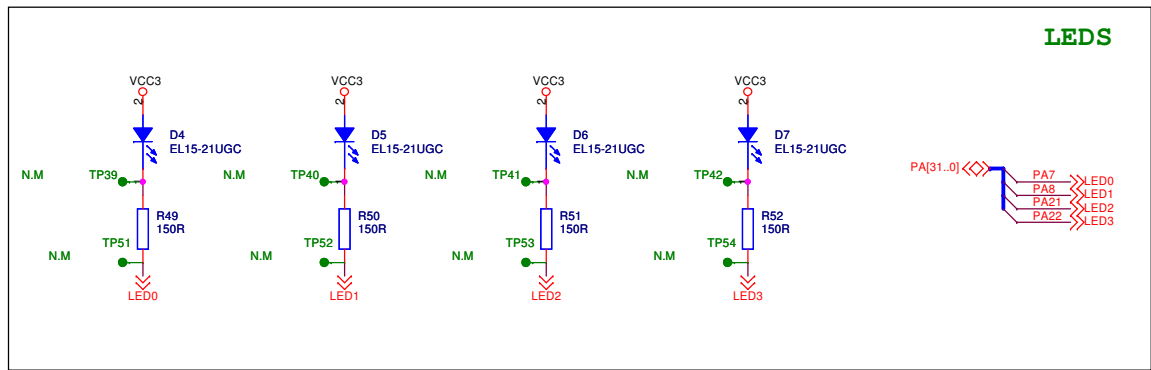
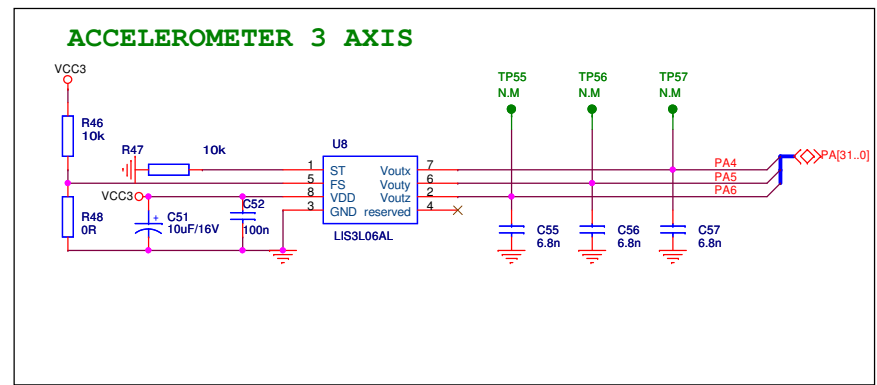
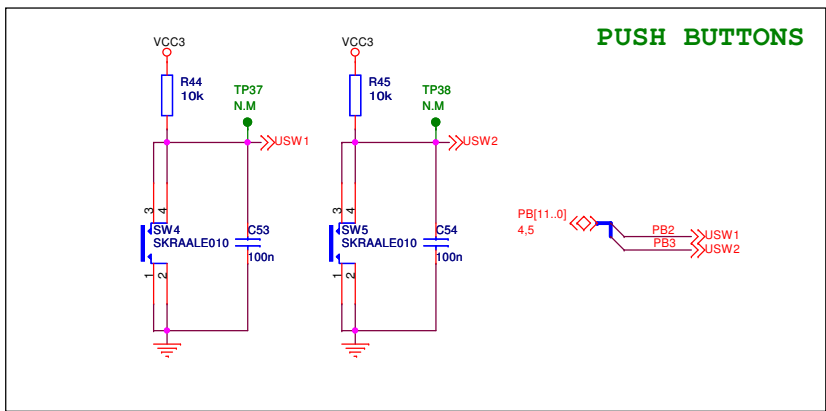
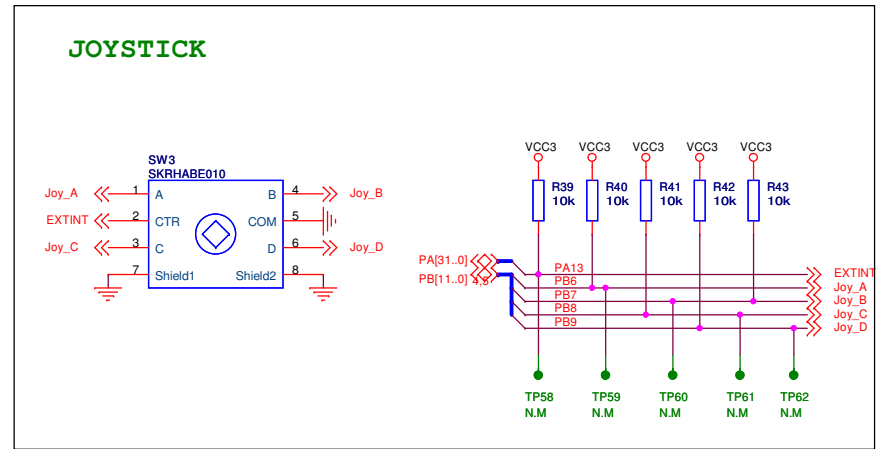
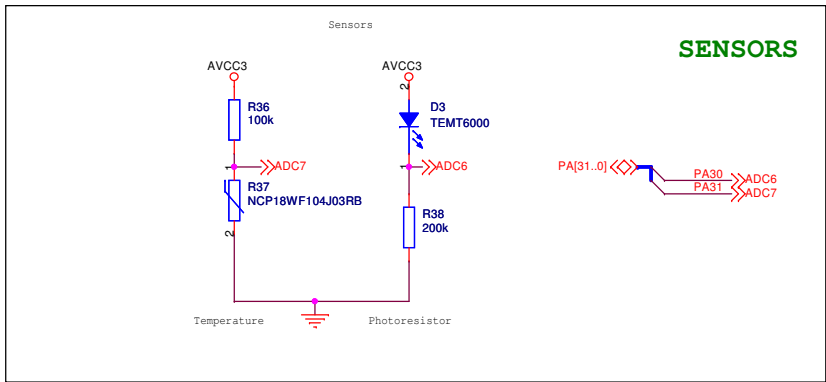


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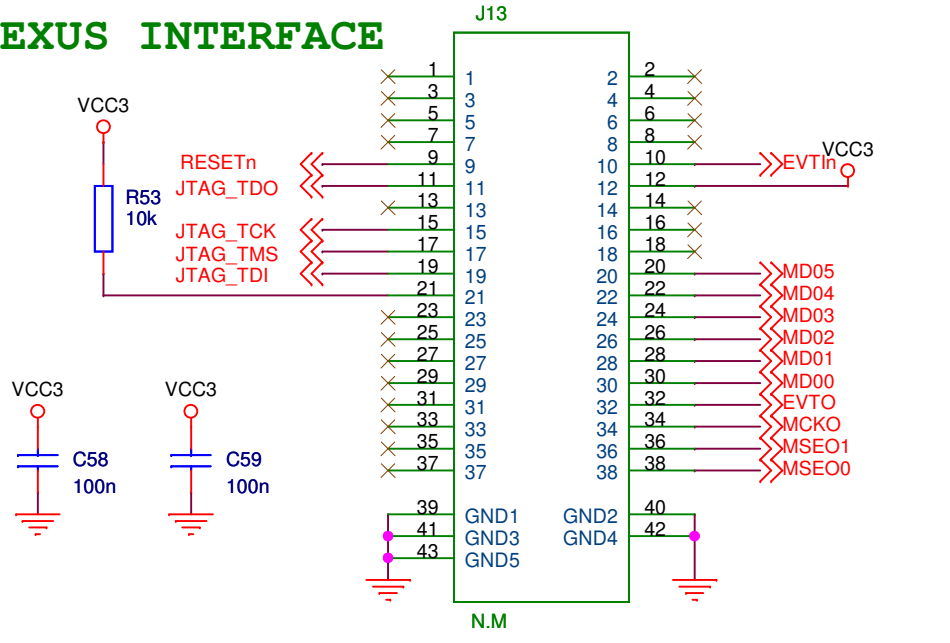


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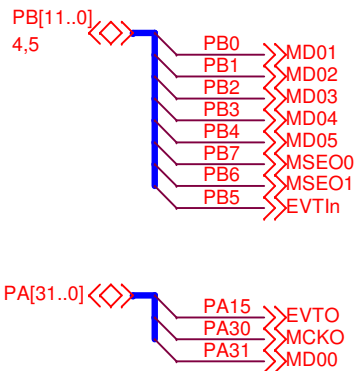
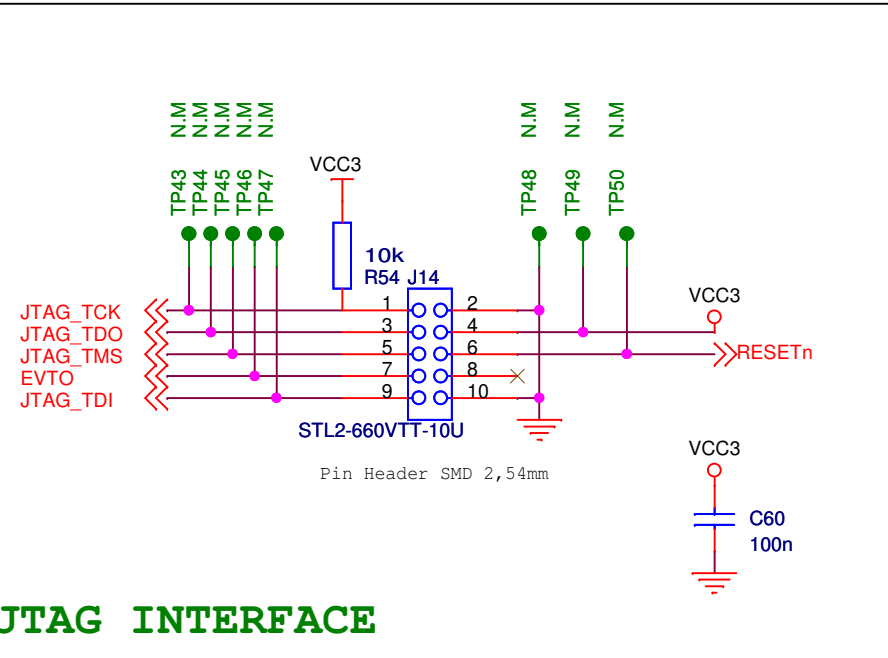


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NEXUS INTERFACE



JTAG INTERFACE



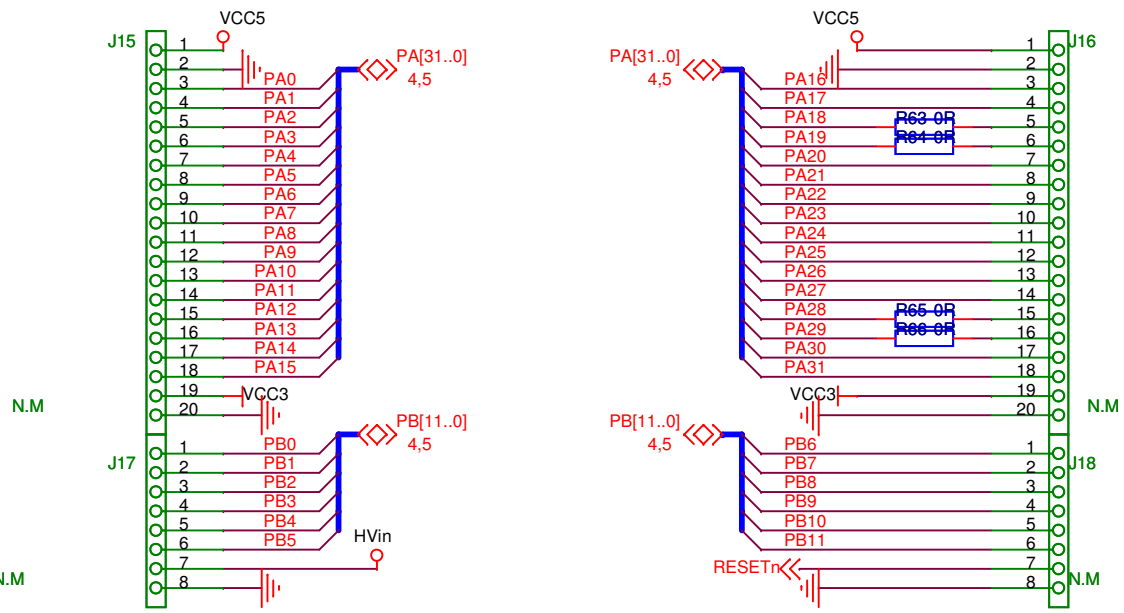
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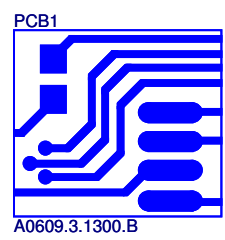
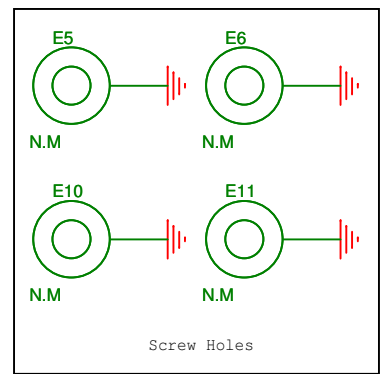
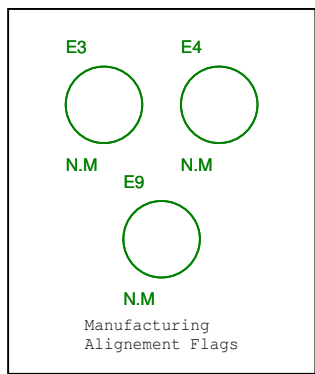
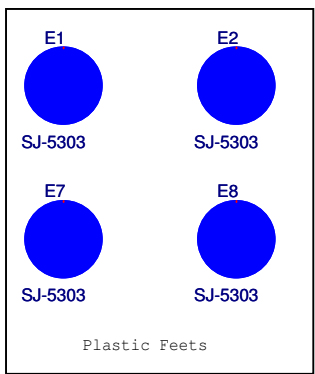
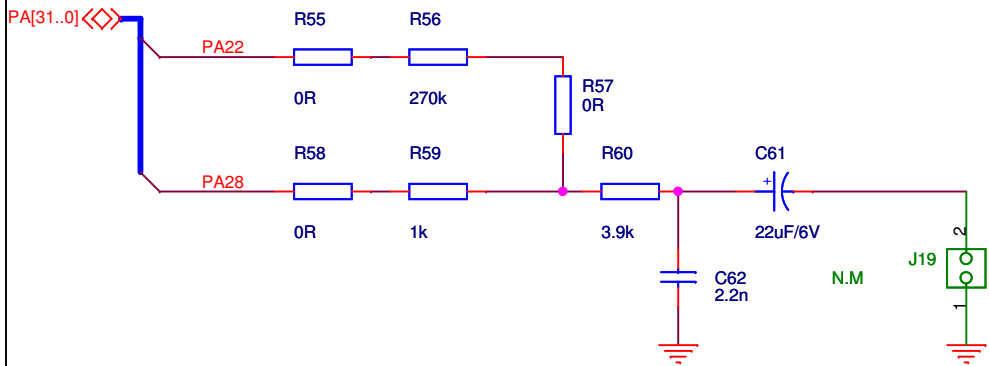


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Audio filter



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