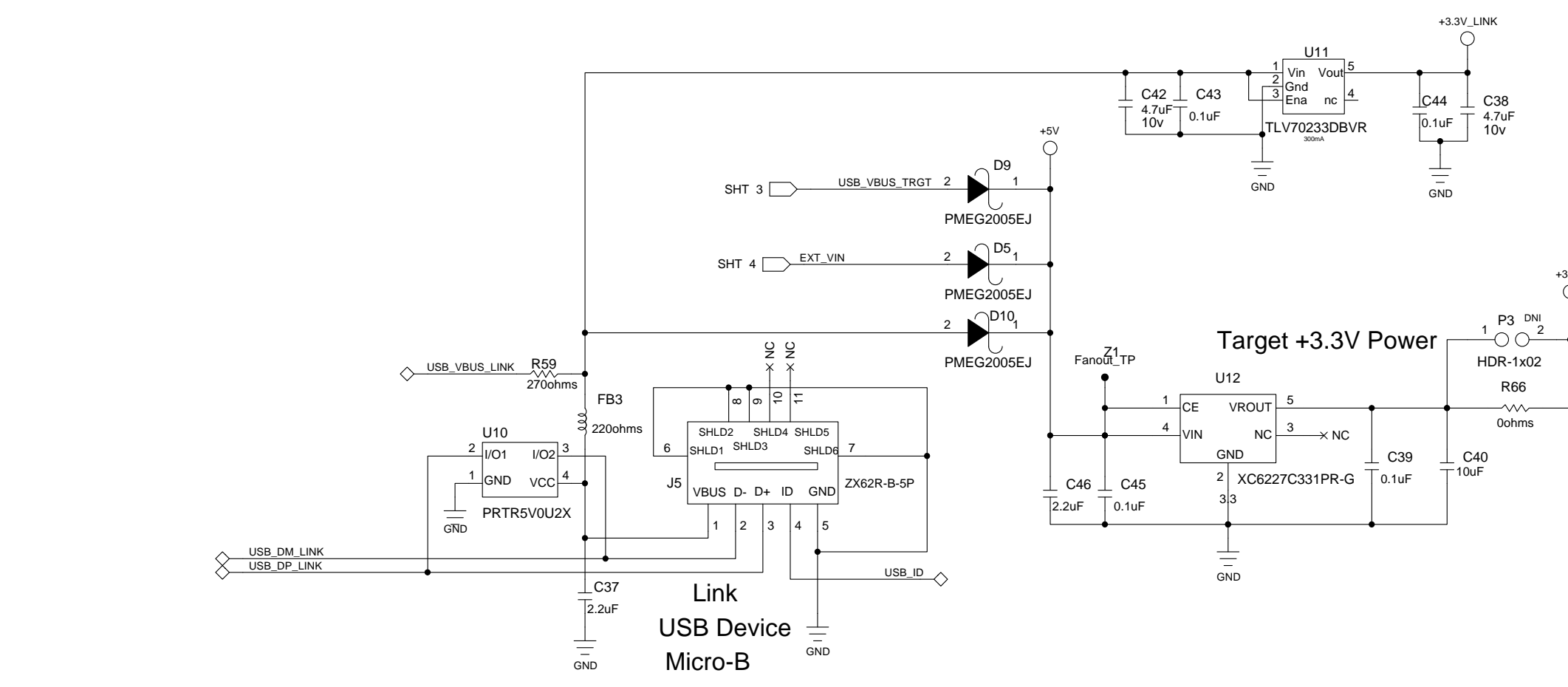
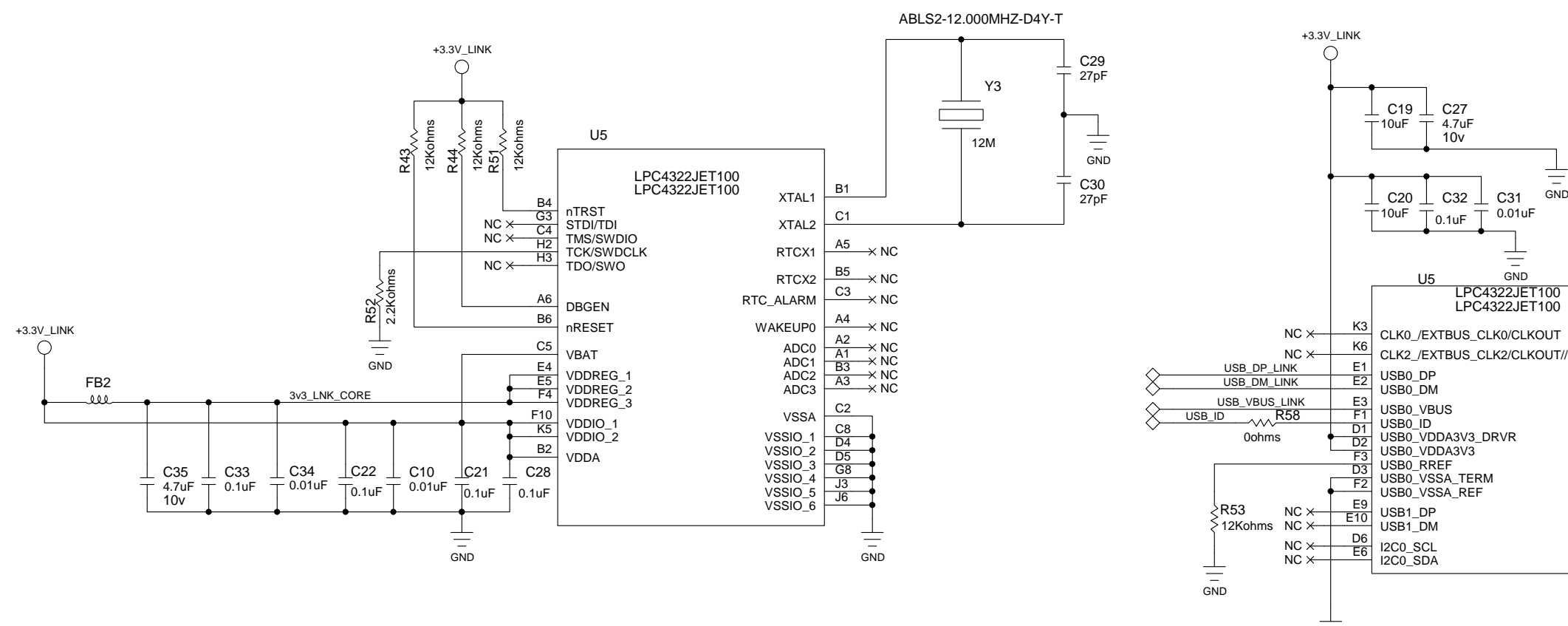
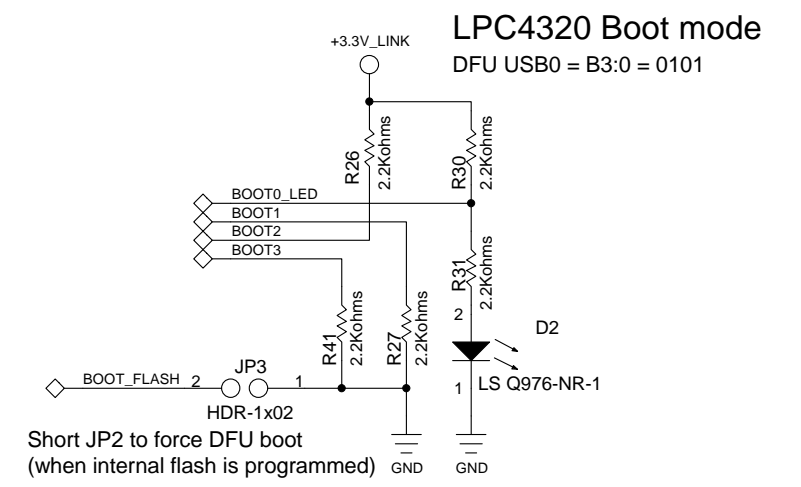
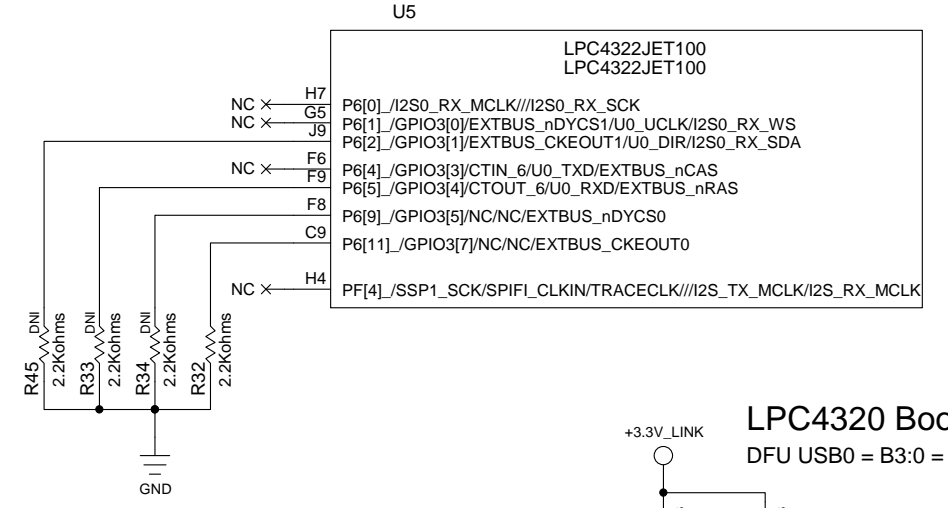
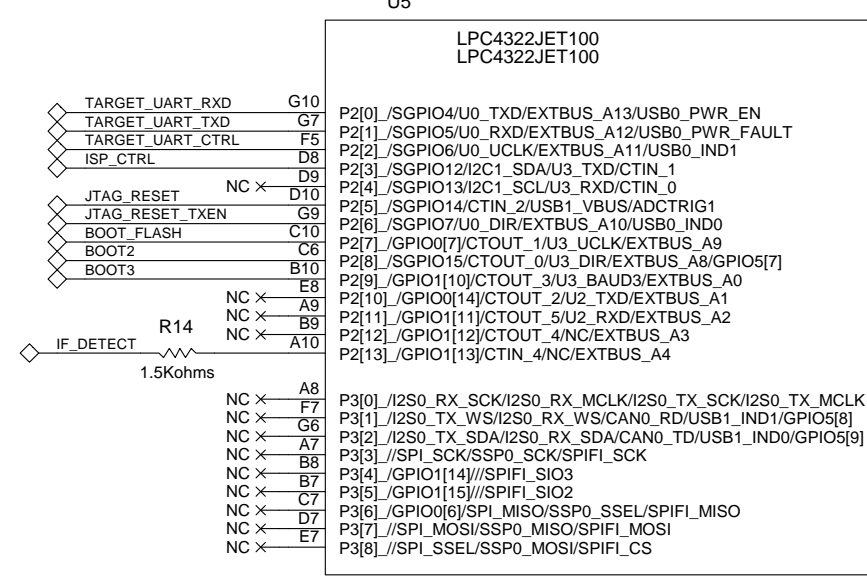
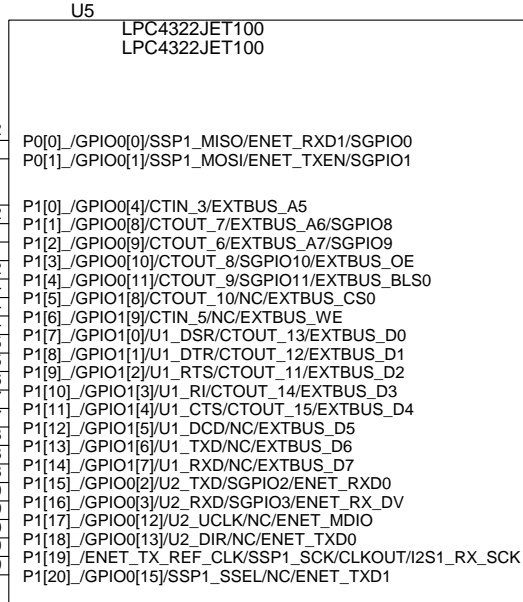


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
B	- Change Y1 crystal to 12.5pF load - Change Y1 load caps to 18pF - Add new SWD/JTAG I/F crct; sht 2 - fix ISPO; Add ISP1 sw2; sht 3 - Add Target Reset sw4; sht 3 - change Port pin for C-SENSE-A, C-SENSE-B, C-SENSE-C, Tri-color LED (sht 3/4).	11/30/2013	
B1	- Add note to ISP Boot table (sht 3).	01/09/2014	
B2	- correct joystick name labels (sht 3,4).	03/03/2014	
B3	- change net name for PIO1_0 (sht 3,4).	05/22/2014	



CONTRACT NO.		LINK LPC4322	
APPROVALS	DATE	NXP Semiconductors	
DRAWN	d.consiglio	411 E. Plumeria Dr	
CHECKED		San Jose, CA 95134	
ISSUED	05/22/14	www.standardics.nxp.com/microcontrollers/	
SCALE		SIZE	D
		FWSCM NO.	
		DWG. NO.	Xpresso-LPC1549
		REV	B3
		SHEET 1 OF 04	

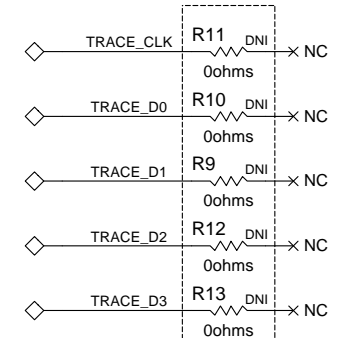
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



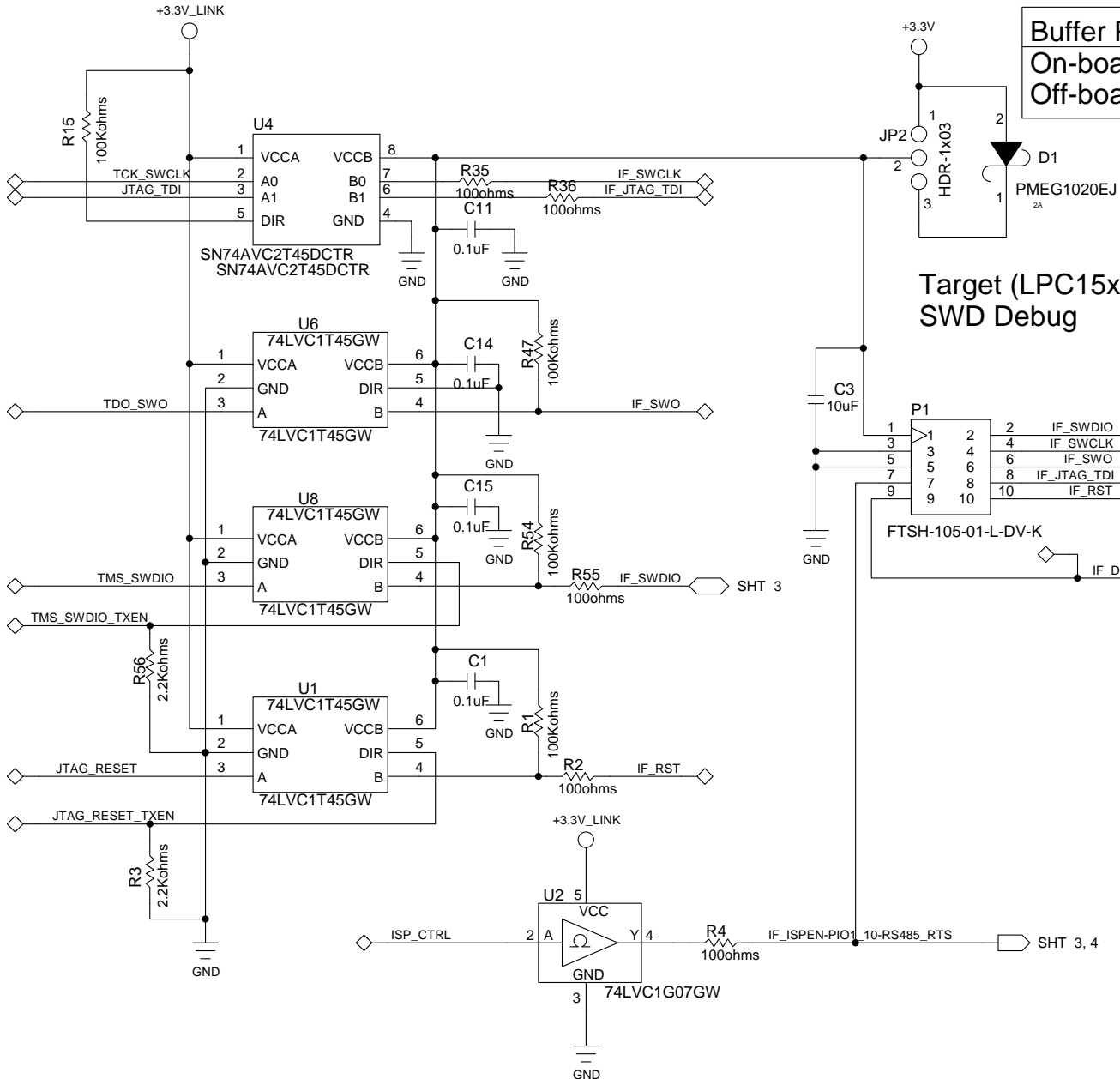
**Buffer Pwr Select (JP2)**  
On-board Target 1 - 2 (default)  
Off-board Target 2 - 3

**Target (LPC15xx) SWD Debug**

**Target to debug location (JP1)**  
On-board Target - open (default)  
Off-board Target - short

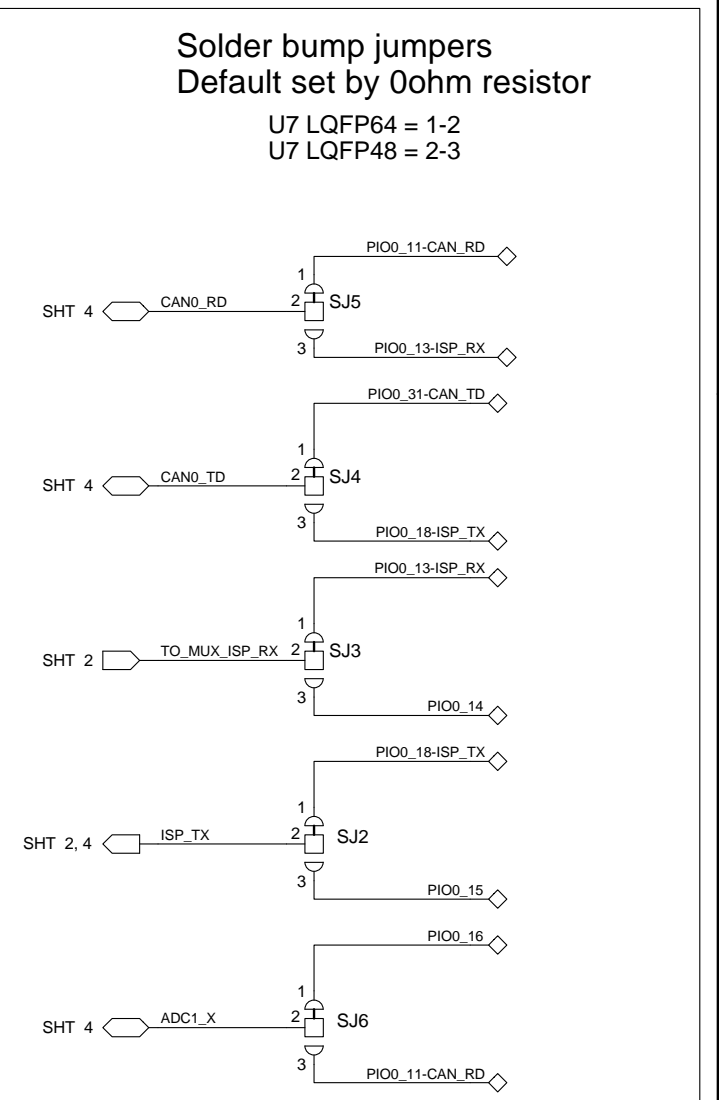
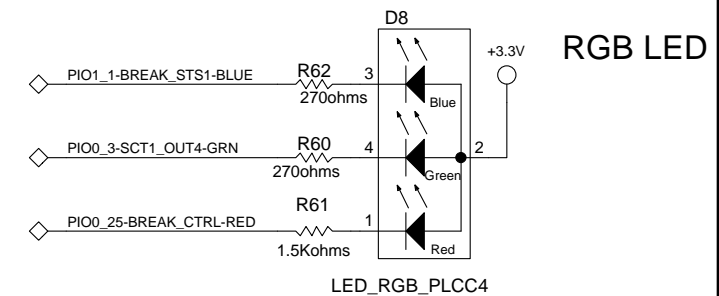
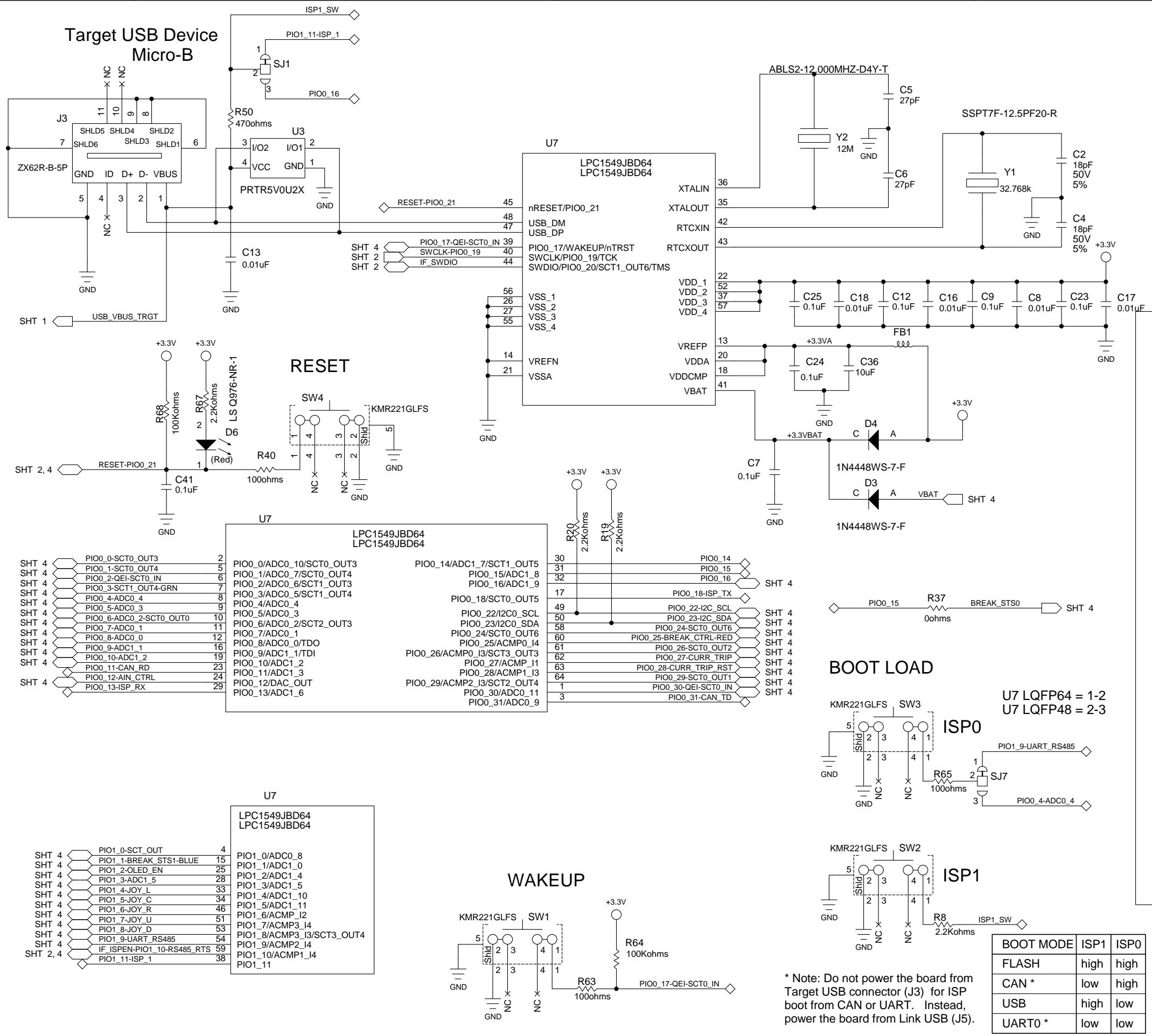


LPC1549 Target does not have trace.



CONTRACT NO.		LINK LPC4322	
APPROVALS	DATE	NXP Semiconductors	
DRAWN d.consiglio	5/22/2014	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
CHECKED		SIZE D	FSCM NO. Xpresso-LPC1549
ISSUED	05/22/14	DWG. NO.	REV B3
		SCALE	SHEET 2 OF 04

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



SHT	Pin	Function
SHT 4	2	PIO0_0-SCT0_OUT3
SHT 4	5	PIO0_1-SCT0_OUT4
SHT 4	6	PIO0_2-QEI-SCT0_IN
SHT 4	7	PIO0_3-SCT1_OUT4-GRN
SHT 4	8	PIO0_4-ADC0_4
SHT 4	9	PIO0_5-ADC0_3
SHT 4	10	PIO0_6-ADC0_2-SCT0_OUT0
SHT 4	11	PIO0_7-ADC0_1
SHT 4	12	PIO0_8-ADC0_0
SHT 4	16	PIO0_9-ADC1_1
SHT 4	19	PIO0_10-ADC1_2
SHT 4	23	PIO0_11-CAN_RD
SHT 4	24	PIO0_12-AIN_CTRL
SHT 4	29	PIO0_13-ISP_RX
SHT 4	2	PIO0_0/ADC0_10/SCT0_OUT3
SHT 4	5	PIO0_1/ADC0_7/SCT0_OUT4
SHT 4	6	PIO0_2/ADC0_6/SCT1_OUT3
SHT 4	7	PIO0_3/ADC0_5/SCT1_OUT4
SHT 4	8	PIO0_4/ADC0_4
SHT 4	9	PIO0_5/ADC0_3
SHT 4	10	PIO0_6/ADC0_2/SCT2_OUT3
SHT 4	11	PIO0_7/ADC0_1
SHT 4	12	PIO0_8/ADC0_0/TDO
SHT 4	16	PIO0_9/ADC1_1/TDI
SHT 4	19	PIO0_10/ADC1_2
SHT 4	23	PIO0_11/ADC1_3
SHT 4	24	PIO0_12/DAC_OUT
SHT 4	29	PIO0_13/ADC1_6
SHT 4	30	PIO0_14/ADC1_7/SCT1_OUT5
SHT 4	31	PIO0_15/ADC1_8
SHT 4	32	PIO0_16/ADC1_9
SHT 4	17	PIO0_18/SCT0_OUT5
SHT 4	49	PIO0_22-I2C_SCL
SHT 4	50	PIO0_23-I2C_SDA
SHT 4	58	PIO0_24-SCT0_OUT6
SHT 4	60	PIO0_25-BREAK_CTRL-RED
SHT 4	61	PIO0_26-SCT0_OUT2
SHT 4	62	PIO0_27-CURR_TRIP
SHT 4	63	PIO0_28-CURR_TRIP_RST
SHT 4	64	PIO0_29-SCT0_OUT1
SHT 4	1	PIO0_30-QEI-SCT0_IN
SHT 4	3	PIO0_31-CAN_TD

SHT	Pin	Function
SHT 4	4	PIO1_0-SCT_OUT
SHT 4	15	PIO1_1-BREAK_STS1-BLUE
SHT 4	25	PIO1_2-OLED_EN
SHT 4	28	PIO1_3-ADC1_5
SHT 4	33	PIO1_4-JOY_L
SHT 4	34	PIO1_5-JOY_C
SHT 4	46	PIO1_6-JOY_R
SHT 4	51	PIO1_7-JOY_U
SHT 4	53	PIO1_8-JOY_D
SHT 4	54	PIO1_9-UART_RS485
SHT 4	59	IF_ISPEN-PIO1_10-RS485_RTS
SHT 4	38	PIO1_11-ISP_1
SHT 4	8	PIO1_0/ADC0_8
SHT 4	10	PIO1_1/ADC1_0
SHT 4	11	PIO1_2/ADC1_4
SHT 4	12	PIO1_3/ADC1_5
SHT 4	13	PIO1_4/ADC1_10
SHT 4	14	PIO1_5/ADC1_11
SHT 4	15	PIO1_6/ACMP1_2
SHT 4	16	PIO1_7/ACMP3_14
SHT 4	17	PIO1_8/ACMP3_13/SCT3_OUT4
SHT 4	18	PIO1_9/ACMP2_14
SHT 4	19	PIO1_10/ACMP1_14
SHT 4	20	PIO1_11

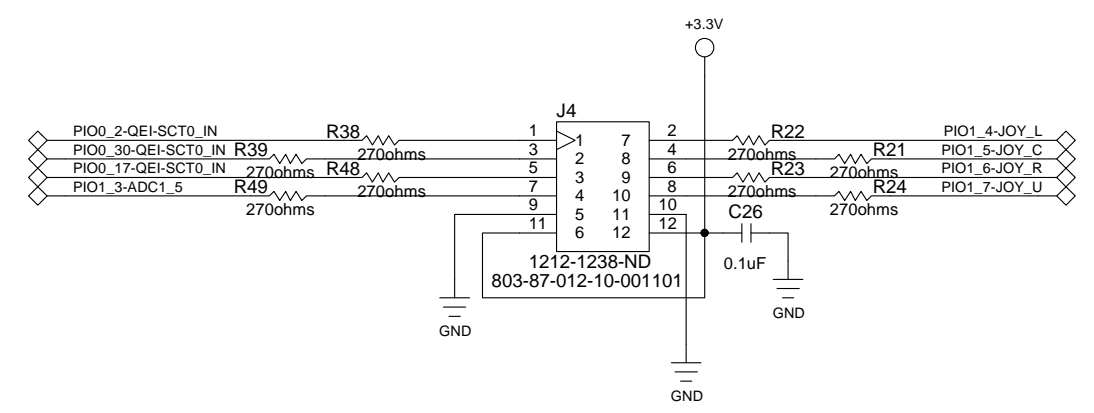
BOOT MODE	ISP1	ISP0
FLASH	high	high
CAN *	low	high
USB	high	low
UART0 *	low	low

\* Note: Do not power the board from Target USB connector (J3) for ISP boot from CAN or UART. Instead, power the board from Link USB (J5).

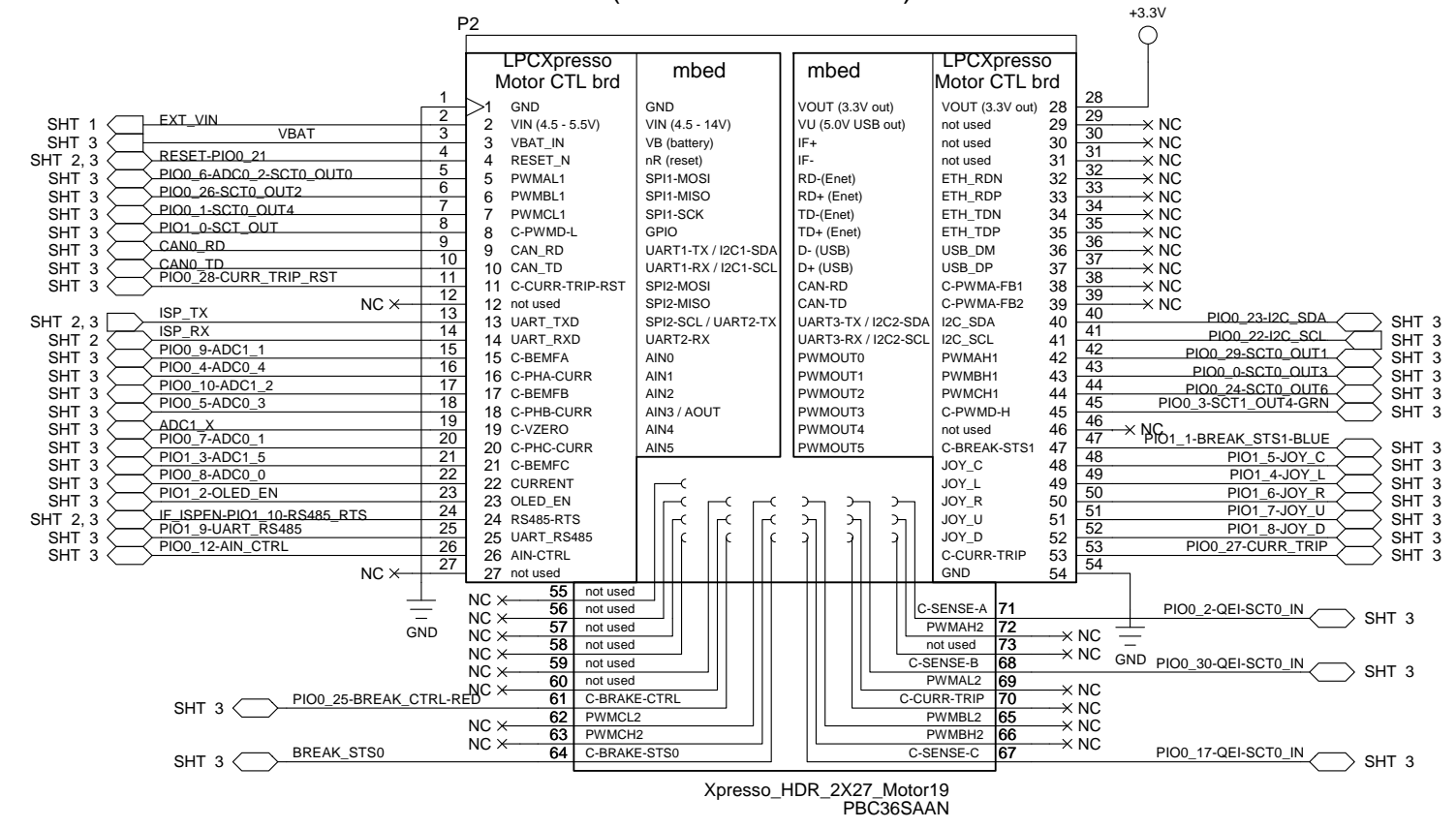
CONTRACT NO.		Target LPC1549	
APPROVALS	DATE	NXP Semiconductors	
DRAWN	5/22/2014	411 E. Plumeria Dr	
CHECKED		San Jose, CA 95134	
ISSUED	05/22/14	www.standardics.nxp.com/microcontrollers/	
SCALE		SIZE	FSCM NO.
		DWG. NO.	Xpresso-LPC1549
		REV	B3
		SHEET 3 OF 04	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

### PMOD compatible header

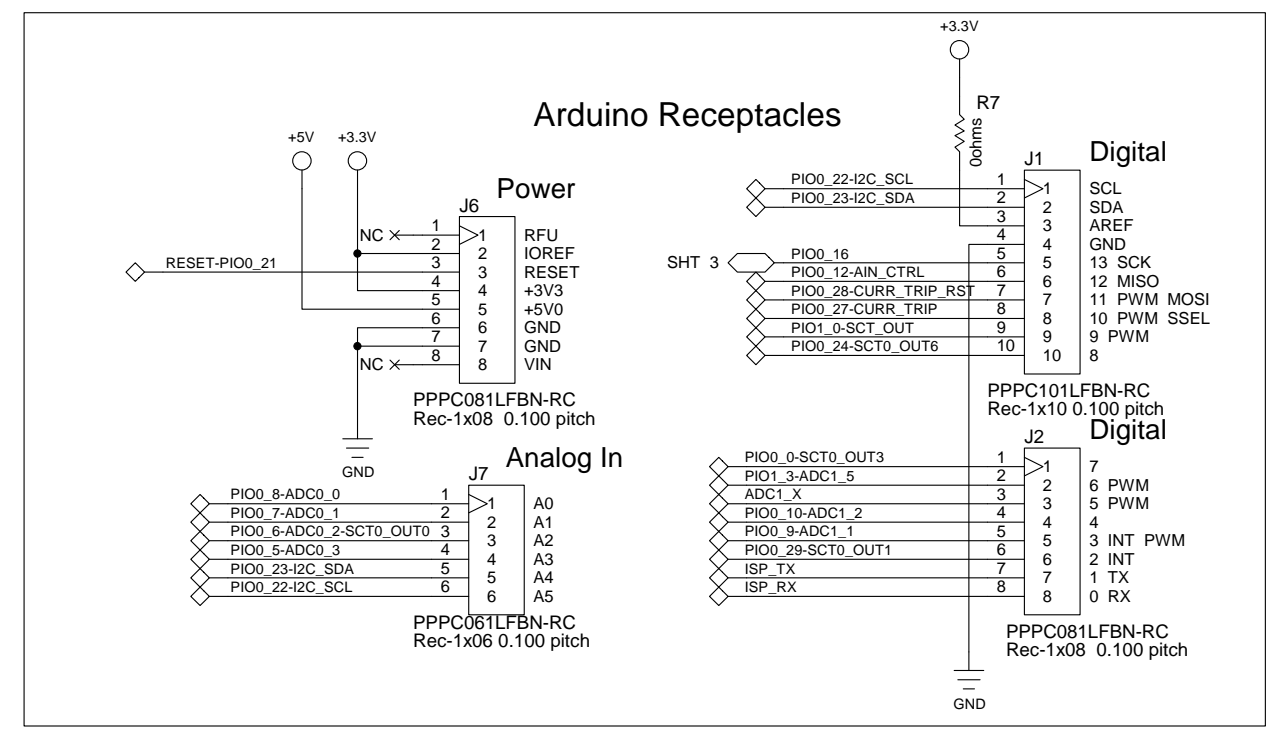


### LPCXpresso Motor Control brd header (Bottom side of PCB)



Xpresso\_HDR\_2X27\_Motor19  
PBC36SAAN

### Arduino Receptacles



CONTRACT NO.		Xpresso MC/Arduino/PMOD headers	
APPROVALS	DATE	<b>NXP Semiconductors</b> 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
DRAWN	d.consiglio		
CHECKED	05/22/14	SIZE	D
ISSUED	05/22/14	DWG. NO.	Xpresso-LPC1549
SCALE		REV	B3
SHEET		4 OF 04	