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MODEL	Rock	TITLE	<h1>Pinout Diagram</h1> <p>Dimensions are in millimeters unless otherwise specified.</p>
NUMBER	8950-A		
DWG.	Allen Hsieh		
DATE	2012/11/8		
SHEET	1 of 4		

Rock Pinout Definitions Table

Name	Definition	
POWER_BT	Pin 1	PWR_BTN#
	Pin 2	GND
PWR_LED	Pin 1	Main Power D+
	Pin 2	GND
PWR_5V	Pin 1	+5V
	Pin 2	GND
PWMOUT_CNT	Pin 1	GND
	Pin 2	VCC3 (+3.3V)
	Pin 3	PWMOUT 0
	Pin 4	PWMOUT 2
	Pin 5	PWMOUT 1
	Pin 6	PWMOUT 3
SPI_CNT	Pin 1	VCC3 (+3.3V)
	Pin 2	SPI1 SS0-
	Pin 3	SPI1 MISO
	Pin 4	SPI1 SS1-
	Pin 5	SPI1 SS0-
	Pin 6	SPI1 SS2-
	Pin 7	SPI1 CLK
	Pin 8	SPI1 SS3-
	Pin 9	SPI1 MOSI
	Pin 10	GND
SFP_CNT	Pin 1	VPROG_SPI
	Pin 2	GND
	Pin 3	SFCS0-
	Pin 4	SFCLK
	Pin 5	SFDI
	Pin 6	SFDO
I2C_CNT	Pin 1	VCC3 (+3.3V)
	Pin 2	GND
	Pin 3	I2C0 SCL
	Pin 4	I2C0 SDA

	Pin 5	I2C2 SCL
	Pin 6	I2C2 SDA
UART_CNT	Pin 1	GND
	Pin 2	VCC3 (+3.3V)
	Pin 3	UART1 RXD1
	Pin 4	UART1 TXD1
	Pin 5	UART1 CTS1
	Pin 6	UART1 RTS1
GPIO_CNT1	Pin 1	GND
	Pin 2	VCC3 (+3.3V)
	Pin 3	SUS_GPIO 0 (Reserve)
	Pin 4	GPIO 0
	Pin 5	SUS_GPIO 1 (Reserve)
	Pin 6	GPIO 1
	Pin 7	WAKEUP 0 (Reserve)
	Pin 8	GPIO 2
	Pin 9	SUS_GPIO 2 (Reserve)
	Pin 10	GPIO 3
	Pin 11	SUS_GPIO 3 (Reserve)
	Pin 12	GND
	Pin 13	3VSB (+3.3V Standby)
	Pin 14	NA
GPIO_CNT2	Pin 1	VCC3 (+3.3V)
	Pin 2	VCC3 (+3.3V)
	Pin 3	GPIO 20
	Pin 4	GPIO 4
	Pin 5	GPIO 21
	Pin 6	GPIO 5
	Pin 7	GPIO 24
	Pin 8	GPIO 6
	Pin 9	GPIO 25
	Pin 10	GPIO 26
	Pin 11	GND
	Pin 12	GND
	Pin 13	GND
	Pin 14	NA
CIR (Reserve)	Pin 1	3VSB (+3.3V Standby)

	Pin 2	GND
	Pin 3	CIR IN
TOUCH_CNT (Reserved for Resistive Touch)	Pin 1	TPWI Power
	Pin 2	TPXP
	Pin 3	TPYM
	Pin 4	TPXM
	Pin 5	TPYP
JTAG_CNT	Pin 1	VCC3 (+3.3V)
	Pin 2	VCC3 (+3.3V)
	Pin 3	JTAGTRST-
	Pin 4	GND
	Pin 5	JTAGTDI
	Pin 6	GND
	Pin 7	JTAGTMS
	Pin 8	GND
	Pin 9	JTAGTCK
	Pin 10	GND
	Pin 11	N/C
	Pin 12	GND
	Pin 13	JTAGTDO
	Pin 14	GND
	Pin 15	JTAGTRST-
	Pin 16	GND
	Pin 17	N/C
	Pin 18	GND
	Pin 19	N/C
	Pin 20	GND
DEBUG_PORT (Console)	Pin 1	GND
	Pin 2	VCC3 (+3.3V)
	Pin 3	UART0 TXD
	Pin 4	UART0 RXD
	Pin 5	N/C
	Pin 6	NA
	Pin 7	N/C