

Title		FMC Carrier-S6		Rev	B.1
Circuit		User I/O, FMC		Copyright 2013	
Doc#	500-256				
Engineer	EG				
Author	DL				
Date	2/21/2013				
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A

A

B

B

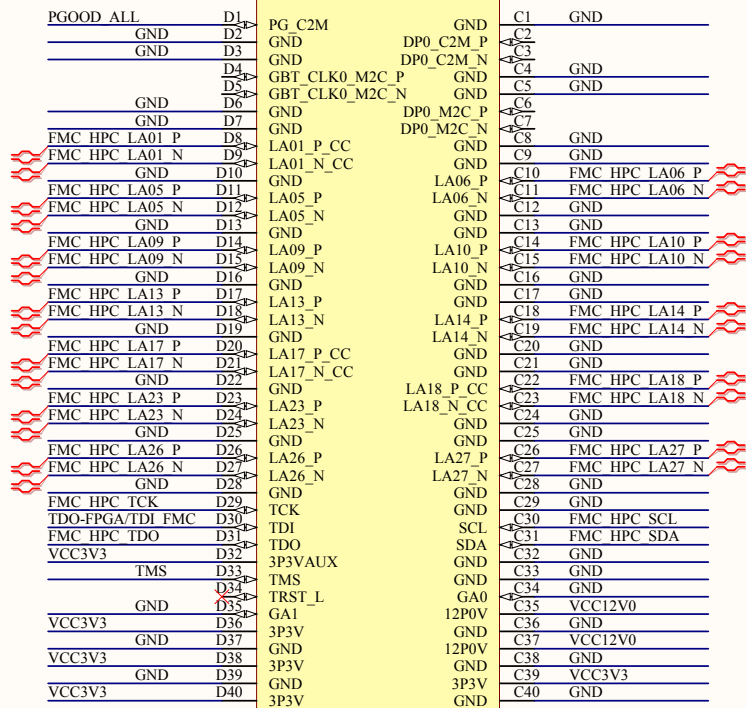
C

C

D

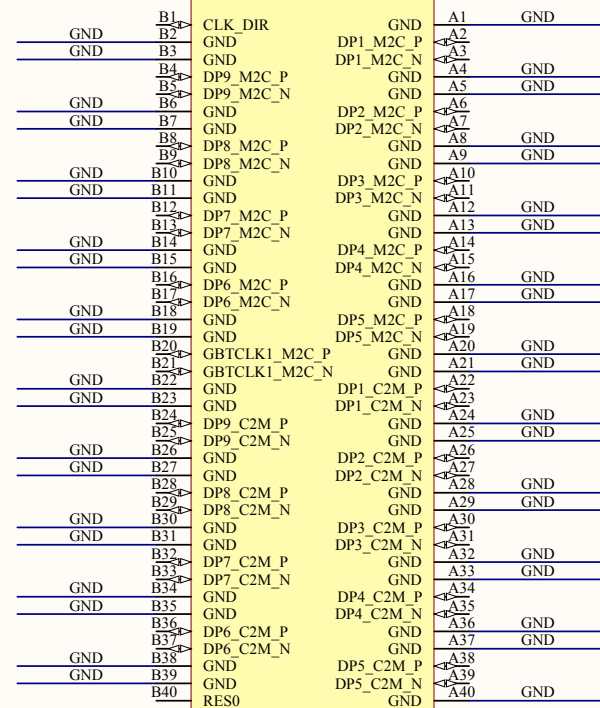
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J1D



ASP-134486-01

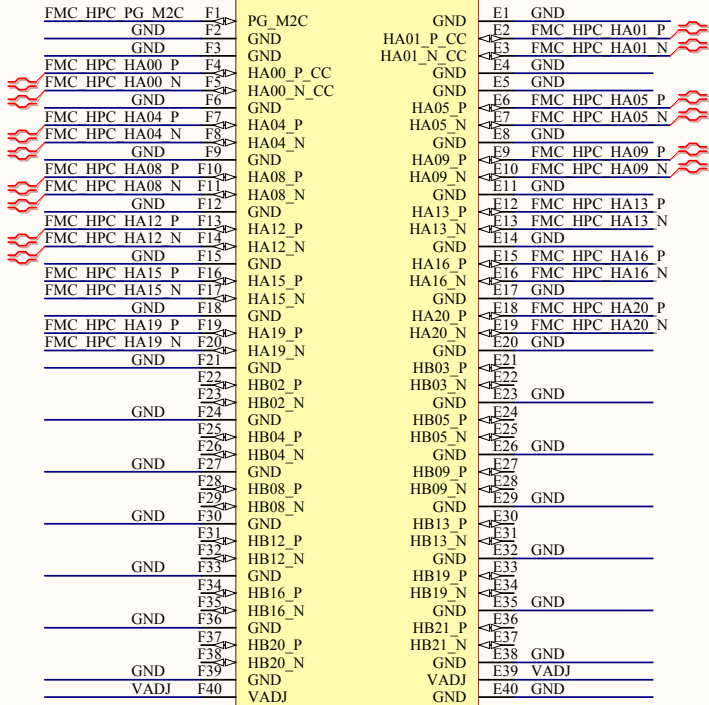
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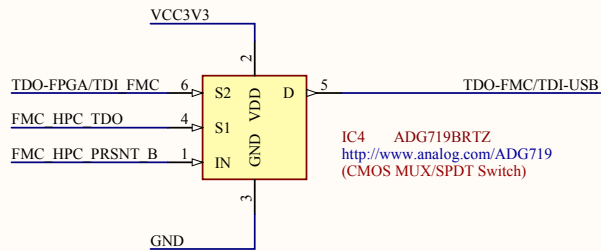
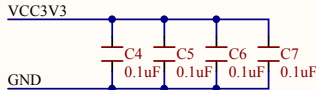
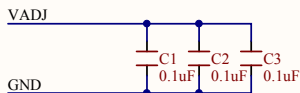
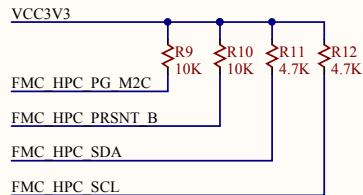
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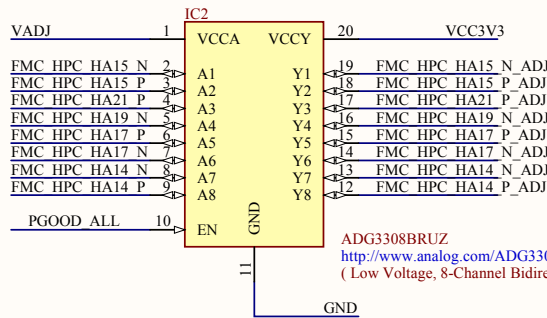
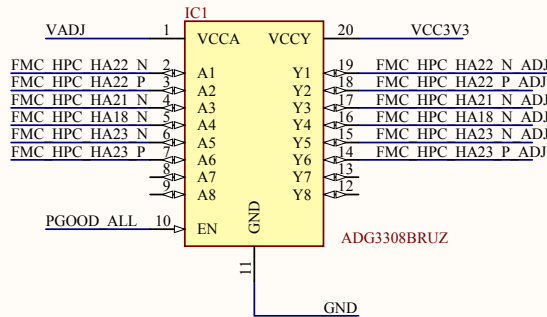
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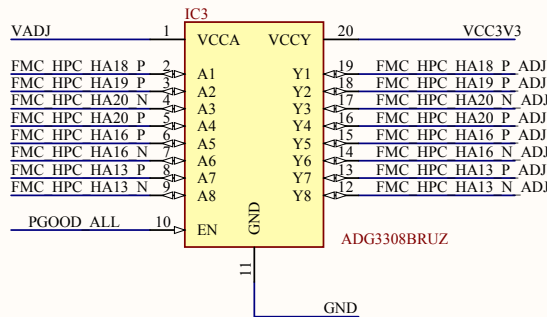
ASP-134486-01



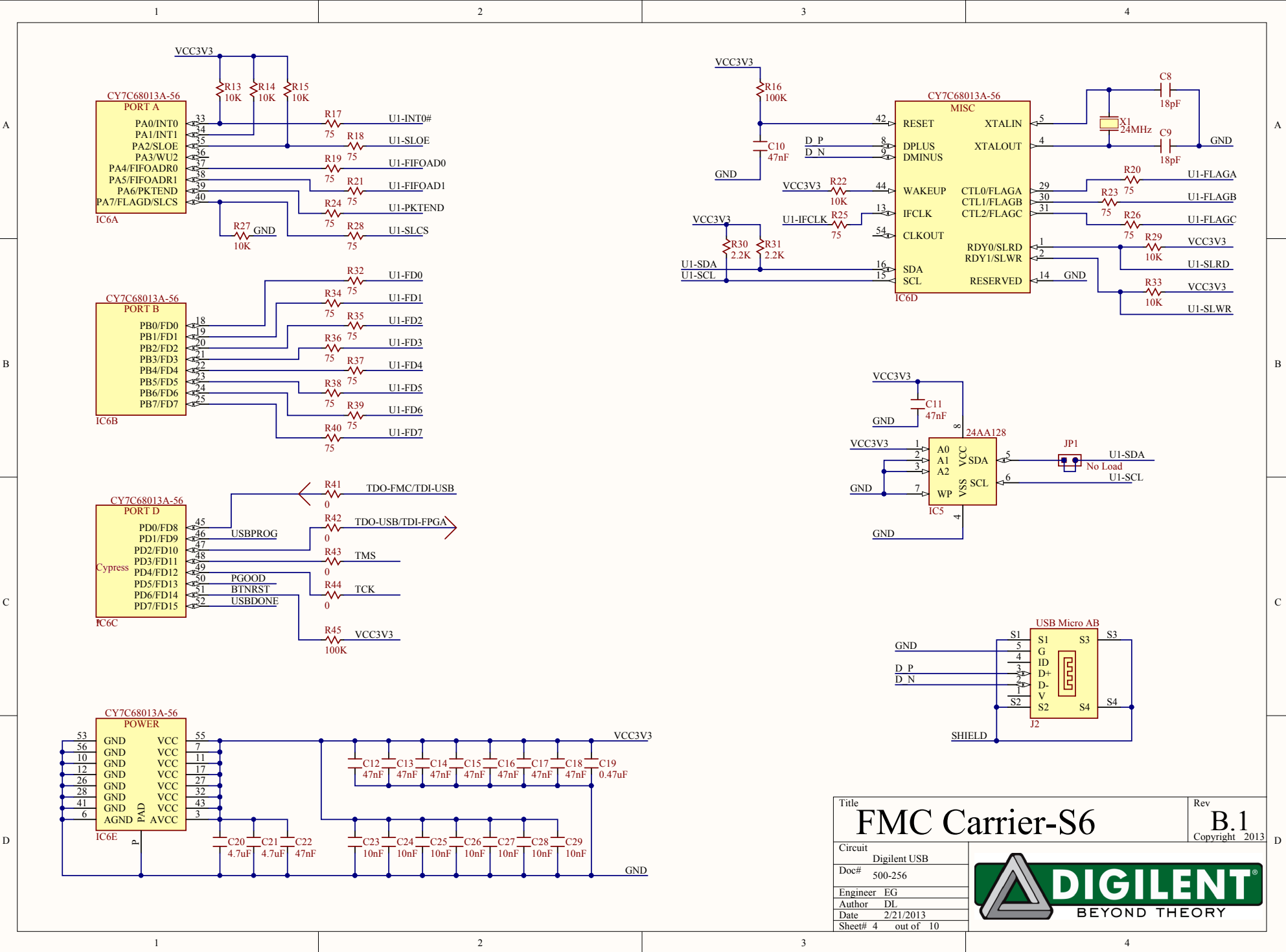
IC4 ADG719BRTZ  
<http://www.analog.com/ADG719>  
 (CMOS MUX/SPDT Switch)



<http://www.analog.com/ADG3308>  
 ( Low Voltage, 8-Channel Bidirectional Logic Level Translator)

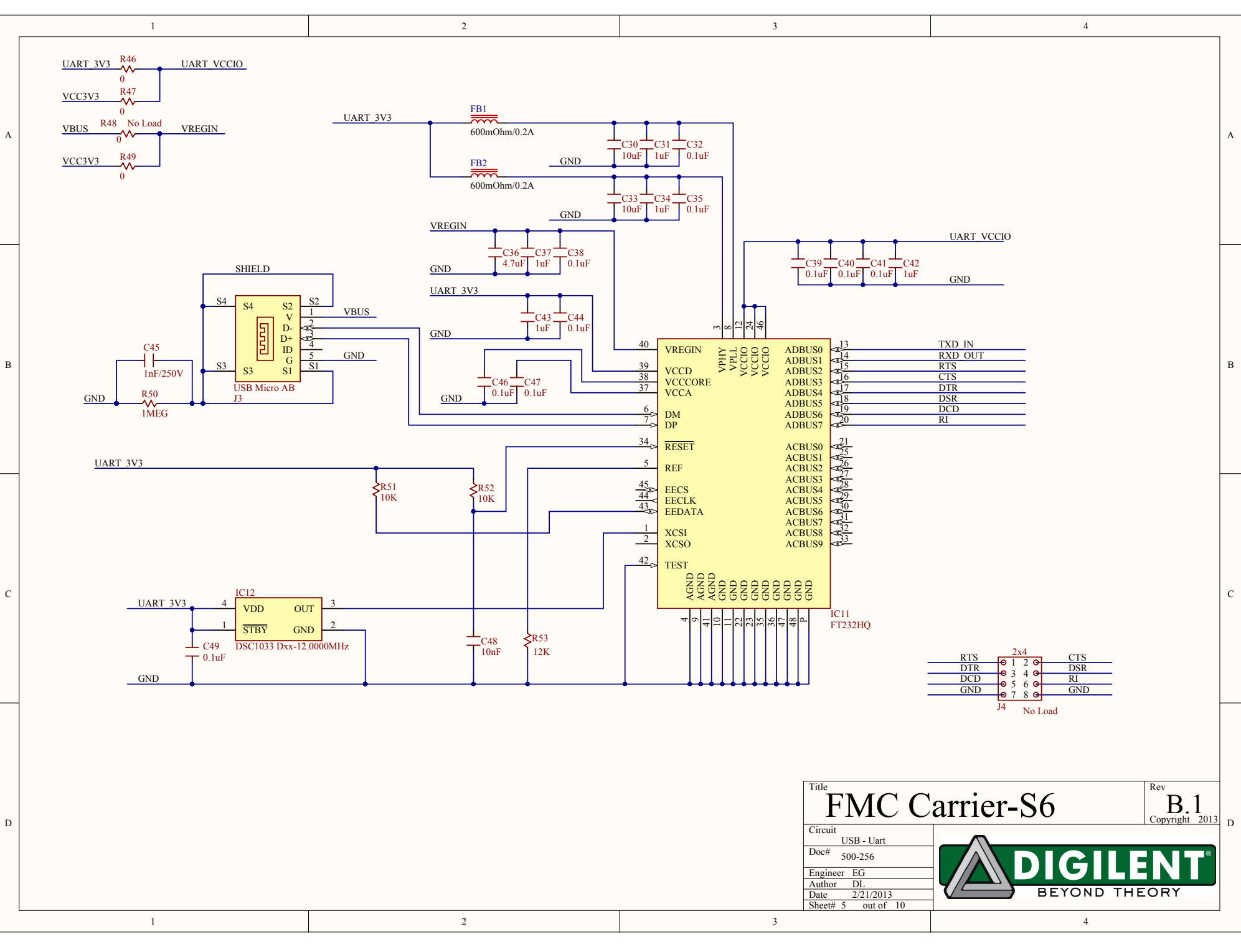


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FMC, Level Shifters			
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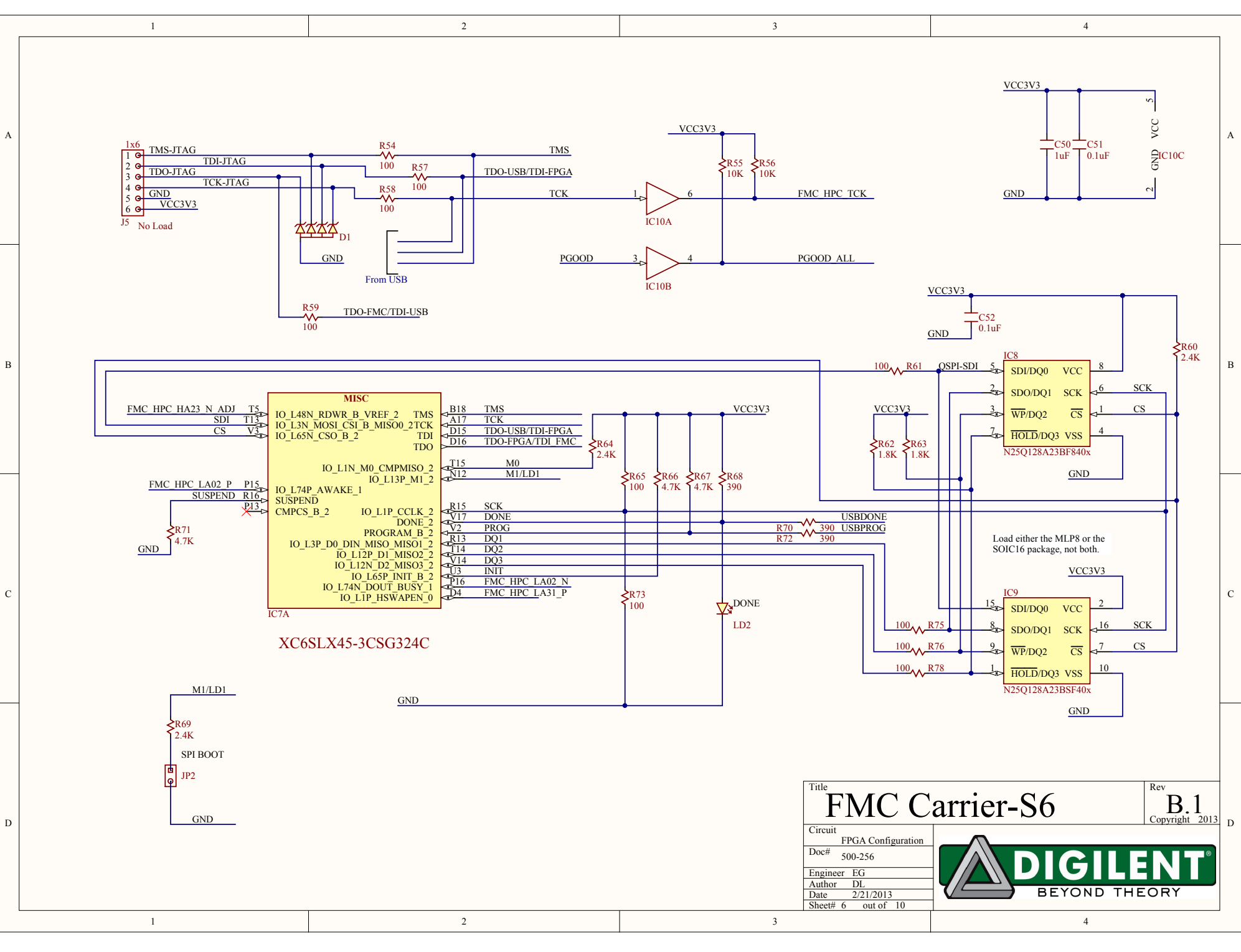


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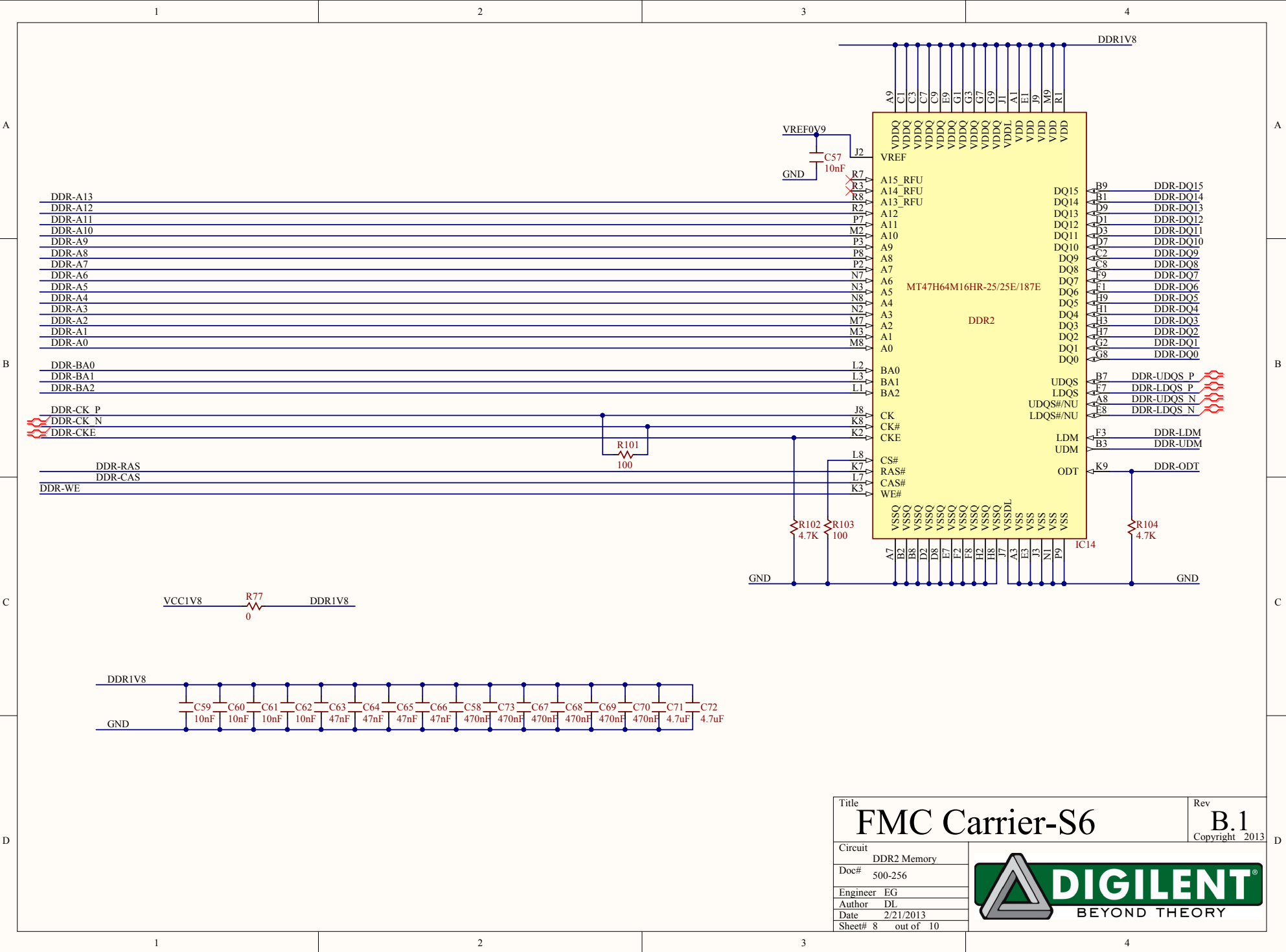
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Circuit		USB - Uart		Copyright		2013	
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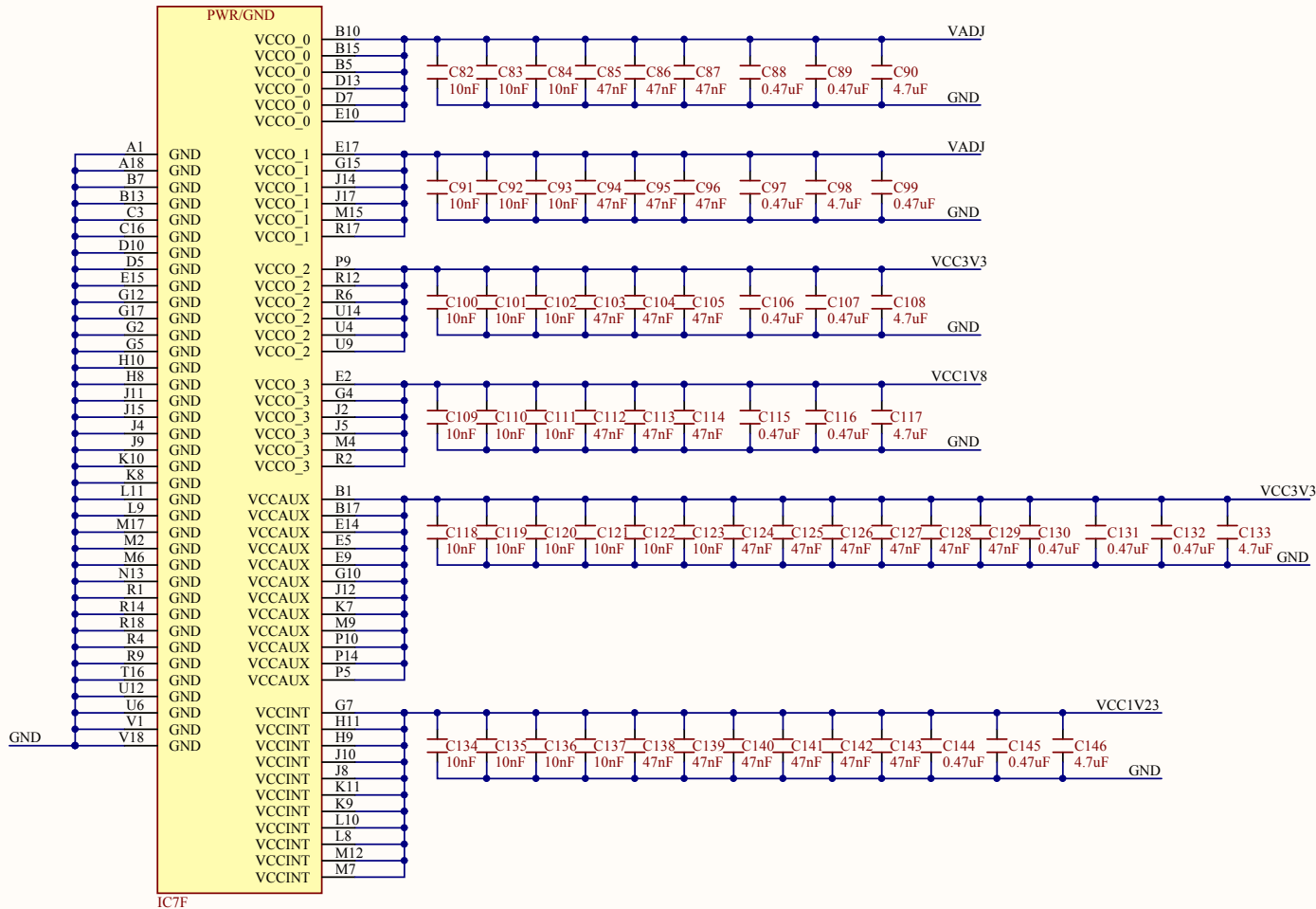






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Foot

F1

Foot

F2

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F3

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F4

Digilent Inc.

Xilinx

CE

Chinese ROHS

ROHS

Data IO

Title

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FPGA Power

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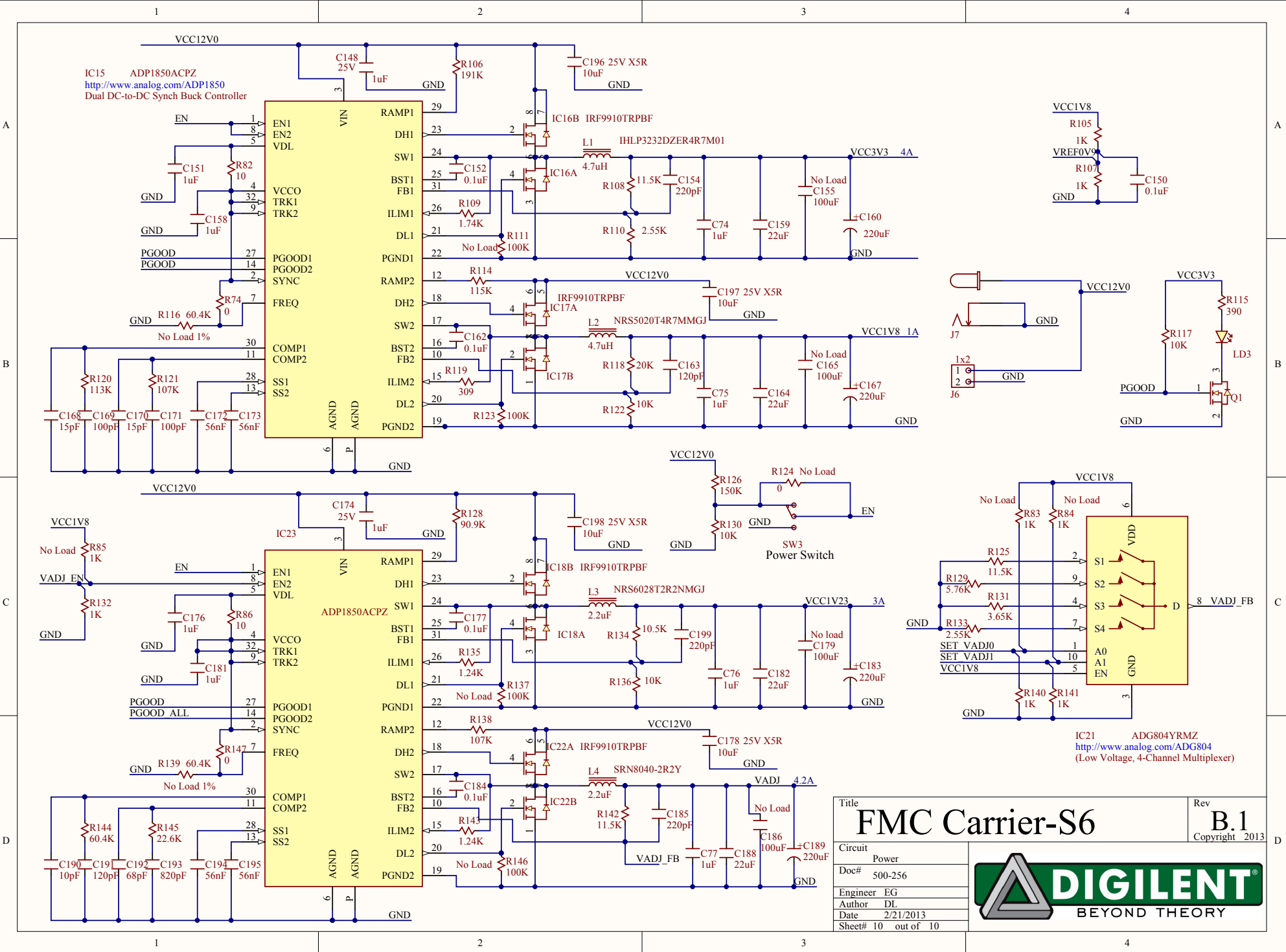
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