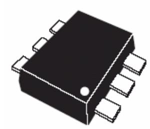
STLQ015xx

150 mA, ultra-low quiescent current linear voltage regulator

Features

- Input voltage from 1.5 to 5.5 V
- Very low quiescent current:
  - 1.0 µA (typ) at no load
  - 1.4 µA (typ) at 150 mA load
  - 1 nA (typ) in OFF mode
  - 200 nA max in OFF mode at 125 °C
- Output voltage tolerance: ± 2% at 25 °C
- 150 mA guaranteed output current
- Wide range of output voltages: 0.8 V to 3.3 V in 100 mV steps
- Logic-controlled electronic shutdown
- Compatible with ceramic capacitor (C_{OUT} = 1 µF)
- Internal current and thermal limit
- Package: SOT666-6L
- Temperature range: -40 °C to 125 °C



SOT666

Description

The STLQ015xx provides 150 mA of maximum current from an input voltage ranging from 1.5 V to 5.5 V, with a typical dropout voltage of 112 mV. The key feature of this device is its quiescent current, which is just 1.4 µA at maximum output current. The device is stable with a ceramic capacitor on the output. It offers very low quiescent current, extending battery life and making the device suitable for applications requiring very long standby time.

The enable logic control function puts the STLQ015xx in shutdown mode, reducing total current consumption to 1 nA. The device also includes short-circuit constant-current limiting and thermal protection. Typical applications for the device are portable and battery-powered systems, electronic sensors, and microcontroller power supply.

Table 1. Device summary

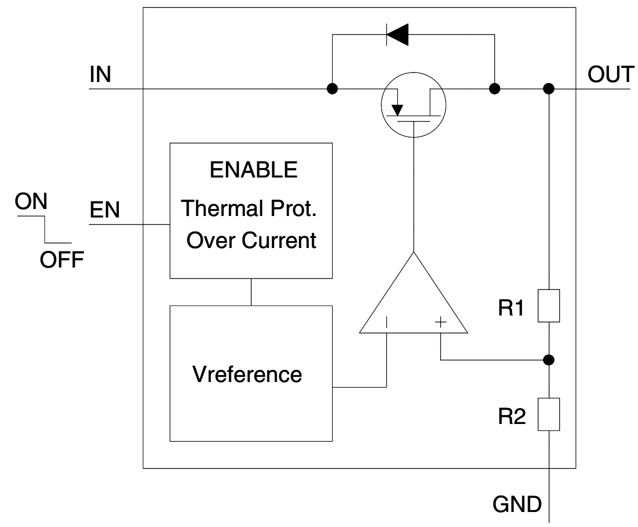
Part numbers	Order codes	Output voltages (1)
STLQ015XX15	STLQ015XG15R	1.5 V
STLQ015XX25	STLQ015XG25R	2.5 V
STLQ015XX28	STLQ015XG28R	2.8 V
STLQ015XX30	STLQ015XG30R	3.0 V
STLQ015XX31	STLQ015XG31R	3.1 V
STLQ015XX33	STLQ015XG33R	3.3 V

1. Other voltages available on request from 0.8 V to 3.3 V in 100 mV steps

Contents

1 STLQ015xx block diagram

Figure 1. Device block diagram



2 Pin configuration and description

Figure 2. Pin configuration (top view)

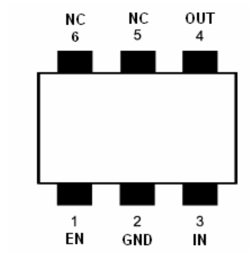
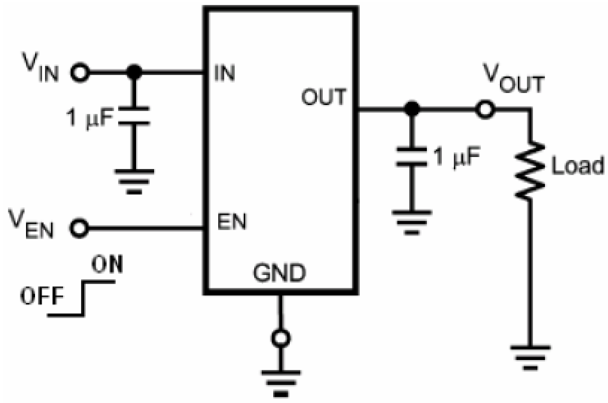


Table 2. Pin description

Pin	Symbol	Function
1	EN	Enable input. Set V_{EN} = High to turn on the device. Set V_{EN} = Low to turn off the device.
2	GND	Ground
3	IN	Input voltage
4	OUT	Output voltage
5	NC	Not connected
6	NC	Not connected

3 Typical application

Figure 3. Typical application circuit



4 Maximum ratings

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{IN}	DC input voltage	-0.3 to 7	V
V_{OUT}	DC output voltage	- 0.3 to V_{IN} + 0.3	V
V_{EN}	Enable input voltage	- 0.3 to V_{IN} + 0.3	V
I_{OUT}	Output current	Internally limited	mA
ESD	Human Body Model	± 3	kV
	Machine Model	± 300	V
P_D	Power dissipation	Internally limited	mW
T_{STG}	Storage temperature range	-65 to 150	°C
T_{OP}	Max junction temperature	150	°C

Note: Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied. All values are referred to GND.

Table 4. Thermal data

Symbol	Parameter	SOT666	Unit
R_{thJA}	Thermal resistance junction-ambient	132	°C/W
R_{thJC}	Thermal resistance junction-case	56	°C/W

5 Electrical characteristics

T_J = 25 °C, $V_{IN} = V_{OUT(NOM)}$ + 1 V, $C_{IN} = C_{OUT}$ = 1 µF, I_{OUT} = 1 mA, $V_{EN} = V_{IN}$, unless otherwise specified.

Table 5. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{IN}	Operating input voltage	I_{OUT} = 0	1.5		5.5	V
		-40°C < T_J < 125°C, I_{OUT} = 150mA	1.55		5.5	
V_{OUT}	V_{OUT} accuracy	I_{OUT} = 1mA	-2		2	%
		I_{OUT} = 1mA, V_{OUT} < 1V	-20		+20	mV
		I_{OUT} = 1mA, -40°C < T_J < 125°C	-3		3	%
$\Delta V_{OUT\text{-}LINE}$	Static line regulation	V_{OUT} +1V ≤ V_{IN} ≤ 5.5V, I_{OUT} = 1mA		±0.01		%/V
$\Delta V_{OUT\text{-}LOAD}$	Static load regulation	I_{OUT} = 1mA to 150mA		±0.002		%/mA
V_{DROP}	Dropout voltage (1)	I_{OUT} = 150mA		112		mV
		I_{OUT} = 150mA, -40°C < T_J < 125°C			300	
e_N	Output noise voltage	10kHz to 100kHz, I_{OUT} = 10mA, V_{OUT} = 0.8V		75		μV_{RMS}
SVR	Supply voltage rejection V_{OUT} = 0.8V	$V_{IN} = V_{OUTNOM}$ + 1V +/-V_{RIPPLE} V_{RIPPLE} = 0.1V, Freq. = 1kHz I_{OUT} = 10mA		40		dB
		$V_{IN} = V_{OUTNOM}$ + 1V +/-V_{RIPPLE} V_{RIPPLE} = 0.1V, Freq.=10kHz I_{OUT} = 1mA		30		
		$V_{IN} = V_{OUTNOM}$ + 1V +/-V_{RIPPLE} V_{RIPPLE} = 0.1V, Freq.=100kHz I_{OUT} = 1mA		15		
I_Q	Quiescent current	I_{OUT} = 0		1.0	1.7	µA
		I_{OUT} = 0 to 150mA, -40°C < T_J < 125°C		1.7	2.4	
I_{OFF}	Shutdown current (2)	V_{IN} input current in OFF mode: V_{EN} = GND, -40°C < T_J < 125°C		1	200	nA
I_{SC}	Short circuit current	R_L = 0	250	350		mA
V_{EN}	Enable input logic low	V_{IN} = 1.5V to 5.5V			0.4	V
	Enable input logic high	V_{IN} = 1.5V to 5.5V	0.7			V
I_{EN}	Enable pin input current	V_{EN} = 5.5V		1	200	nA
T_{ON}	Turn-on time (3)	V_{OUT} = 0.8 V, I_{OUT} = 150 mA		160		µs
T_{SHDN}	Thermal shutdown			170		°C
	Hysteresis			180		

Table 5. Electrical characteristics (continued)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{OUT}	Output capacitor	Capacitance (see typical performance characteristics for stability)	0.47		10	µF
	ESR		0.056		6	Ω

1. Dropout voltage is the input-to-output voltage difference at which the output voltage is 100 mV below its nominal value. This specification does not apply for output voltages below 1.5 V
2. During shutdown and at no load, P-channel leakage current flowing through the internal resistor divider determines the increase of V_{OUT}
3. Turn-on time is the time measured between the enable input just exceeding V_{EN} high value and the output voltage just reaching 95% of its nominal value

6 Typical performance characteristics

Figure 4. Output voltage vs. temperature

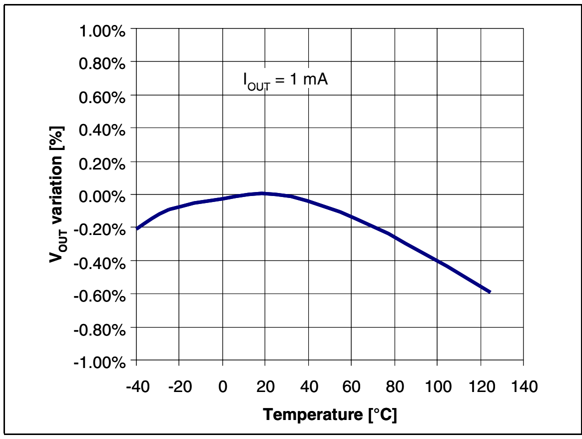


Figure 5. Output voltage vs. input voltage

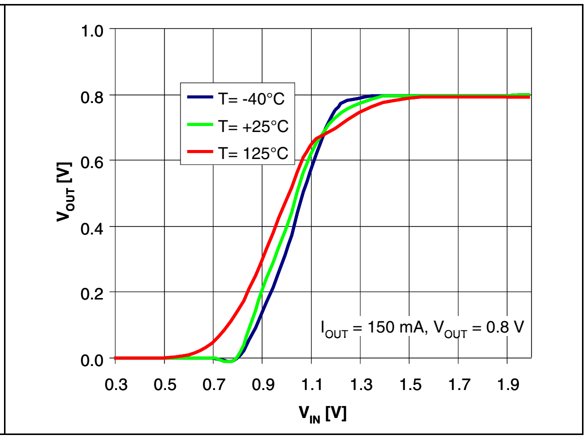


Figure 6. Output voltage vs. input voltage

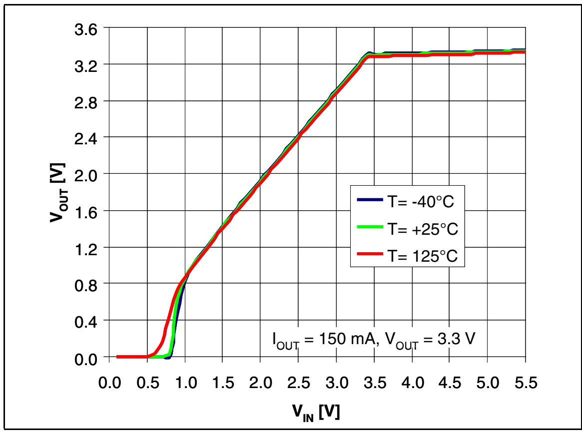


Figure 7. Dropout voltage vs. temperature

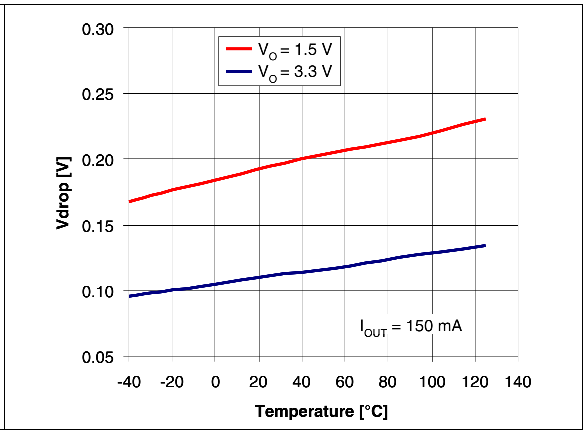


Figure 8. Dropout voltage vs. output current

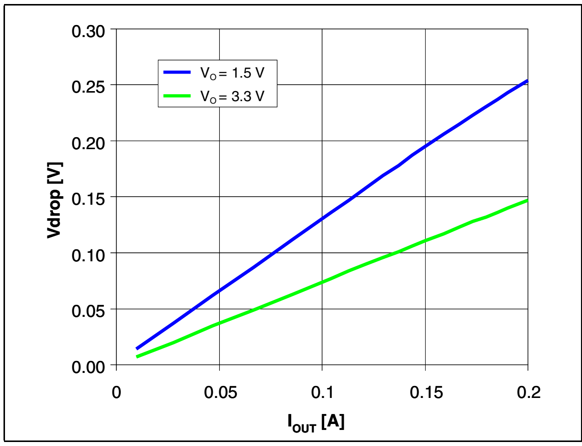


Figure 9. Quiescent current vs. temperature

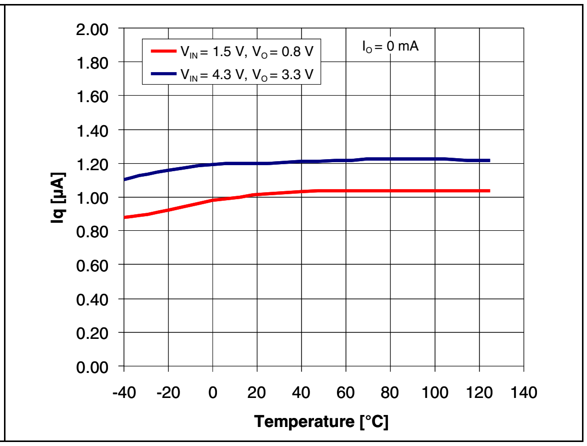


Figure 10. Supply voltage rejection vs. frequency

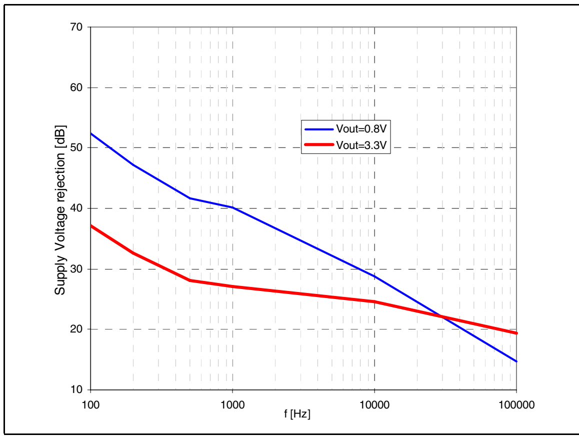


Figure 11. Supply voltage rejection vs. I_{OUT}

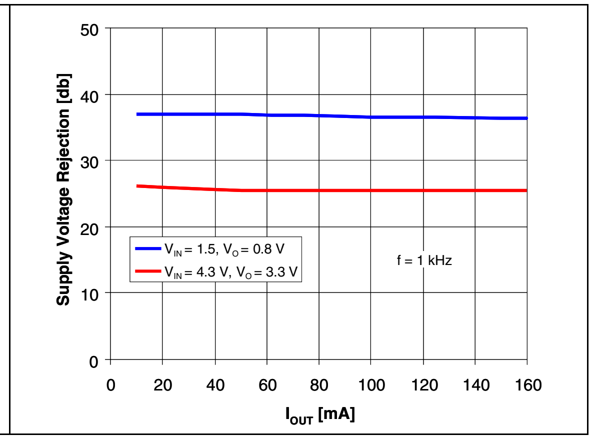


Figure 12. Quiescent current vs. input voltage

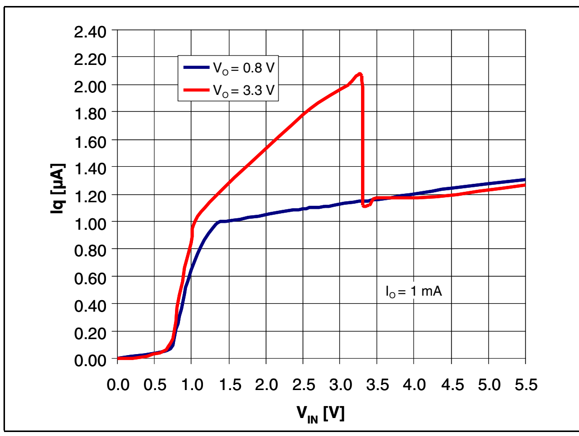


Figure 13. Quiescent current vs. output current

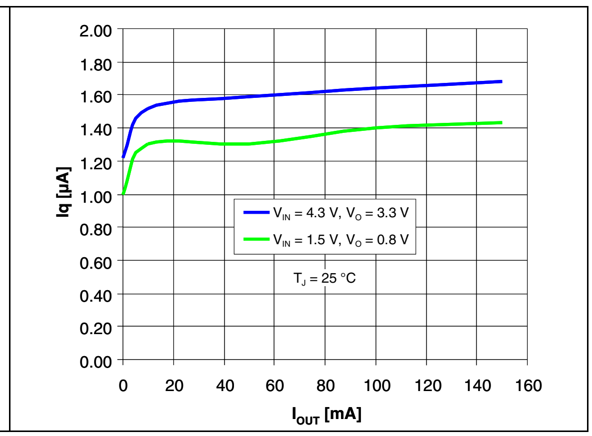


Figure 14. Output noise voltage vs. frequency

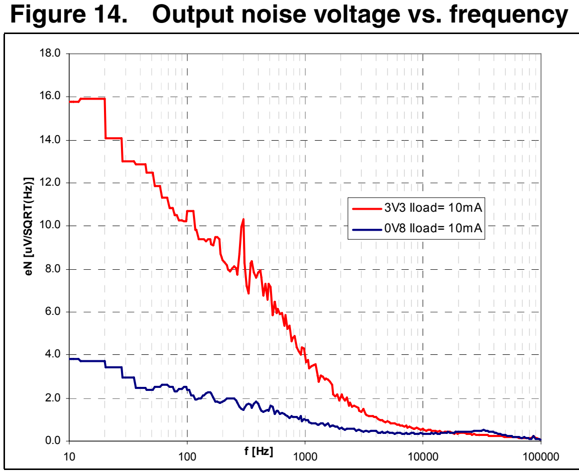


Figure 15. C_{OUT} stability region

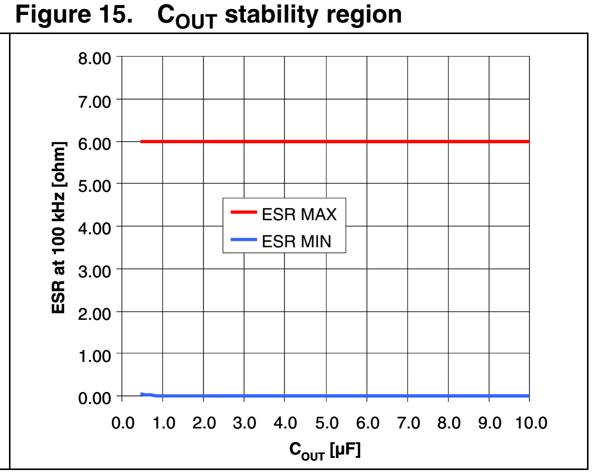
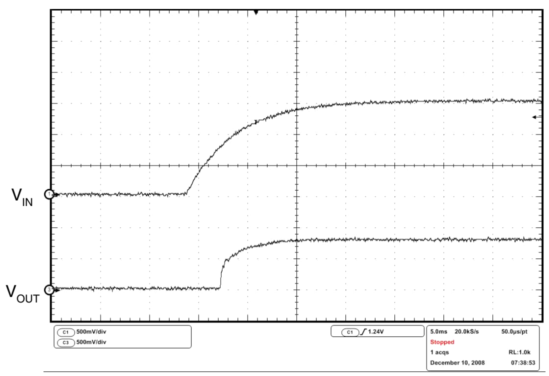
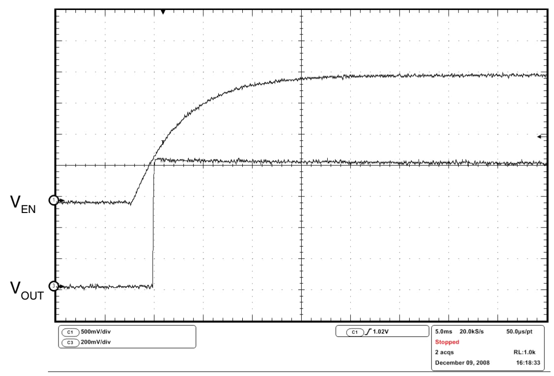


Figure 16. Startup transient



V_{IN} from 0 to 1.5 V, V_{EN} tied to V_{IN}, No Load C_{OUT} = 1 µF

Figure 17. Enable transient



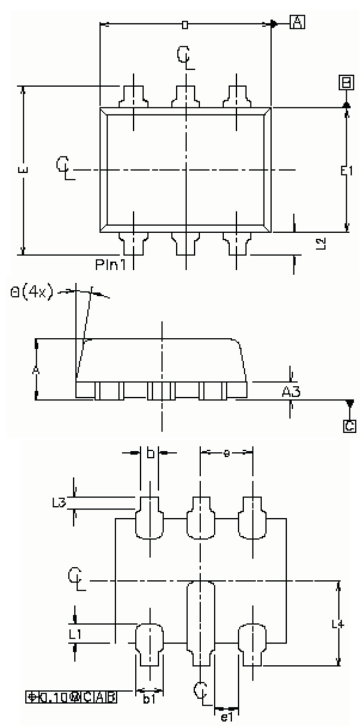
V_{IN} = 1.5 V; V_{EN} from 0 to 2 V, No Load, T = 25 °C

7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK® is an ST trademark.

SOT666 mechanical data

Dim.	mm.			inch.		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.53	0.57	0.60	0.021	0.022	0.024
A3	0.13	0.17	0.18	0.005	0.006	0.007
D	1.50	1.66	1.70	0.059	0.065	0.067
E	1.50	1.65	1.70	0.059	0.065	0.067
E1	1.10	1.20	1.30	0.043	0.047	0.051
L1	0.11	0.19	0.26	0.004	0.007	0.010
L2	0.10	0.23	0.30	0.004	0.009	0.012
L3	0.05	0.10		0.002	0.004	
b	0.17		0.25	0.17		0.25
b1		0.27	0.34		0.27	0.34
e		0.50			0.5	
e1	0.20			0.2		
θ	8°	10°	12°	8°	10°	12°



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8 Different output voltage versions of the STLQ015xx available on request

Table 6. Options available on request

Order codes	Output voltages
STLQ015XG08R	0.8 V
STLQ015XG12R	1.2 V
STLQ015XG18R	1.8 V

9 Revision history

Table 7. Document revision history

Date	Revision	Changes
23-Mar-2010	1	Initial release.

Please Read Carefully: