

# Schematic Diagram

## Micro2440SDK 20090530

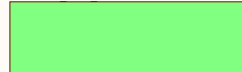
Micro2440  
01-Micro2440.sch



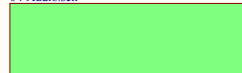
interface  
02-Interface.sch



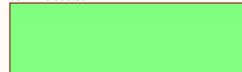
UART  
03-UART USB ETC.sch



Audio  
04-Audio.sch



DM9000  
05-DM9000.sch



Power  
06-POWER.sch



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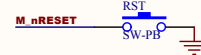
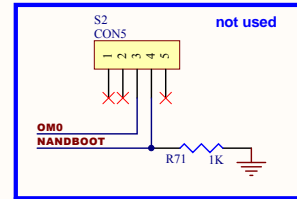
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|--|--|----------|
| Title  |  |          |
| Micro2440SDK   |  |          |
| Size<br>B  | Number<br>Translation by S. Watterott 01.07.2009 | Revision |
| Date: 05.07.2009   | Sheet of   |          |
| File: D:\Dokumente und Einstellungen\...\main.SchDrawn By: |  |          |

|                 |      |      |                |
|-----------------|------|------|----------------|
| MICRO2440C      |      |      |                |
| <b>GND</b>      | PC56 | PC55 | <b>VDD5V</b>   |
| <b>LDATA15</b>  | PC54 | PC53 | <b>LDATA14</b> |
| <b>LDATA13</b>  | PC52 | PC51 | <b>LDATA12</b> |
| <b>LDATA11</b>  | PC50 | PC49 | <b>LDATA10</b> |
| <b>LDATA9</b>   | PC48 | PC47 | <b>LDATA8</b>  |
| <b>LDATA7</b>   | PC46 | PC45 | <b>LDATA6</b>  |
| <b>LDATA5</b>   | PC44 | PC43 | <b>LDATA4</b>  |
| <b>LDATA3</b>   | PC42 | PC41 | <b>LDATA2</b>  |
| <b>LDATA1</b>   | PC40 | PC39 | <b>LDATA0</b>  |
| <b>LADDR24</b>  | PC38 | PC37 | <b>LADDR23</b> |
| <b>LADDR22</b>  | PC36 | PC35 | <b>LADDR21</b> |
| <b>LADDR20</b>  | PC34 | PC33 | <b>LADDR19</b> |
| <b>LADDR18</b>  | PC32 | PC31 | <b>LADDR17</b> |
| <b>LADDR16</b>  | PC30 | PC29 | <b>LADDR15</b> |
| <b>LADDR14</b>  | PC28 | PC27 | <b>LADDR13</b> |
| <b>LADDR12</b>  | PC26 | PC25 | <b>LADDR11</b> |
| <b>LADDR10</b>  | PC24 | PC23 | <b>LADDR9</b>  |
| <b>LADDR8</b>   | PC22 | PC21 | <b>LADDR7</b>  |
| <b>LADDR6</b>   | PC20 | PC19 | <b>LADDR5</b>  |
| <b>LADDR4</b>   | PC18 | PC17 | <b>LADDR3</b>  |
| <b>LADDR2</b>   | PC16 | PC15 | <b>LADDR1</b>  |
| <b>nXDRREQ0</b> | PC14 | PC13 | <b>LADDR0</b>  |
| <b>nXDACK0</b>  | PC12 | PC11 | <b>nWAIT</b>   |
| <b>nRESET</b>   | PC10 | PC9  | <b>LnOE</b>    |
| <b>LnWE</b>     | PC8  | PC7  | <b>LnGC84</b>  |
| <b>LnWBE1</b>   | PC6  | PC5  | <b>LnGC82</b>  |
| <b>LnGCS3</b>   | PC4  | PC3  | <b>LnGCS1</b>  |
| <b>EINT9</b>    | PC2  | PC1  | <b>EINT7</b>   |

MICRO2440

|                |      |      |                 |
|----------------|------|------|-----------------|
| MICRO2440A     |      |      |                 |
| <b>VDD5V</b>   | PA1  | PA2  | <b>GND</b>      |
| <b>EINT19</b>  | PA3  | PA4  | <b>EINT18</b>   |
| <b>EINT17</b>  | PA5  | PA6  | <b>EINT16</b>   |
| <b>EINT15</b>  | PA7  | PA8  | <b>EINT14</b>   |
| <b>EINT13</b>  | PA9  | PA10 | <b>EINT11</b>   |
| <b>EINT8</b>   | PA11 | PA12 | <b>EINT6</b>    |
| <b>EINT5</b>   | PA13 | PA14 | <b>EINT4</b>    |
| <b>EINT3</b>   | PA15 | PA16 | <b>EINT2</b>    |
| <b>EINT1</b>   | PA17 | PA18 | <b>EINT0</b>    |
| <b>WP SD</b>   | PA19 | PA20 | <b>SDCLK</b>    |
| <b>SDCMD</b>   | PA21 | PA22 | <b>SDDATA2</b>  |
| <b>SDDATA3</b> | PA23 | PA24 | <b>SDDATA0</b>  |
| <b>SDDATA1</b> | PA25 | PA26 | <b>OM0</b>      |
| <b>LCDVFO</b>  | PA27 | PA28 | <b>M nRESET</b> |
| <b>DN1</b>     | PA29 | PA30 | <b>DP1</b>      |
| <b>DM0</b>     | PA31 | PA32 | <b>DP0</b>      |
| <b>AIN2</b>    | PA33 | PA34 | <b>VDDRTC</b>   |
| <b>AIN0</b>    | PA35 | PA36 | <b>AIN1</b>     |
| <b>L3MODE</b>  | PA37 | PA38 | <b>L3DATA</b>   |
| <b>L3CLOCK</b> | PA39 | PA40 | <b>I2SLRCK</b>  |
| <b>I2SSCLK</b> | PA41 | PA42 | <b>CDCLK</b>    |
| <b>I2SSD0</b>  | PA43 | PA44 | <b>I2SSD0</b>   |
| <b>GPB0</b>    | PA45 | PA46 | <b>GPB1</b>     |
| <b>TXD2</b>    | PA47 | PA48 | <b>RXD2</b>     |
| <b>TXD1</b>    | PA49 | PA50 | <b>RXD1</b>     |
| <b>TXD0</b>    | PA51 | PA52 | <b>RXD0</b>     |
| <b>nCT80</b>   | PA53 | PA54 | <b>nRT80</b>    |
| <b>I2CSDA</b>  | PA55 | PA56 | <b>I2CSCL</b>   |

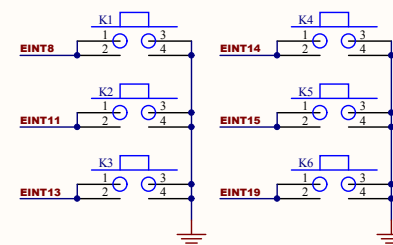
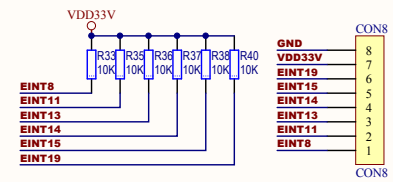
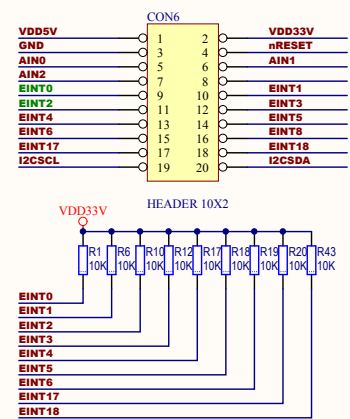
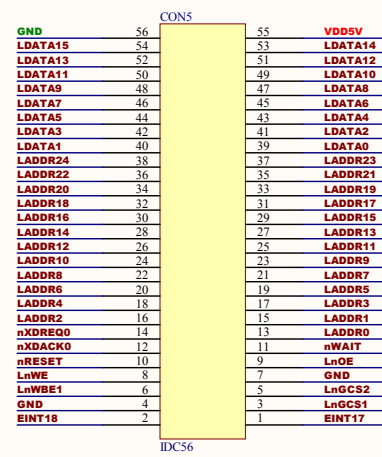
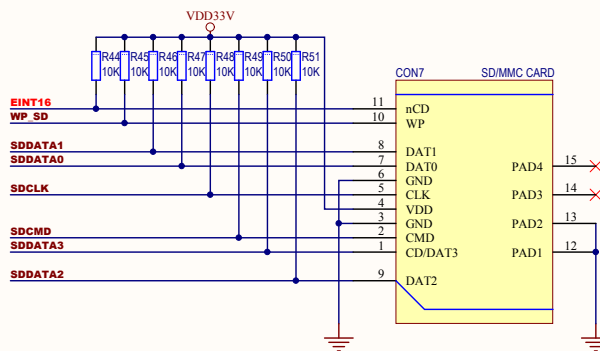
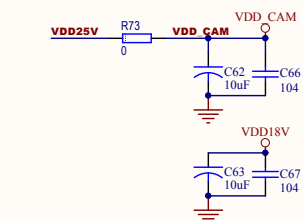
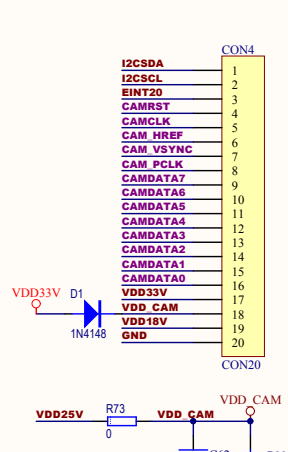
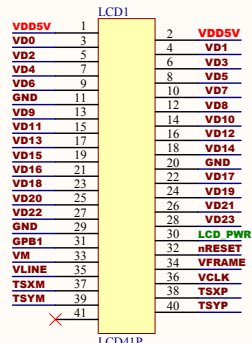
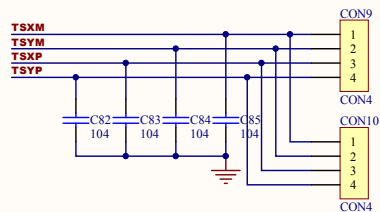
MICRO2440



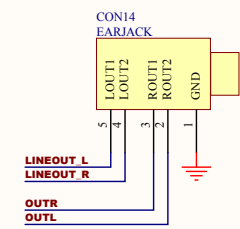
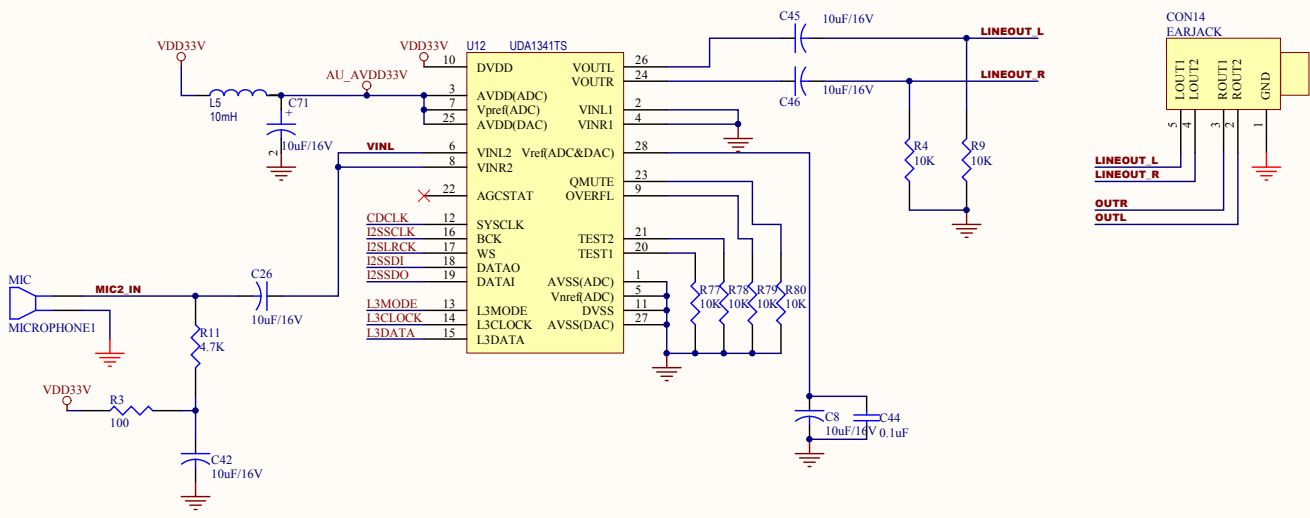
|                  |      |      |                  |
|------------------|------|------|------------------|
| MICRO2440B       |      |      |                  |
| <b>GND</b>       | PB50 | PB49 | <b>VDD5V</b>     |
| <b>CAMRST</b>    | PB48 | PB47 | <b>EINT20</b>    |
| <b>CAM_VSYNC</b> | PB46 | PB45 | <b>CAM_VSYNC</b> |
| <b>CAM_PCLK</b>  | PB44 | PB43 | <b>CAMCLK</b>    |
| <b>CAMDATA2</b>  | PB42 | PB41 | <b>CAMDATA1</b>  |
| <b>CAMDATA4</b>  | PB40 | PB39 | <b>CAMDATA3</b>  |
| <b>CAMDATA6</b>  | PB38 | PB37 | <b>CAMDATA5</b>  |
| <b>LEND</b>      | PB36 | PB35 | <b>CAMDATA7</b>  |
| <b>VLINE</b>     | PB34 | PB33 | <b>VCLK</b>      |
| <b>VM</b>        | PB32 | PB31 | <b>VFRAME</b>    |
| <b>VD1</b>       | PB30 | PB29 | <b>LCD_PWR</b>   |
| <b>VD3</b>       | PB28 | PB27 | <b>VDD</b>       |
| <b>VD5</b>       | PB26 | PB25 | <b>VD2</b>       |
| <b>VD7</b>       | PB24 | PB23 | <b>VD6</b>       |
| <b>VD9</b>       | PB22 | PB21 | <b>VD8</b>       |
| <b>VD11</b>      | PB20 | PB19 | <b>VD10</b>      |
| <b>VD13</b>      | PB18 | PB17 | <b>VD12</b>      |
| <b>VD15</b>      | PB16 | PB15 | <b>VD14</b>      |
| <b>VD17</b>      | PB14 | PB13 | <b>VD16</b>      |
| <b>VD19</b>      | PB12 | PB11 | <b>VD18</b>      |
| <b>VD21</b>      | PB10 | PB9  | <b>VD20</b>      |
| <b>VD23</b>      | PB8  | PB7  | <b>VD22</b>      |
| <b>TSK40</b>     | PB6  | PB5  | <b>TSK40</b>     |
| <b>TSVP</b>      | PB4  | PB3  | <b>TSYM</b>      |

MICRO2440B  
MICRO2440

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|--------------|---|----------|--|
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| Size         | Number  | Revision |  |
| B            | Translation by S. Watterott 01.07.2009            |          |  |
| Date:        | 05.07.2009  | Sheet of |  |
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| B            | Translation by S. Watterott                    | 01.07.2009 |    |
| Date:        | 05.07.2009                                     | Sheet      | of |
| File:        | D:\Dokumente und Einstellungen\...04-Audio\... | Drawn By:  |    |

