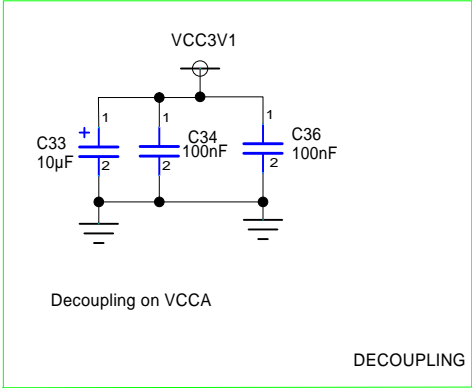
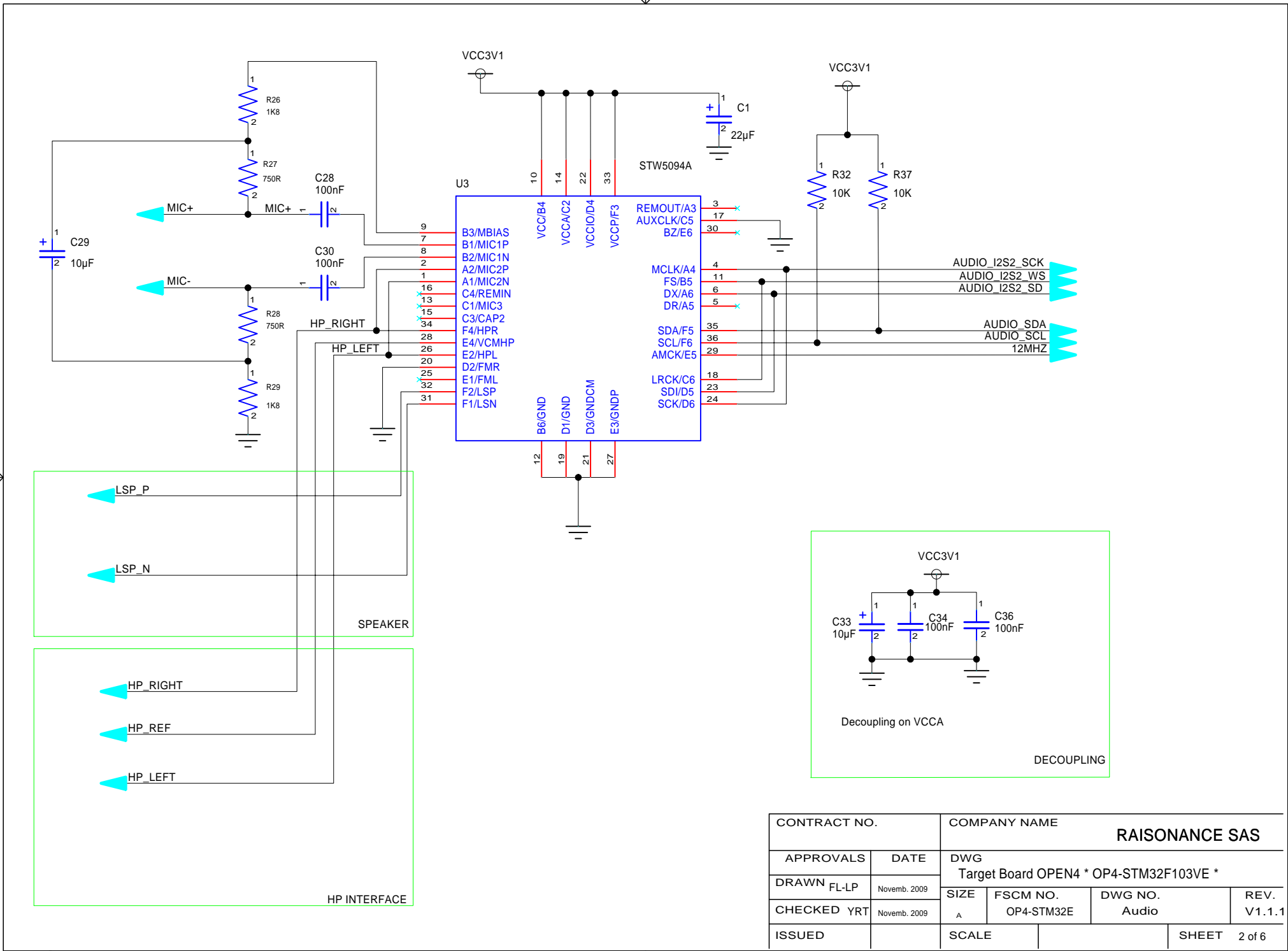
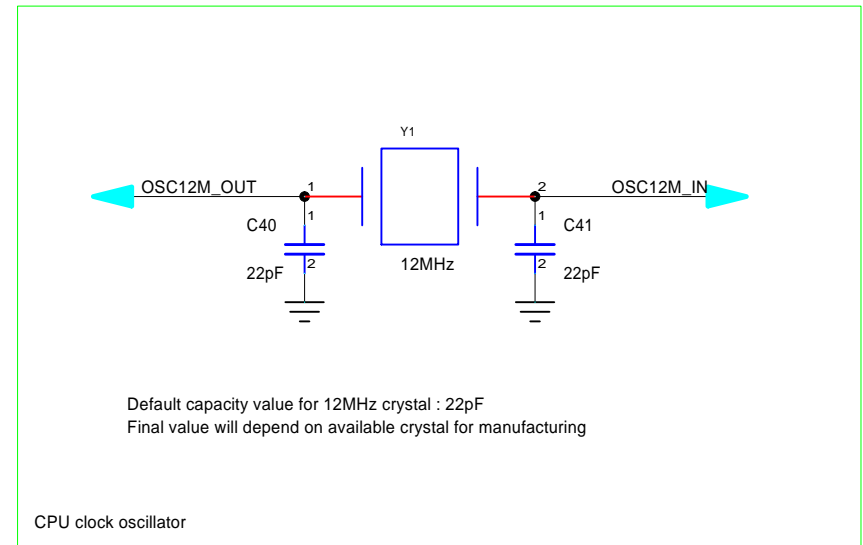
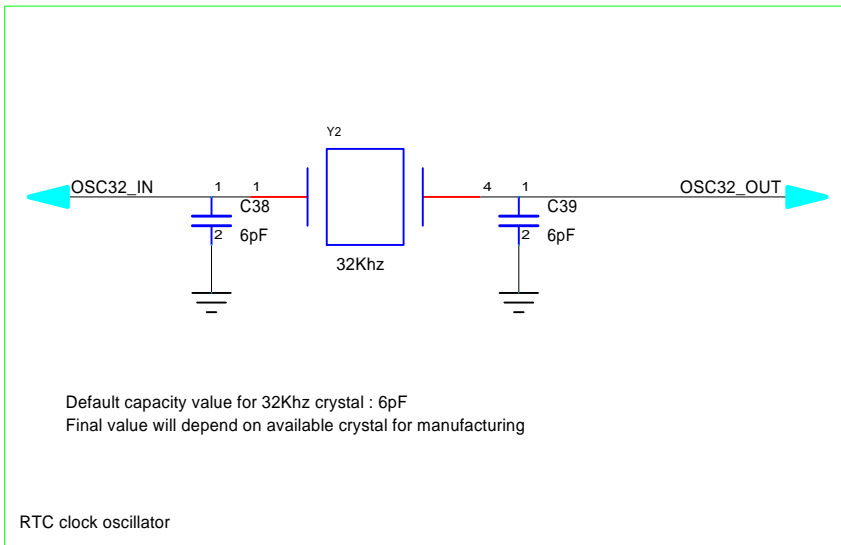


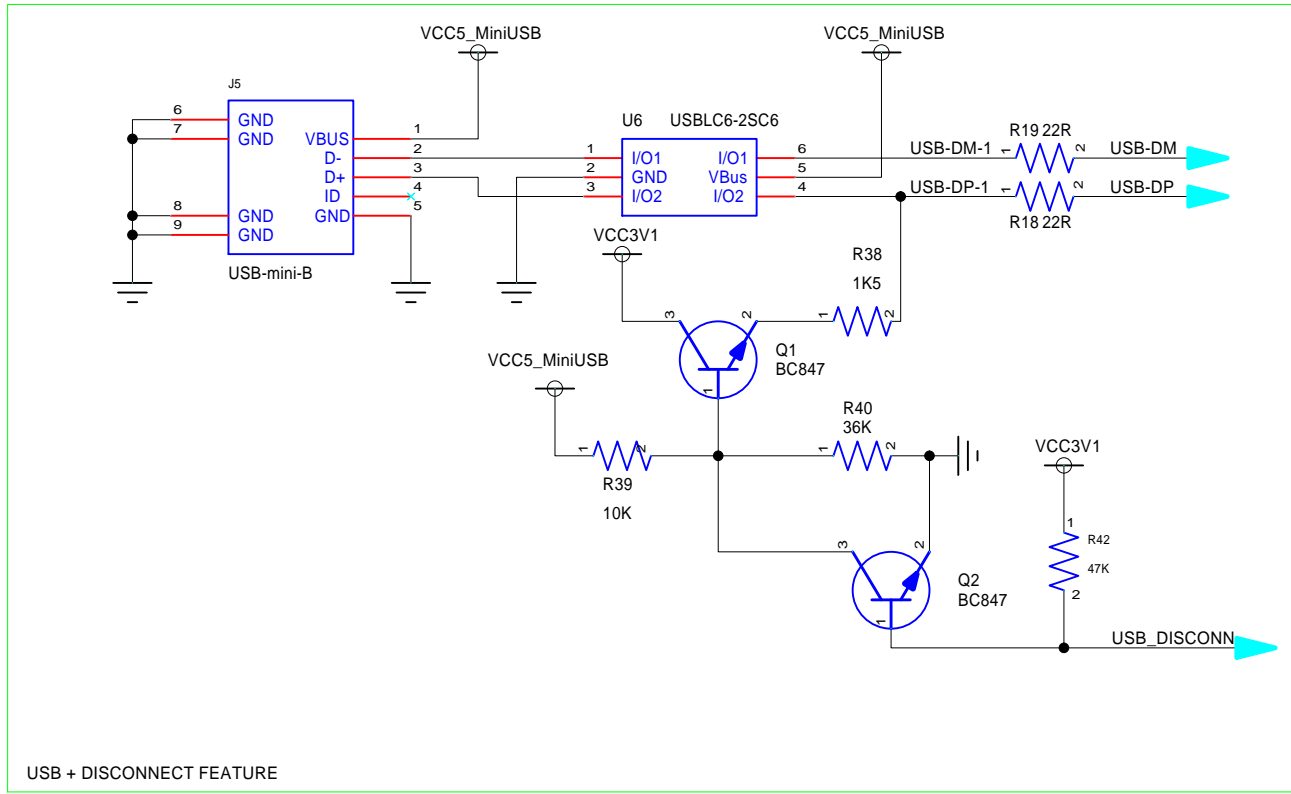
CONTRACT NO.		COMPANY NAME <b>RAISONANCE SAS</b>			
APPROVALS	DATE	DWG Target Board OPEN4 * OP4-STM32F103VE *			
DRAWN FL-LP	Novemb. 2009	SIZE A	FSCM NO. OP4-STM32E	DWG NO. Microcontrôleur	REV. V1.1.1
CHECKED YRT	Novemb. 2009	SCALE		SHEET 1 of 6	
ISSUED					



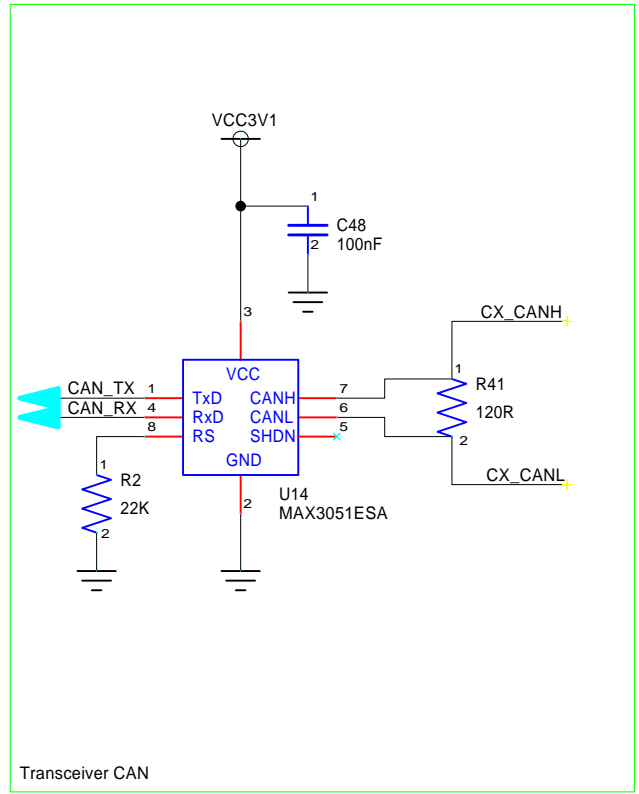
CONTRACT NO.		COMPANY NAME <b>RAISONANCE SAS</b>			
APPROVALS	DATE	DWG Target Board OPEN4 * OP4-STM32F103VE *			
DRAWN FL-LP	Novemb. 2009	SIZE	FSCM NO.	DWG NO.	REV.
CHECKED YRT	Novemb. 2009	A	OP4-STM32E	Audio	V1.1.1
ISSUED		SCALE		SHEET 2 of 6	



CONTRACT NO.		COMPANY NAME <b>RAISONANCE SAS</b>			
APPROVALS	DATE	DWG Target Board OPEN4 * OP4-STM32F103VE *			
DRAWN FL-LP	Novemb. 2009	SIZE	FSCM NO.	DWG NO.	REV.
CHECKED YRT	Novemb. 2009	A	OP4-STM32E	Clock source	V1.1.1
ISSUED		SCALE		SHEET	3 of 6

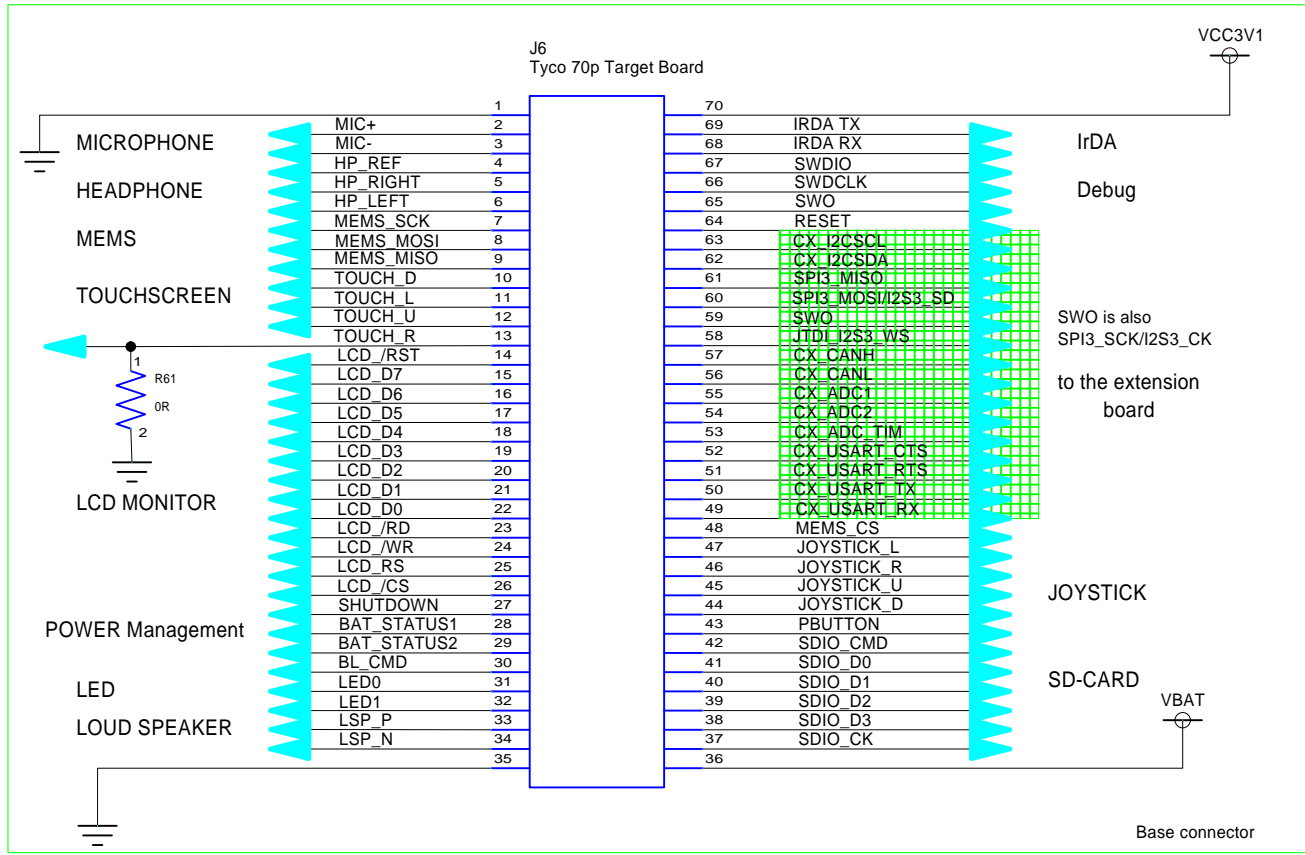


USB + DISCONNECT FEATURE

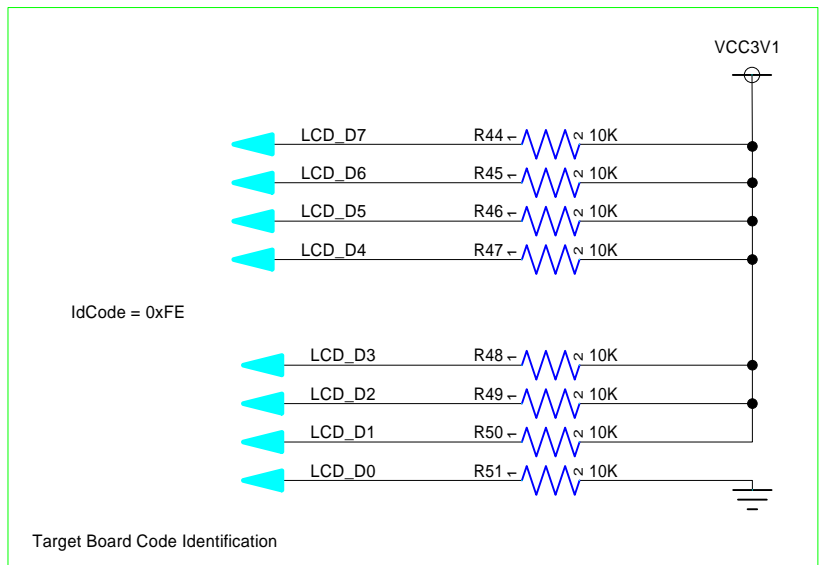


Transceiver CAN

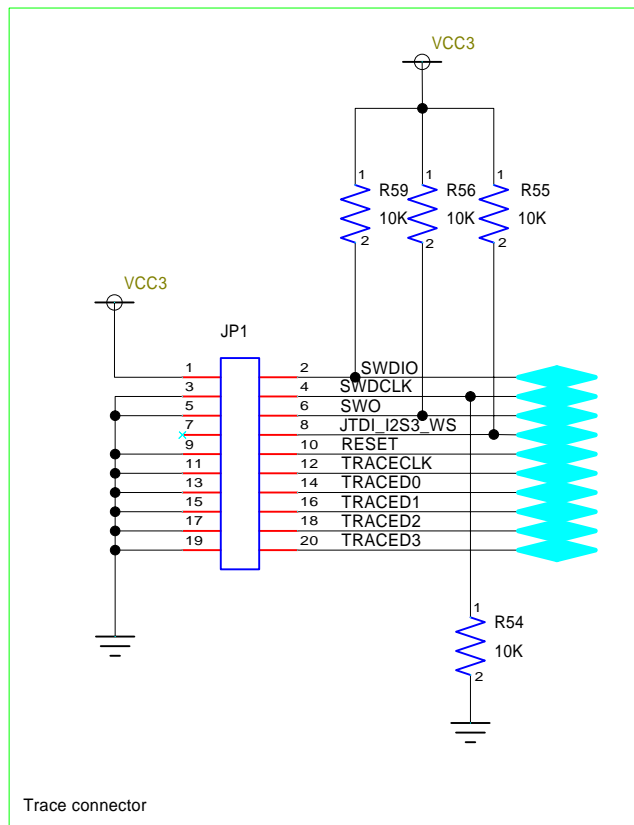
CONTRACT NO.		COMPANY NAME <b>RAISONANCE SAS</b>			
APPROVALS	DATE	DWG Target Board OPEN4 * OP4-STM32F103VE *			
DRAWN FL-LP	Novemb. 2009	SIZE	FSCM NO.	DWG NO.	REV.
CHECKED YRT	Novemb. 2009	A	OP4-STM32E	USB Peripherals CAN	V1.1.1
ISSUED		SCALE		SHEET 4 of 6	



Extension Board connector			
Daughter Board printed name:	JP2 Extension connector:	J6 Target Board connector:	Target Board signals:
V2V8	1	70	VCC3V1
GND	2	1	GND
SCL	3	63	CX_I2CSCL
SDA	4	62	CX_I2CSDA
MISO	5	61	SPI3_MISO
SD	6	60	SPI3_MOSI/SD
SCK	7	59	SWO
WS	8	58	JTDL_I2S3_WS
CANH	9	57	CX_CANH
CANL	10	56	CX_CANL
ADC1	11	55	CX_ADC1
ADC2	12	54	CX_ADC2
A_TIM	13	53	CX_ADC_TIM
CTS	14	52	CX_USART_CTS
RTS	15	51	CX_USART_RTS
TX	16	50	CX_USART_TX
CK	17	43	P_BUTTON
RX	18	49	CX_USART_RX
VEXT	19	36	VBAT
GND	20	35	GND



CONTRACT NO.		COMPANY NAME			
		<b>RAISONANCE SAS</b>			
APPROVALS	DATE	DWG			
DRAWN FL-LP	Novemb. 2009	Target Board OPEN4 * OP4-STM32F103VE *			
CHECKED YRT	Novemb. 2009	SIZE	FSCM NO.	DWG NO.	REV.
		A	OP4-STM32E	Base connector	V1.1.1
ISSUED		SCALE		SHEET 5 of 6	



CONTRACT NO.		COMPANY NAME <b>RAISONANCE SAS</b>			
APPROVALS	DATE	DWG Target Board OPEN4 * OP4-STM32F103VE *			
DRAWN FL-LP	Novemb. 2009	SIZE	FSCM NO.	DWG NO.	REV.
CHECKED YRT	Novemb. 2009	A	OP4-STM32E	Trace connector	V1.1.1
ISSUED		SCALE		SHEET	6 of 6