


**Notes:**

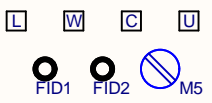
\*1) Configure unused PIO pins as output driving '0' and disable pullup; do this for PIOs not bonded out too (PIO2-5)

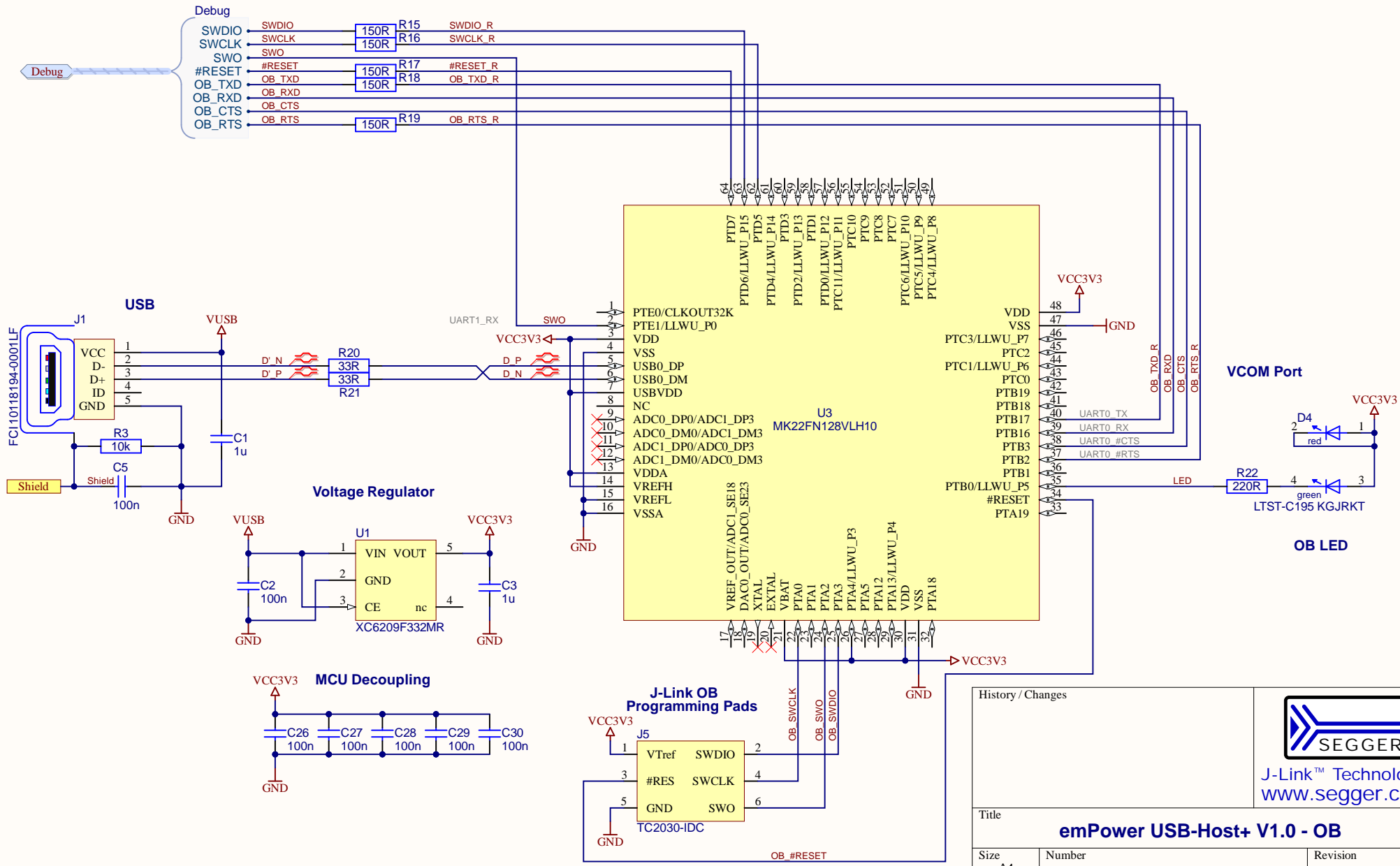
\*2) Boot from int. Flash: P0\_4[5][6] = '111' (internally pulled high)


\*3) LPC54605 supports SWD for debugging only

Unless otherwise specified: Ceramic capacitors are of type X5R/X7R or COG/NP0 with a voltage rating of at least 6.3V; Resistors are 1% tolerance types

History / Changes		 J-Link™ Technology www.segger.com	
(Initial version)			
Title			
<b>emPower USB-Host+ V1.0 - MCU</b>			
Size	A4	Number	Revision
Date:	190108	Sheet	1 / 2
File:	01_emPowerUSBHostPlus_MCU_V1.0.Sch	Drawn:	VK





History / Changes		 <b>J-Link™ Technology</b> <a href="http://www.segger.com">www.segger.com</a>
Title		
<b>emPower USB-Host+ V1.0 - OB</b>		
Size	Number	Revision
A4	-	-
Date: 190108	Sheet 2 / 2	
File: 02_emPowerUSBHostPlus_OB_V1.0.SchDoc	Drawn: VK	