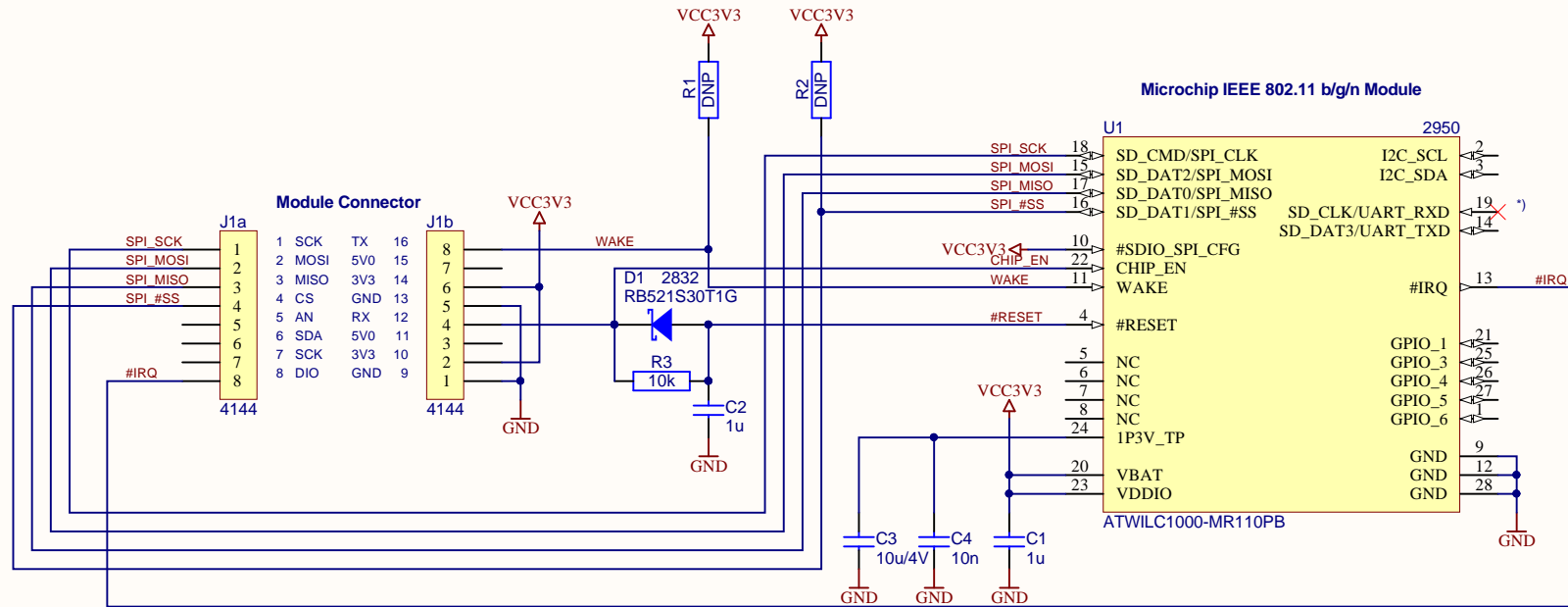


A

B

C

D



**History / Changes**


- Rev. A: - Connected CHIP\_EN to J1.4  
- Added low pass to #RESET
- Rev. B: - Added 10u/10n to 1P3V\_TP, increased board length

**NOTES:**

\*) Enable internal pull-up of module to avoid floating

**Unless otherwise specified:**

- Ceramic capacitors are of type X5R/X7R or C0G/NP0 with a voltage rating of at least 4V (100n at least 16V)
- Resistors are 1% tolerance types

History / Changes		 J-Link™ Technology www.segger.com
(see left)		
Title: <b>emPower WiFi Module</b>		
Size: A4	Number: -	Revision: <b>Rev.B</b>
Date: 190716		Sheet 1 / 1
File: emPower_WiFi_RevB.SchDoc		Drawn: VK

