

JLCJLCJLCJLC

H2

VCC

GND

TXD

RXD

RTS#

CTS#

[R2]

PWR

POWER1

TXD

TXD1

RXD

RXD1

[R4]

[Q3]

[Q1]

[Q2]

[D1]

[D2]

H1

U1

P1

H3

5V

VCC

3V3

[C5]

[C3]

[R3]

[C4]

[R7]

[R6]

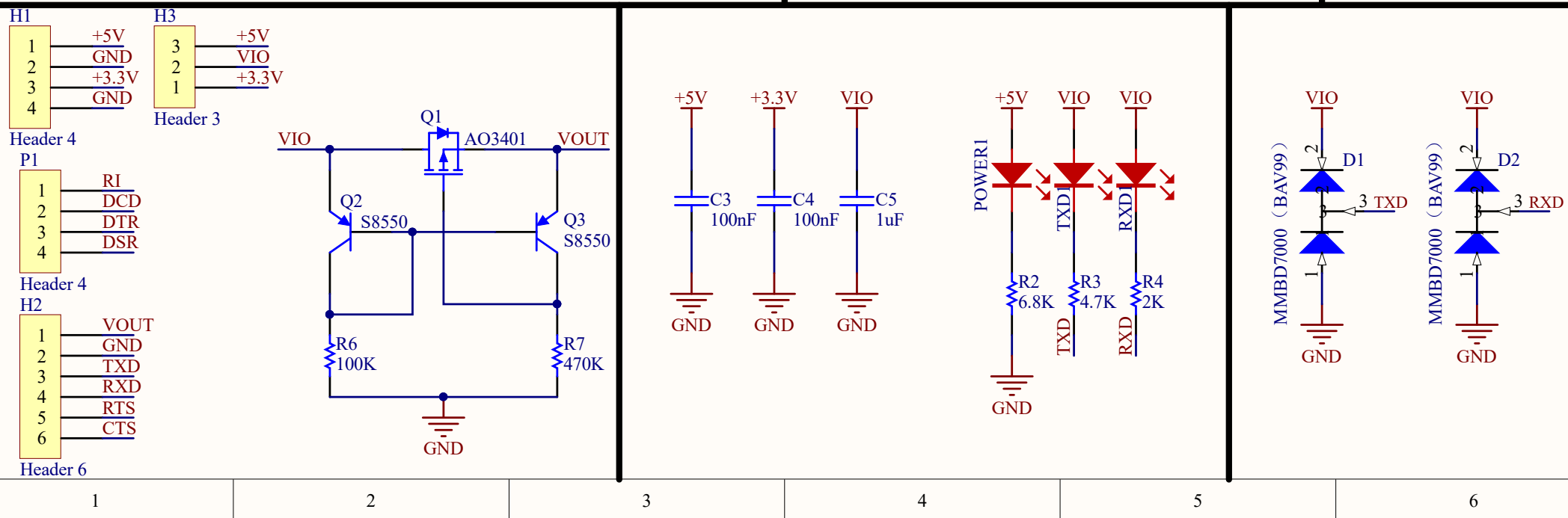
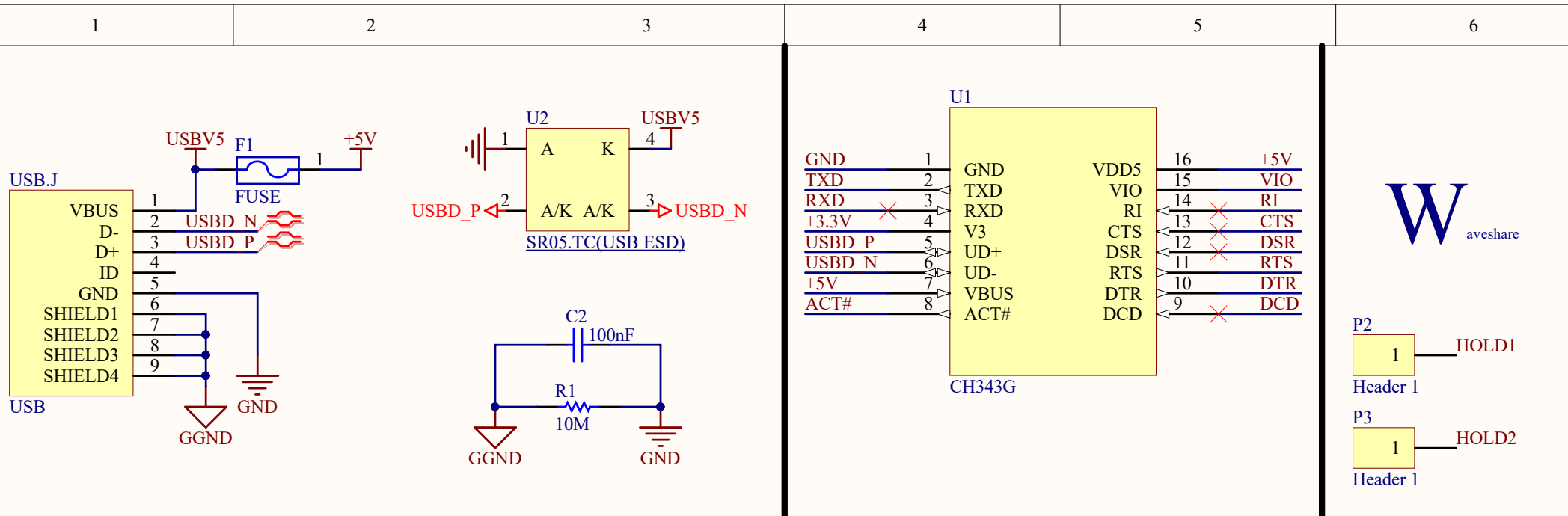
F1

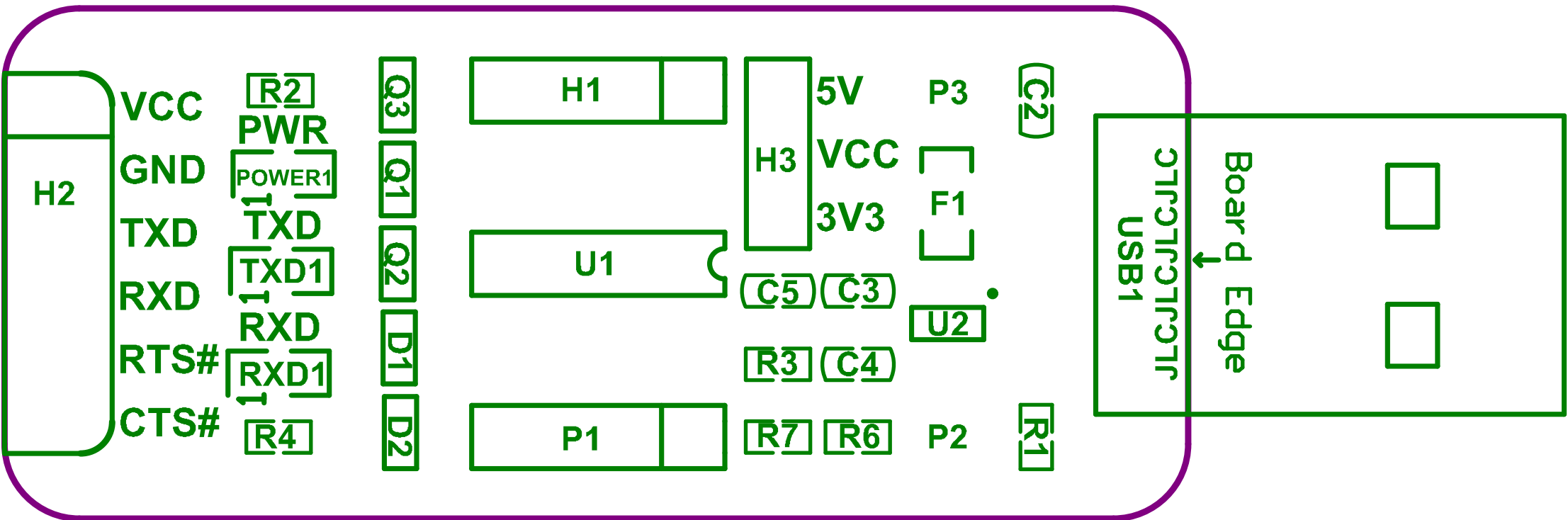
U2

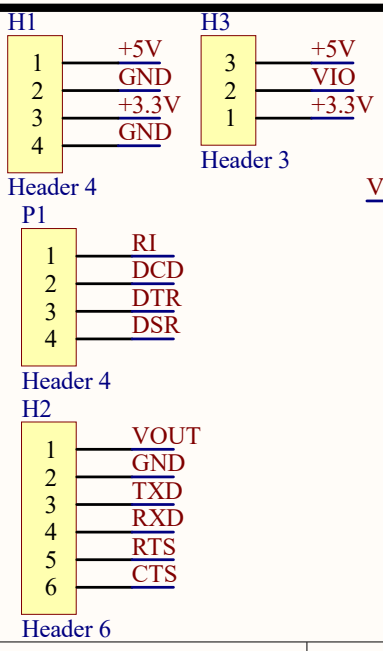
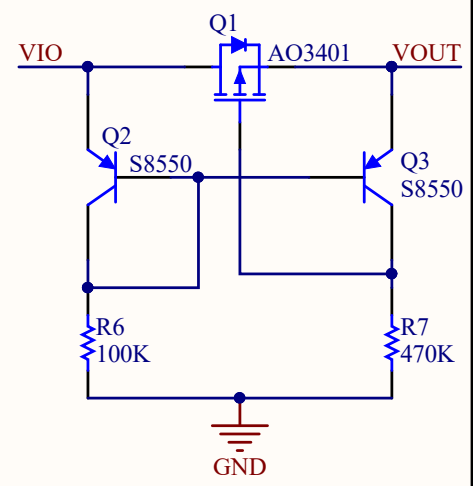
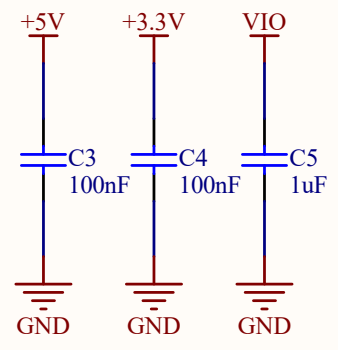
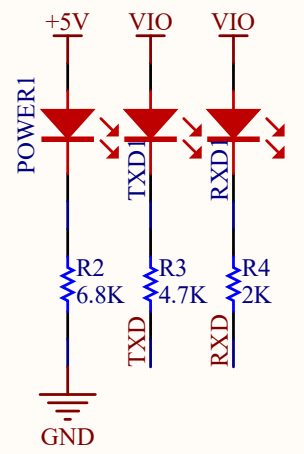
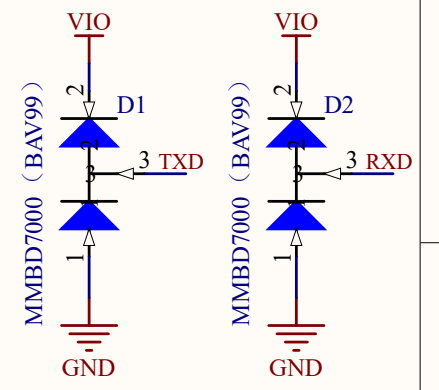
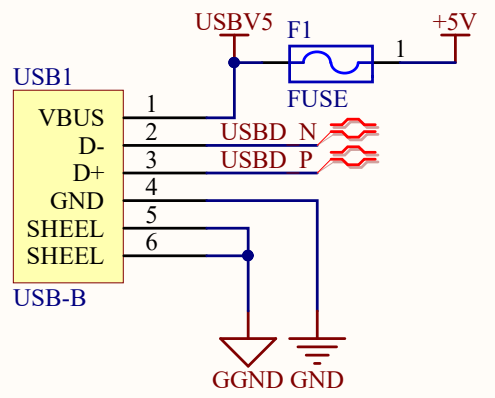
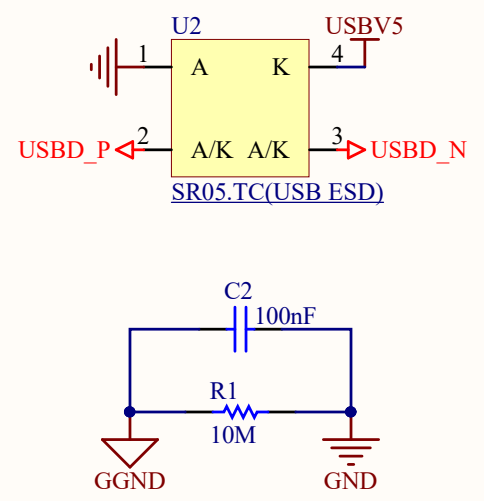
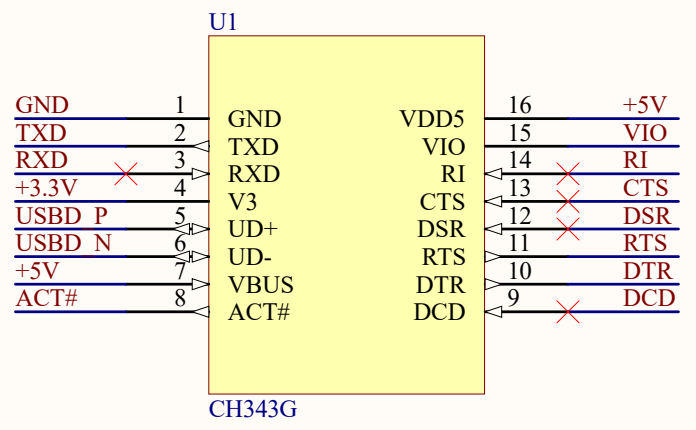
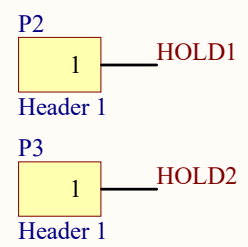
[C2]

USB1

[R1]







JLCJLCJLCJLC  
H2

VCC  
GND  
TXD  
RXD  
RTS#  
CTS#

[R2]  
PWR  
POWER1  
TXD  
TXD1  
RXD  
RXD1  
R4

Q3  
Q1  
Q2  
D1  
D2



H1



H3



U1



P1

5V  
VCC  
3V3

P3  
F1  
U2

(C5) (C3)

(R3) (C4)

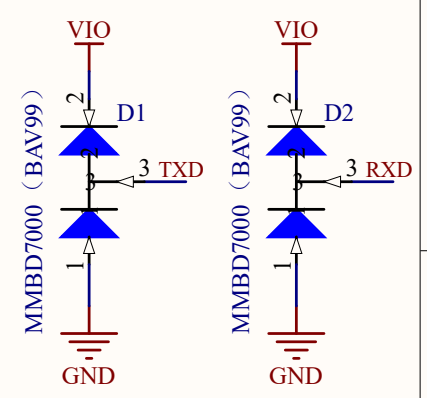
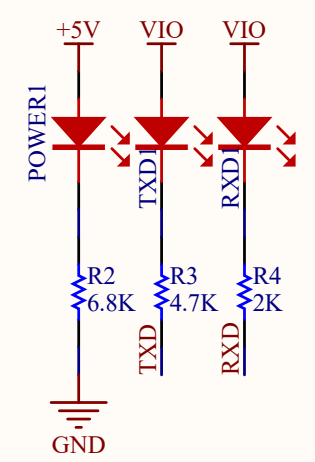
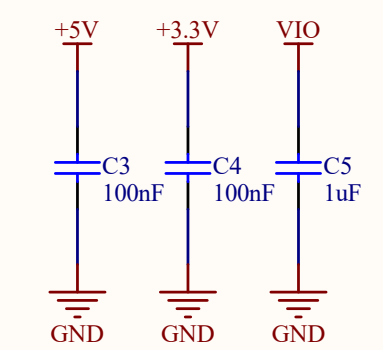
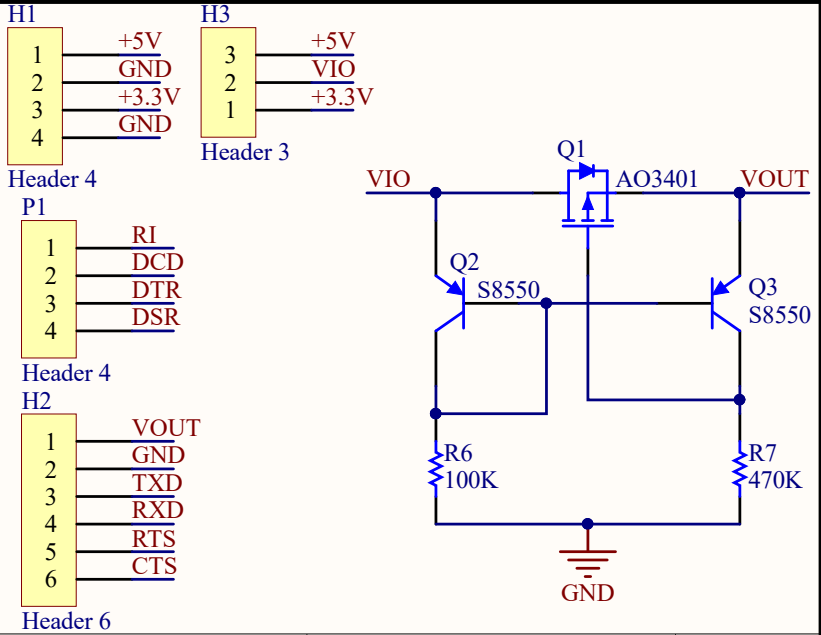
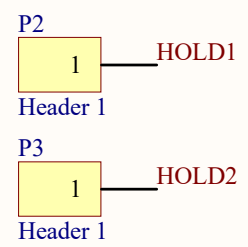
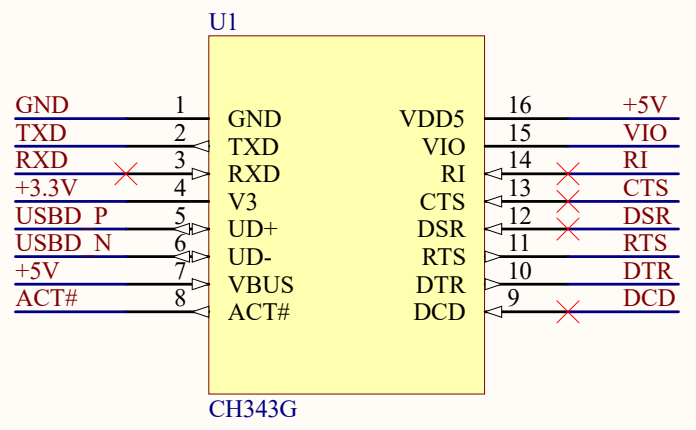
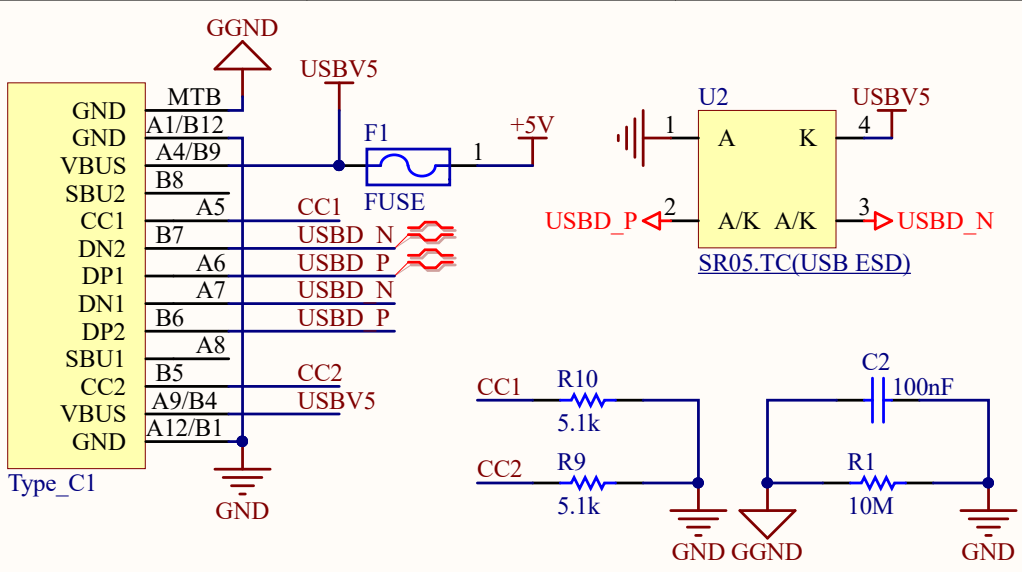
(R7) (R6)

(C2)

USB.J

(R1)

P2



JLCJLCJLCJLC

VCC

GND

TXD

RXD

RTS#

CTS#

[R2] PWR  
[POWER1]  
1 [TXD]  
[TXD1]  
1 [RXD]  
[RXD1]  
1 [R4]

[Q3]  
[Q1]  
[Q2]  
[D1]  
[D2]



5V  
VCC  
3V3

