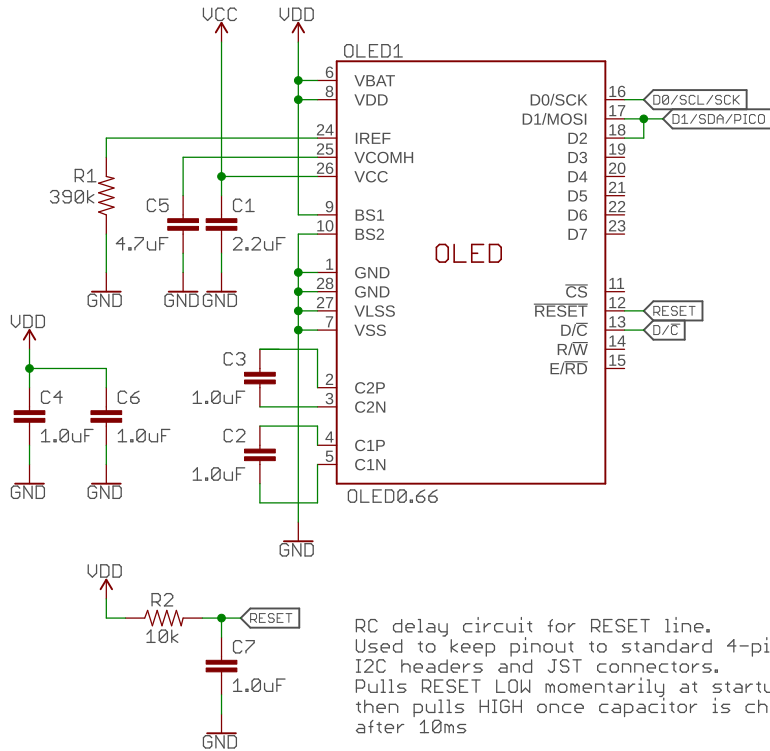


## Micro OLED - SSD1306

VDD: 1.65V - 3.3V  
 VBAT: 3.3V - 4.5V  
 VCC: 7V - 7.5V (Internal)



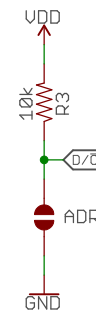
RC delay circuit for RESET line.  
 Used to keep pinout to standard 4-pin I2C headers and JST connectors.  
 Pulls RESET LOW momentarily at startup then pulls HIGH once capacitor is charged after 10ms

## Power Considerations

The OLED requires a 1.65-3.3V supply for its logic circuits (VDD) and a 7-7.5V supply for its display circuitry (VCC). Fortunately, it features a charge-pump boost converter to generate a 7V supply (VCC) from 3.3-4.2V. The charge-pump input voltage is taken from the VBAT line.

VDD current < 300 uA  
 VCC current (Internally generated) = 5.8-20.9mA  
 VCC current (Externally supplied) = 1.7-6.9mA

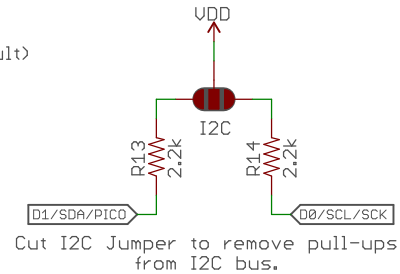
## Jumpers + Interface selection



Interface	BS1	BS2
SPI	0	0
I2C	1	0
8-bit (6800)	0	1
8-bit (8080)	1	1

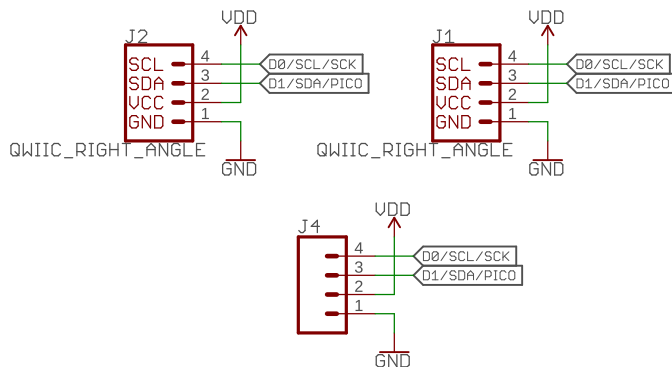
The D/C jumper should be open if SPI or parallel interfaces are used. In those interfaces this pin determines whether incoming signals are data or command.

7-bit unshifted Address: 0x3D (Default)  
 Alternate Address: 0x3C  
 Close jumper for Alternate Address



Cut I2C Jumper to remove pull-ups from I2C bus.

## Connectors



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[Special Instructions](#)

TITLE: Qwiic\_OLED\_Breakout

Design by: Joel Bartlett

Revision By: Andy England, Elias Santistevan

REV:

v11

Date: 8/18/2023 12:46 PM

Sheet: 1/1