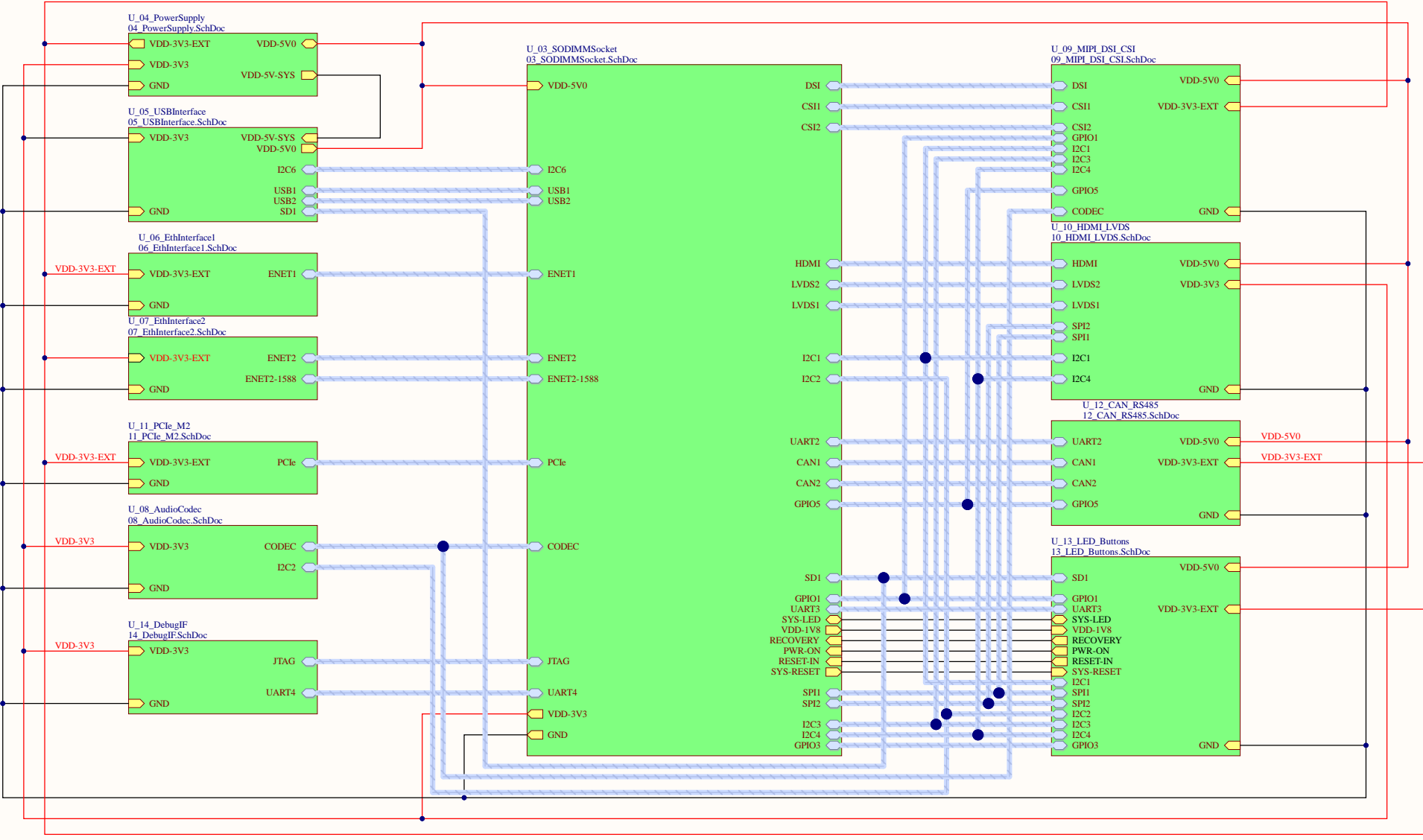
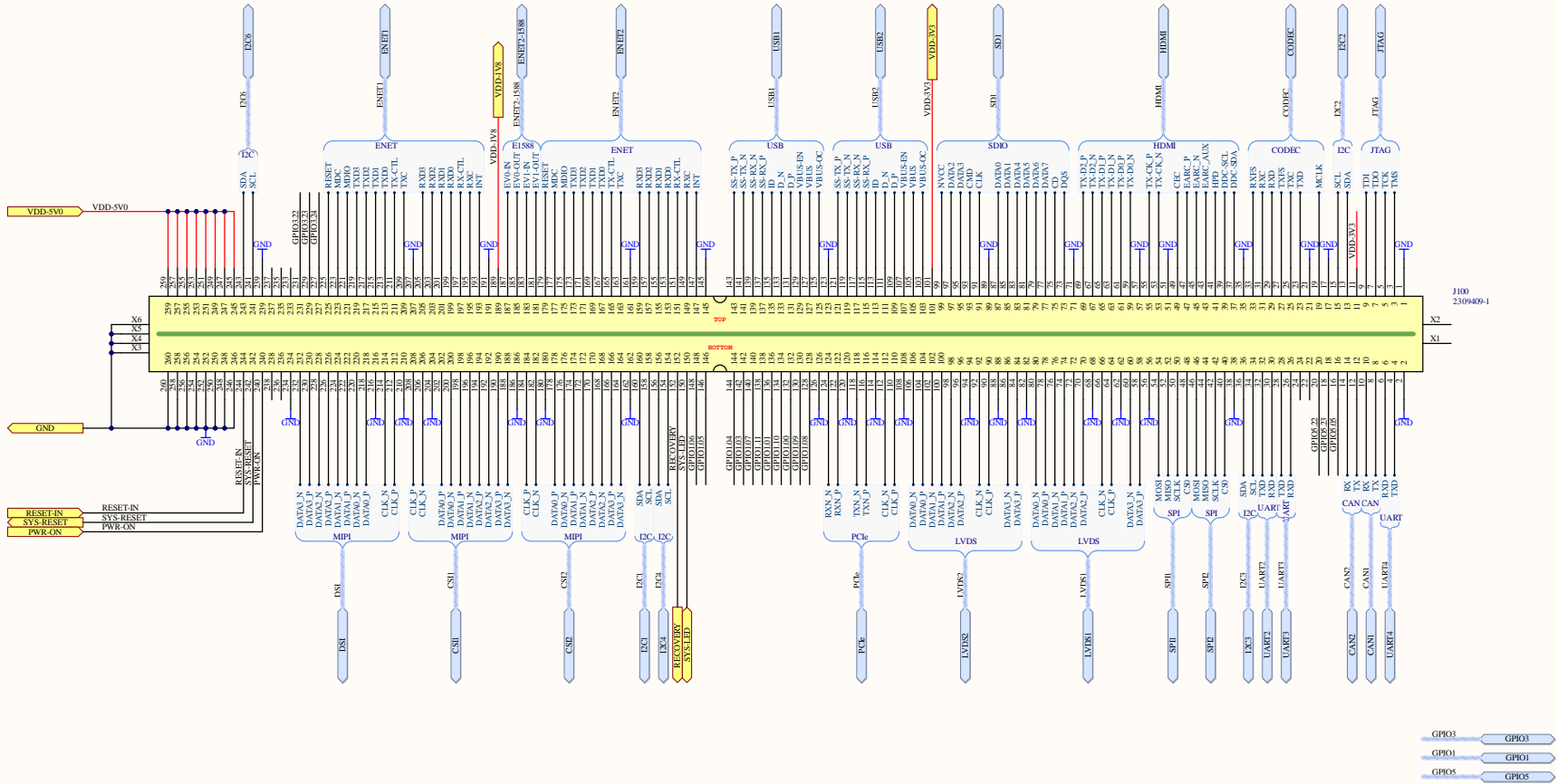


VisionSOM-8MP top level sheet



Title		
Size	Number	Revision
A3		
Date:	3.15.2023	Sheet of
File:	D:\Projekty\02_PowerTree.SchDoc	Drawn By:

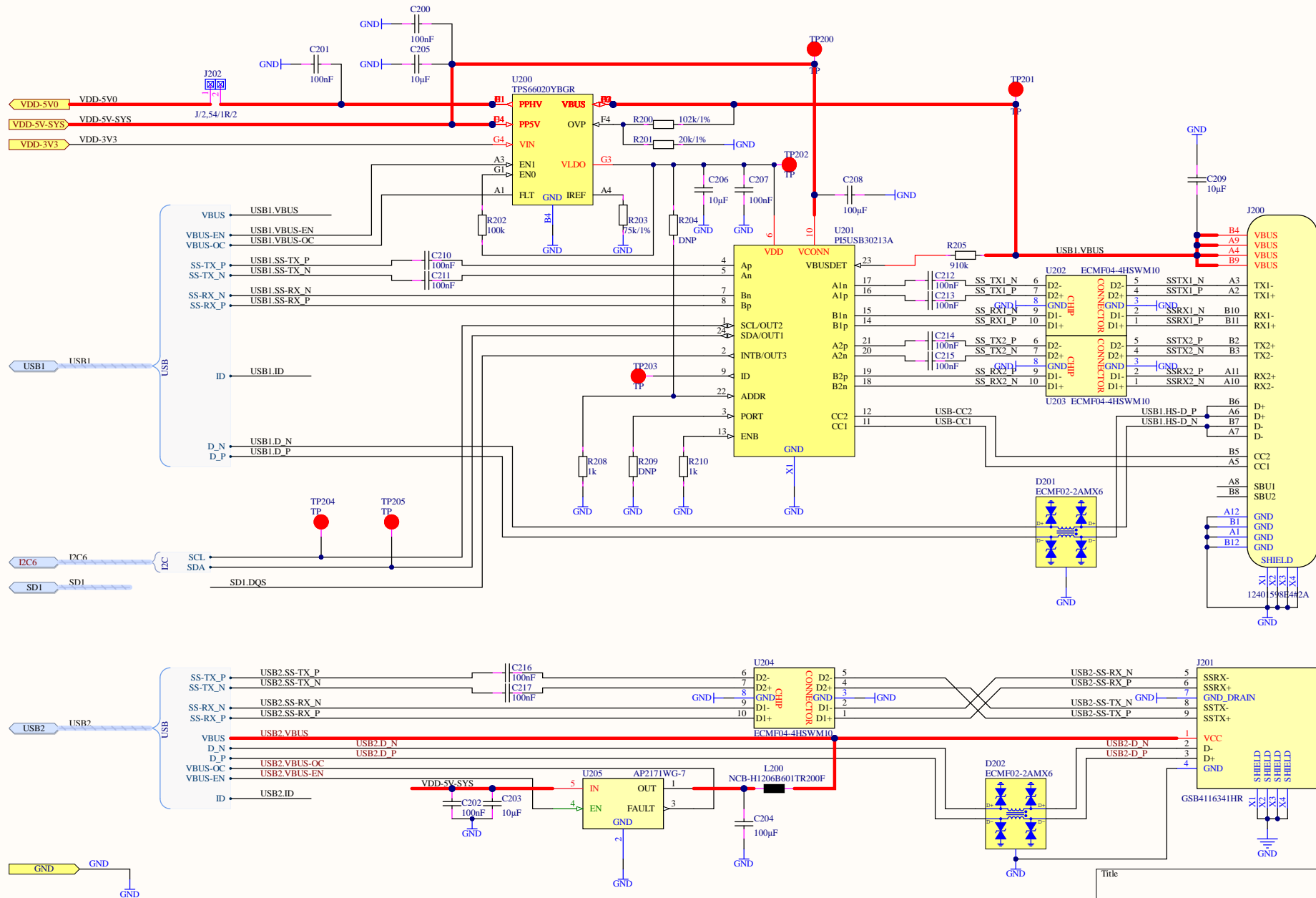
SODIMM connector



A

D

USB interfaces

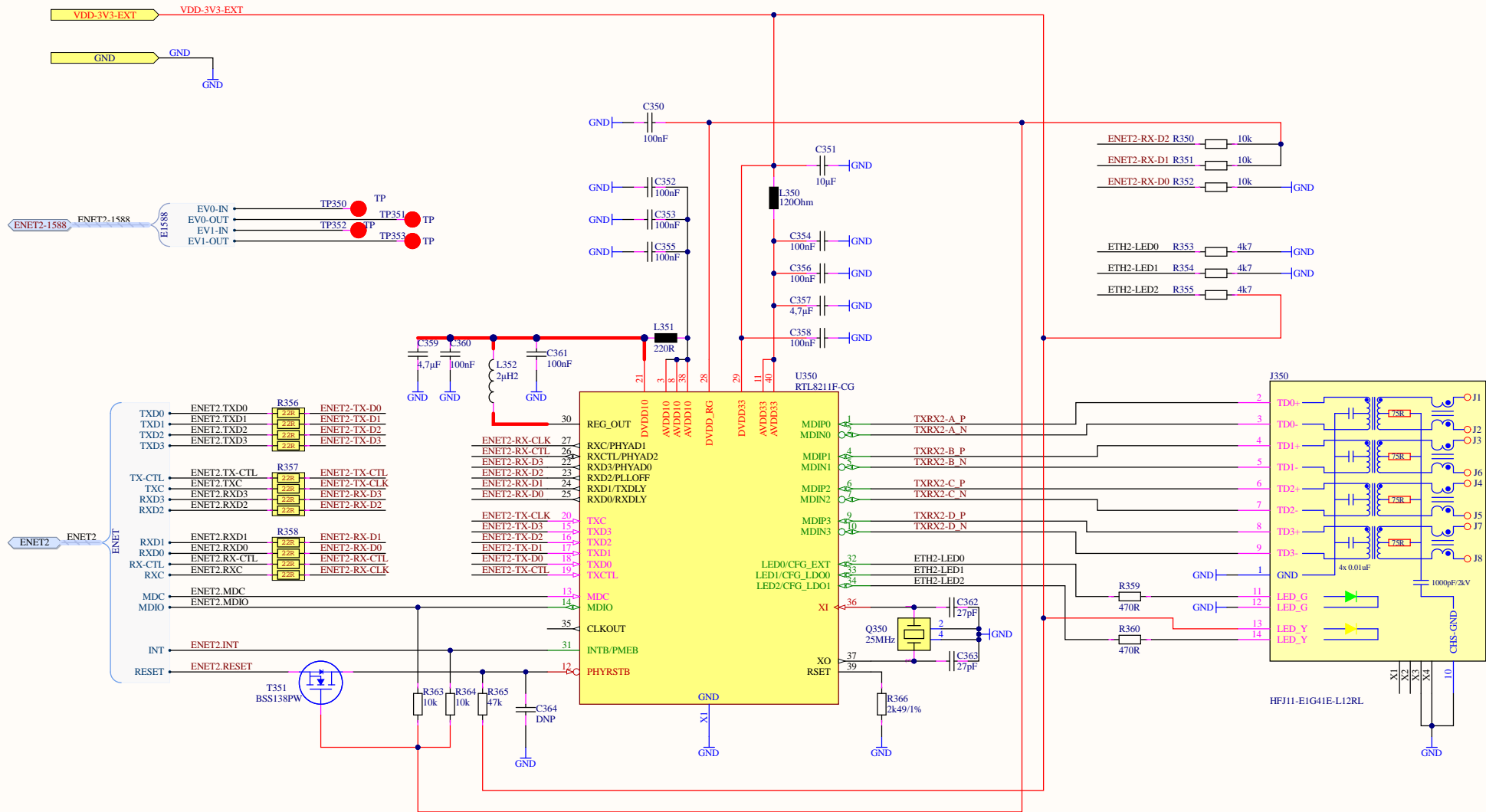


Title		
Size	Number	Revision
A3		
Date:	3.15.2023	Sheet of
File:	D:\Projekty\05_USBInterface.SchDoc	Drawn By:

The schematic illustrates the ENET1 interface circuit. The central component is the U300 RTL8211F-CG Ethernet PHY. It is powered by VDD-3V3-EXT and GND. The schematic shows various signal lines including TXD0-TXD3, RXD1-RXD3, TX-CTL, RX-CTL, MDC, MDIO, INT, and RESET. It also includes a crystal oscillator (Q300) and a reset circuit (T300). The PHY is connected to a transformer (J300) for Ethernet signaling. Various passive components like resistors (R300-R316) and capacitors (C300-C314) are shown for impedance matching and decoupling.

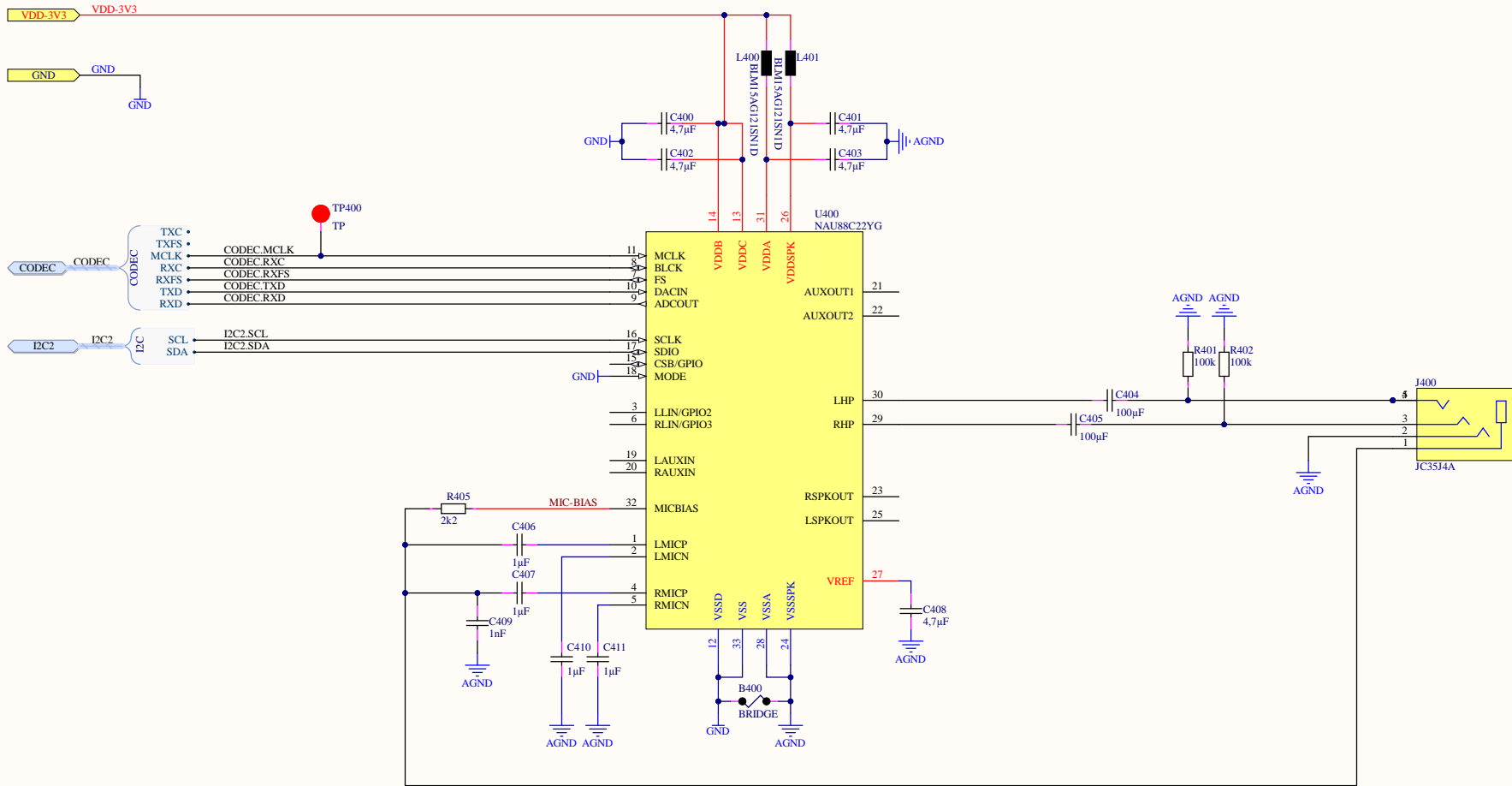
Title			
Size A3	Number		Revision
Date:	3.15.2023	Sheet of	
File:	D:\Projekty\...06_EthInterface1.SchDoc	Drawn By:	

Gigabit Ethernet 2



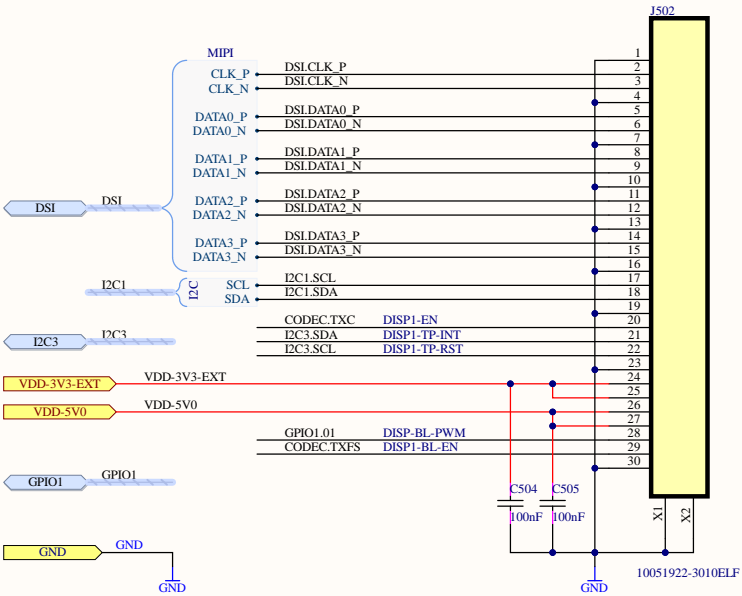
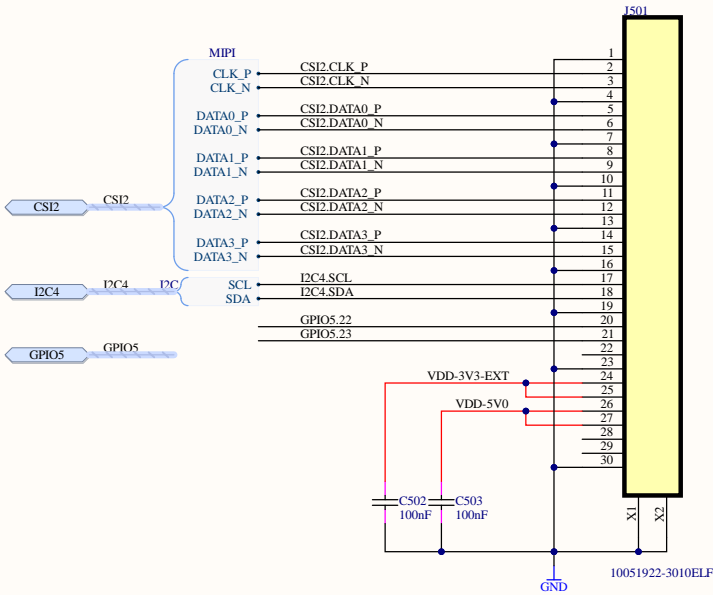
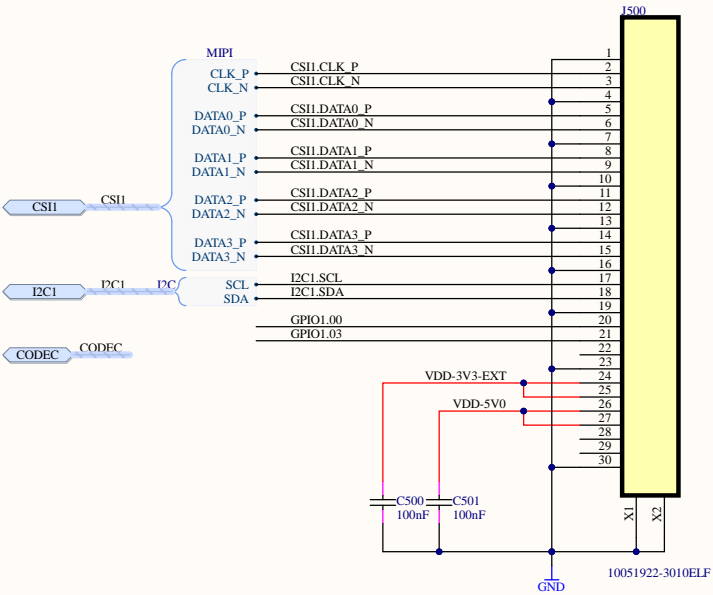
Title		
Size	Number	Revision
A3		
Date:	3.15.2023	Sheet of
File:	D:\Projekty\07_EthInterface2.SchDoc	Drawn By:

AUDIO-CODEC



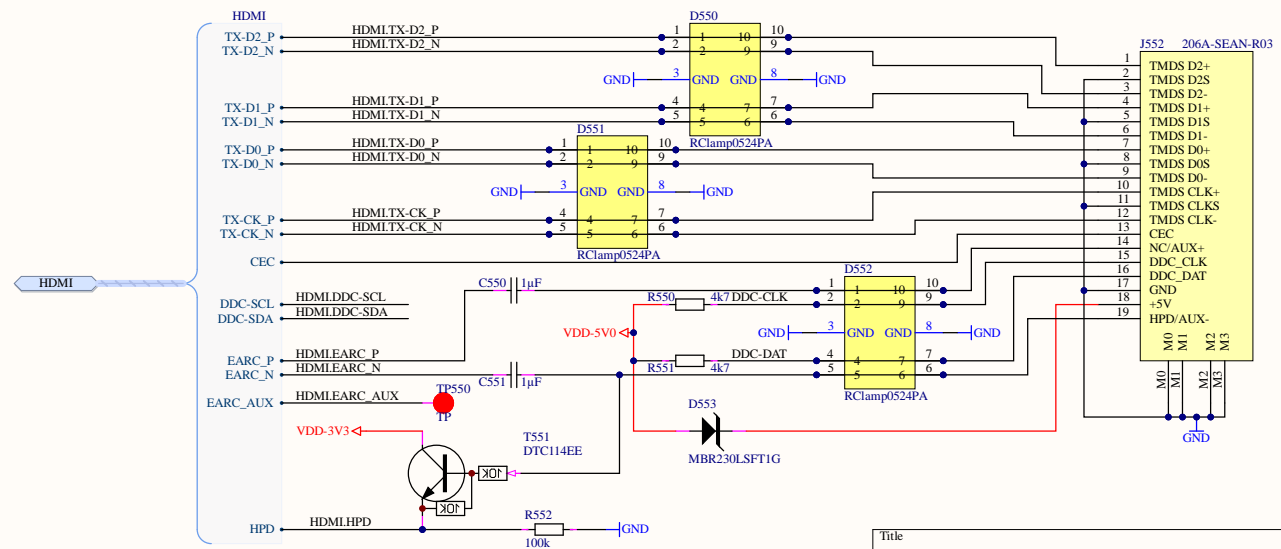
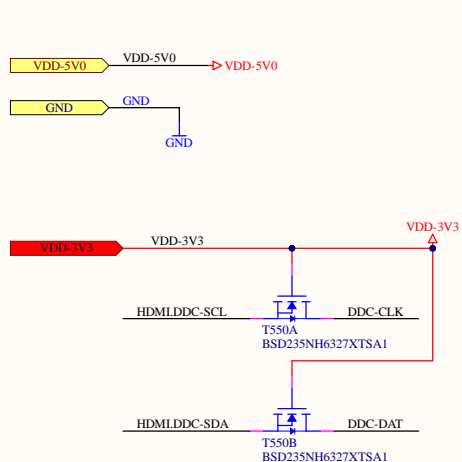
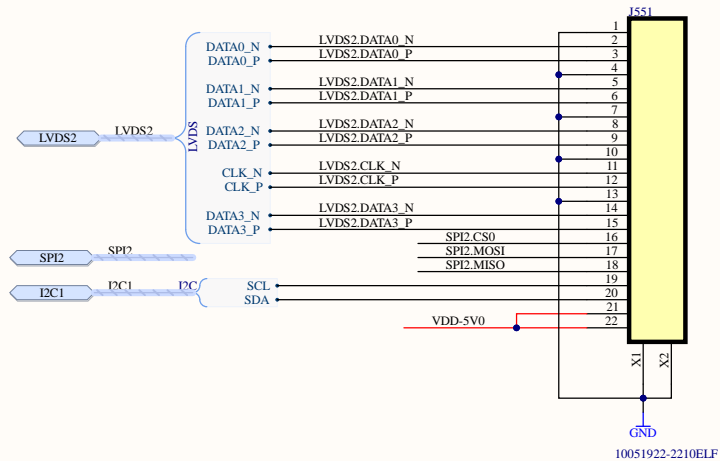
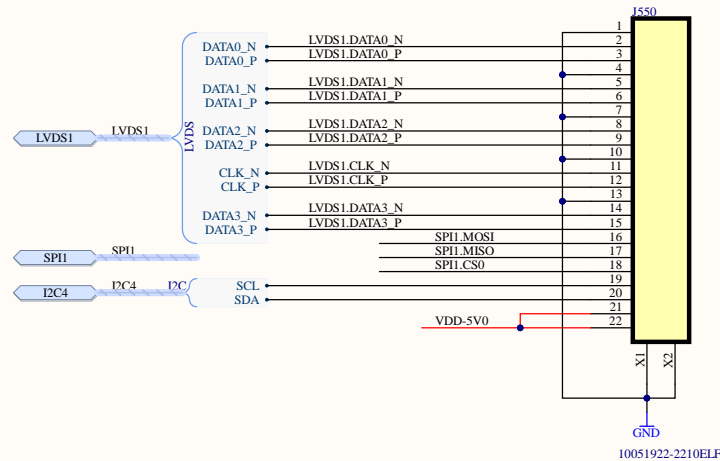
Title		
Size	Number	Revision
A3		
Date:	3.15.2023	Sheet of
File:	D:\Projekty\...08_AudioCodec.SchDoc	Drawn By:

MIPI Display/Camera IF



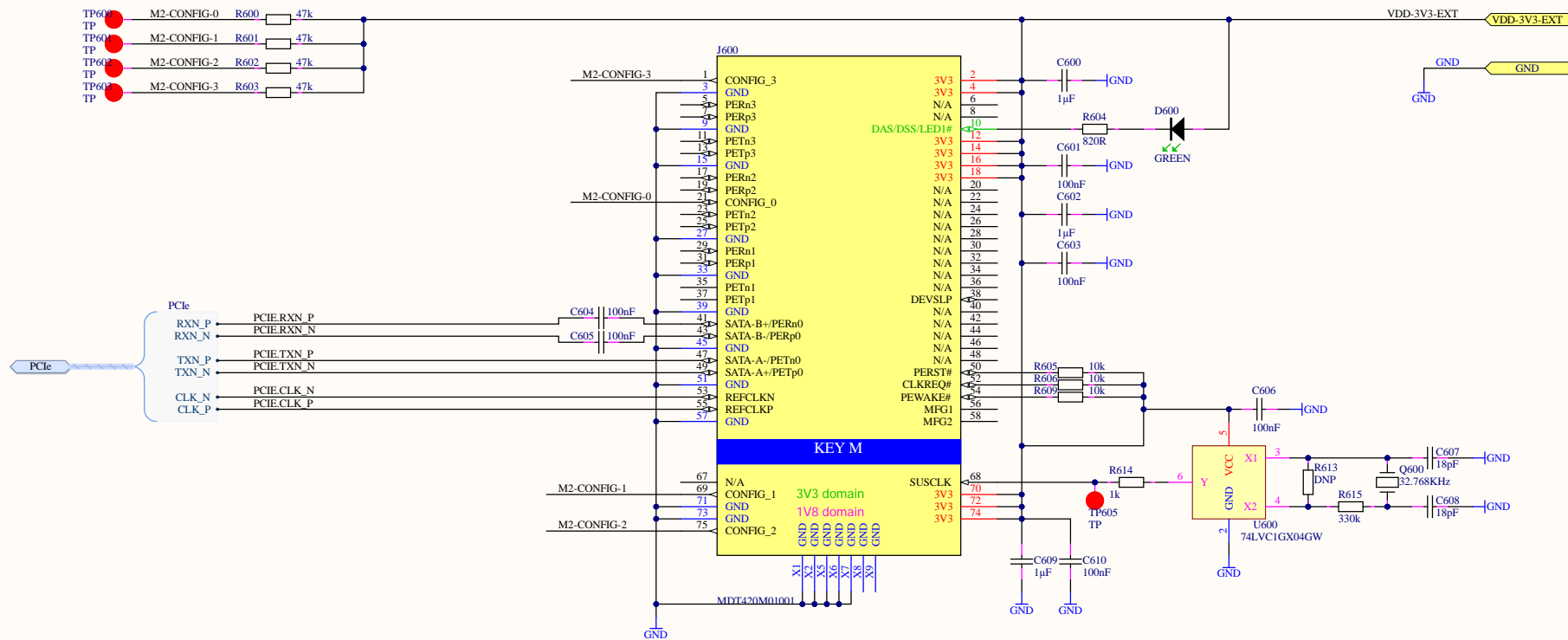
Title		
Size	Number	Revision
A3		
Date:	3.15.2023	Sheet of
File:	D:\Projekty\...09_MIPI_DSI_CSI_SchDoq	Drawn By:

HDMI/LVDS display IF



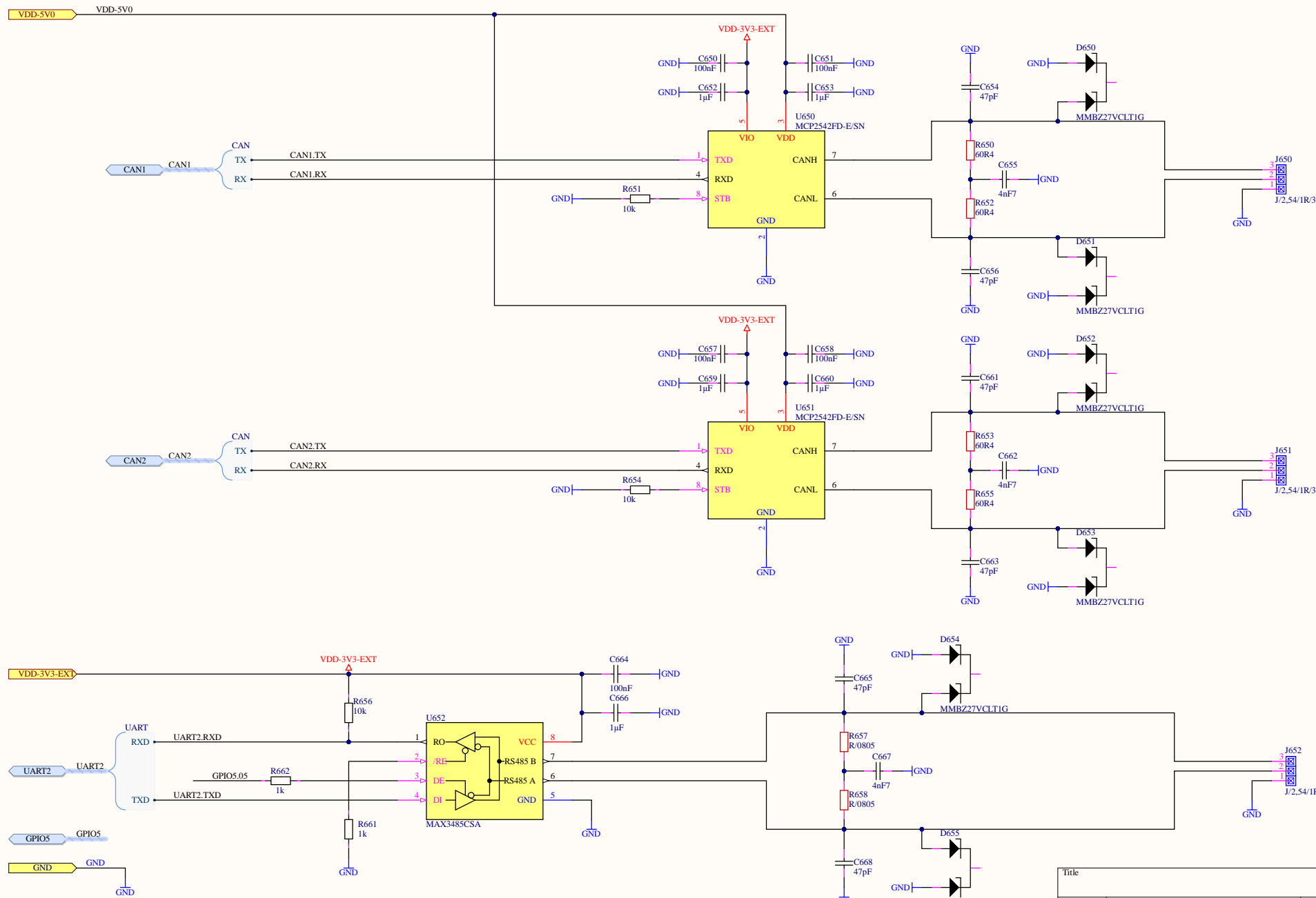
Title		
Size	Number	Revision
A3	3.15.2023	
Date:	3.15.2023	Sheet of
File:	D:\Projekty\10_HDMI_LVDS.SchDoc	Drawn By:

M.2 PCI-e interface



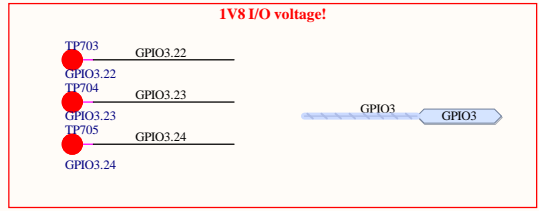
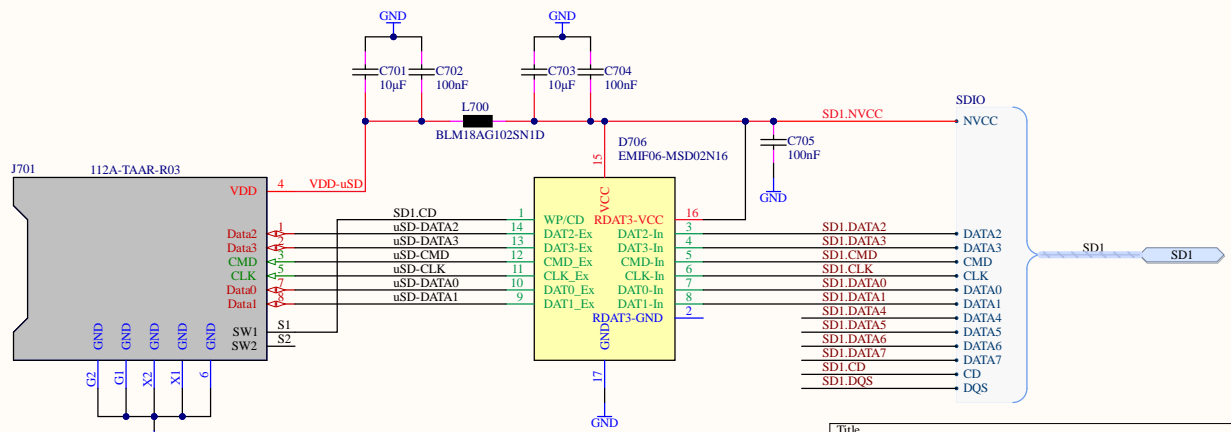
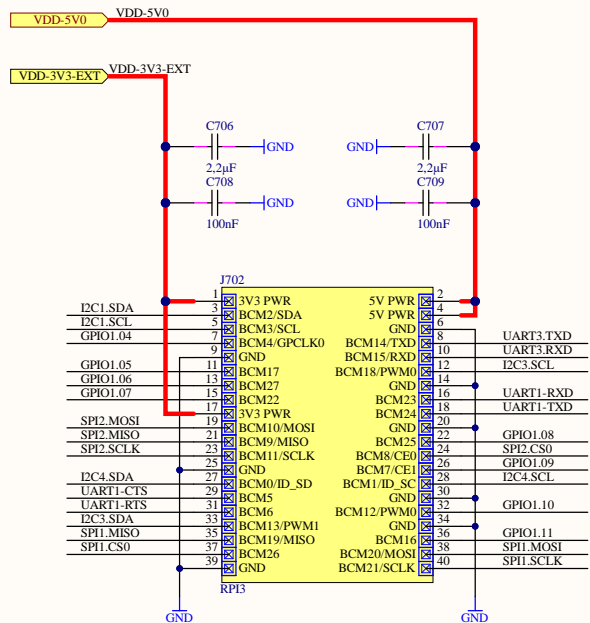
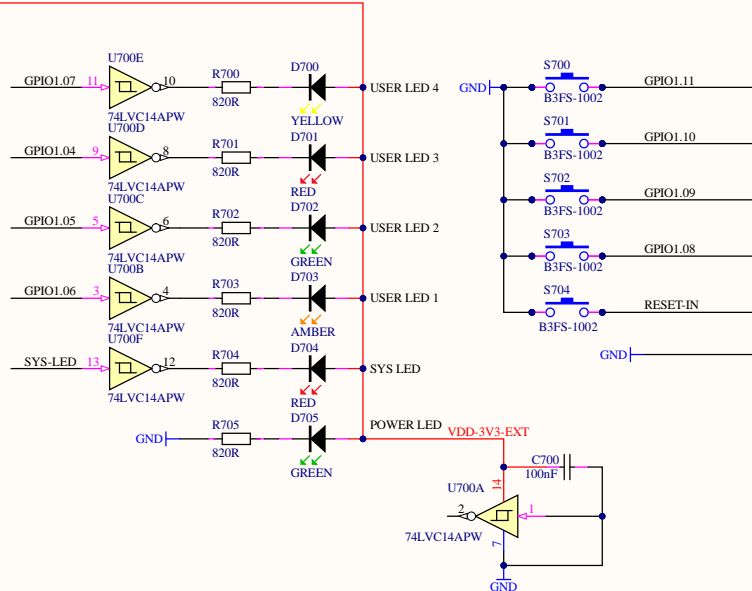
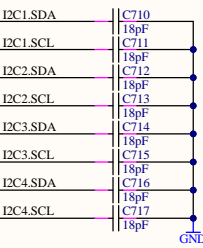
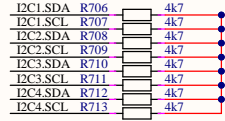
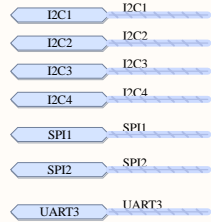
Title		
Size A3	Number	Revision
Date: File:	3.15.2023 D:\Projekty\111_PCle_M2.SchDoc	Sheet of Drawn By:

CAN/RS485 IF



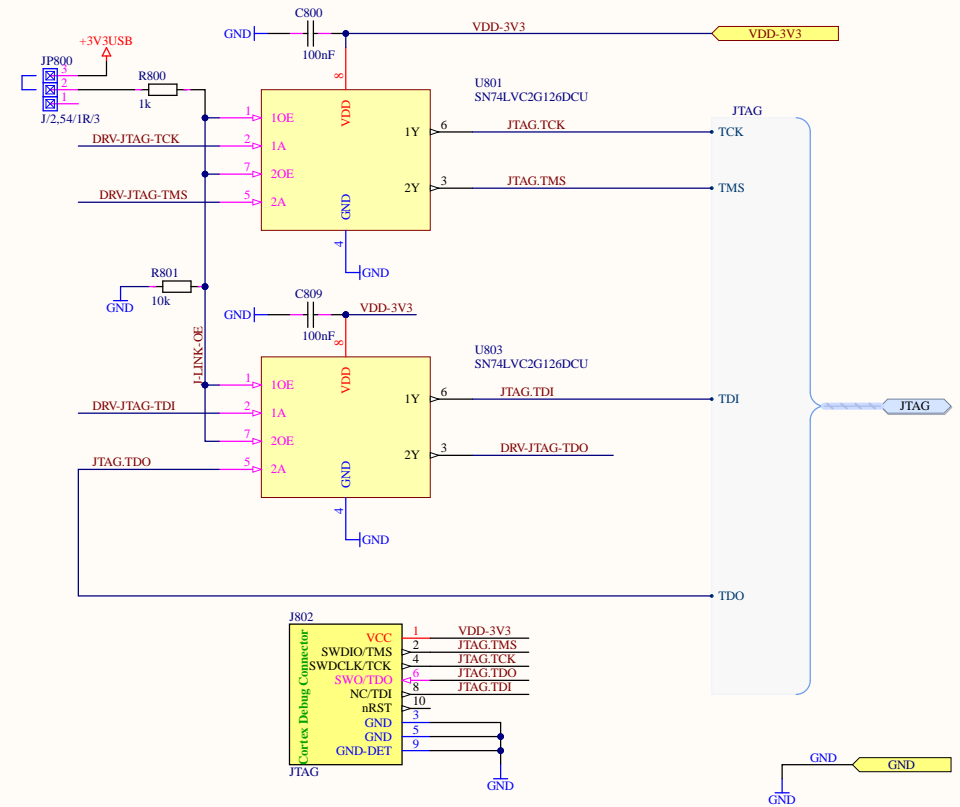
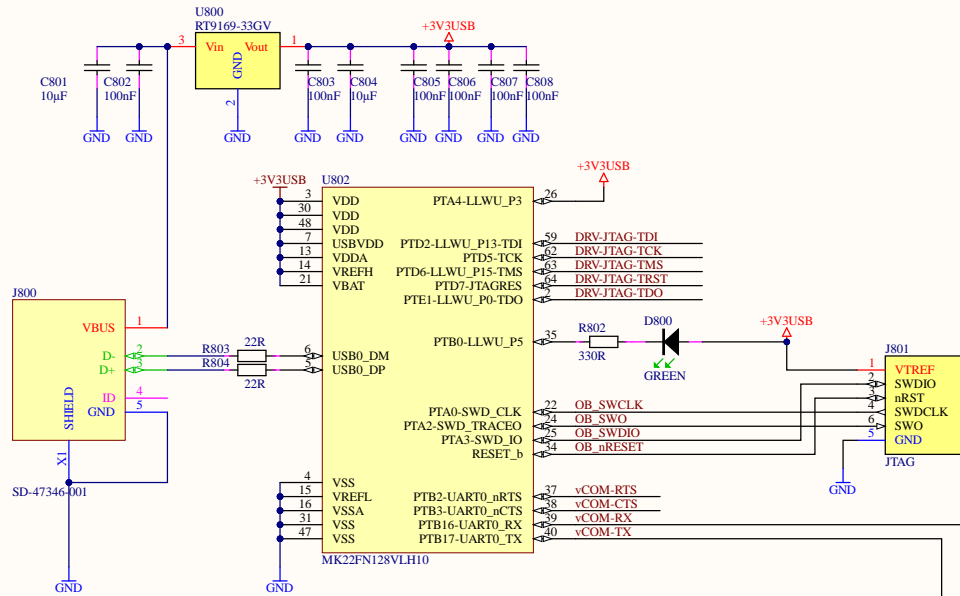
Title		
Size	Number	Revision
A3		
Date:	3.15.2023	Sheet of
File:	D:\Projekty\12_CAN_RS485.SchDoc	Drawn By:

LEDs, Buttons & GPIO



Title		
Size	Number	Revision
A3		
Date:	3.15.2023	Sheet of
File:	D:\Projekty\13_LED_Buttons.SchDoc	Drawn By:

Segger J-Link OB



Title		
Size	Number	Revision
A3		
Date:	3.15.2023	Sheet of
File:	D:\Projekt\14_Debug\IF_SchDoc	Drawn By: