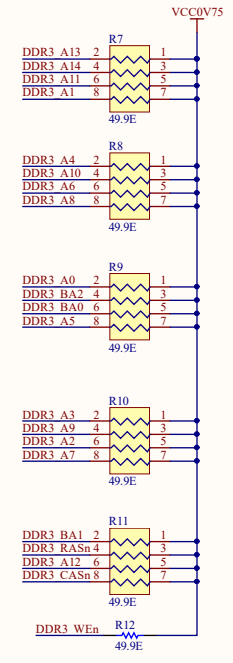
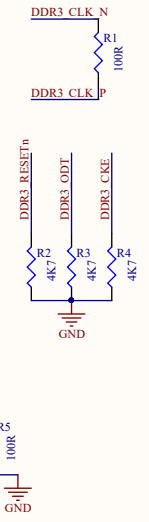
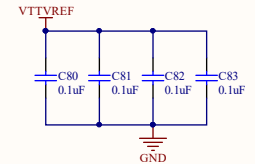
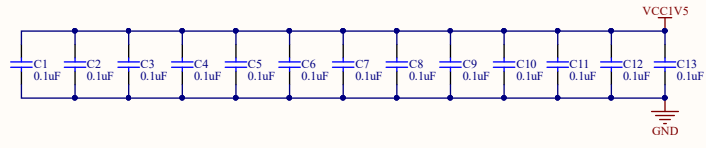
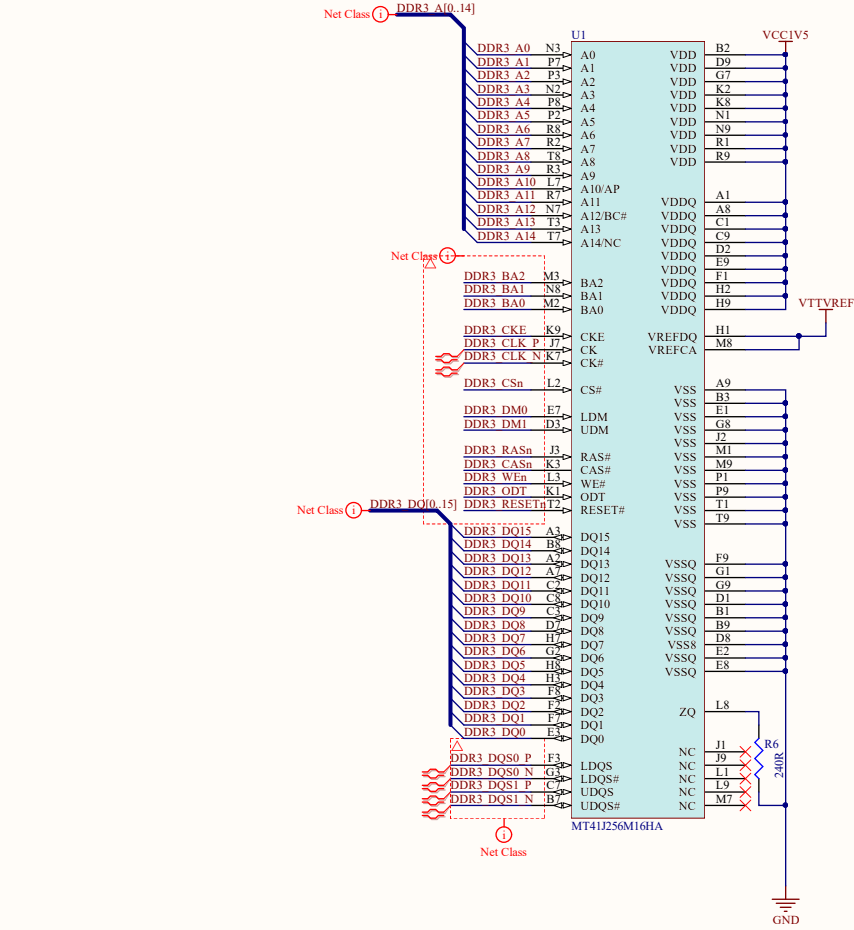


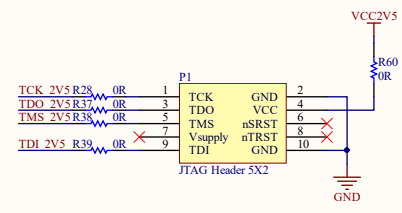
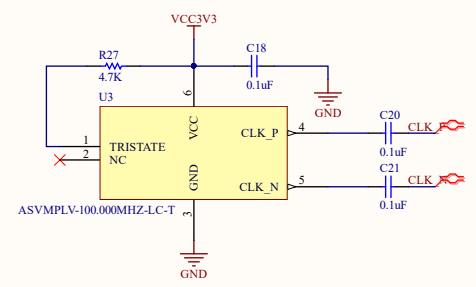
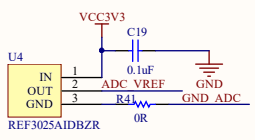
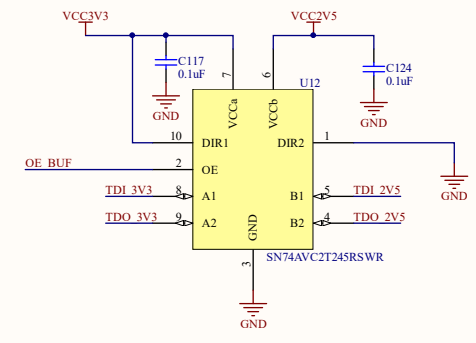
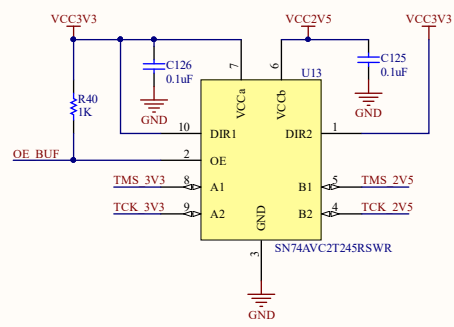
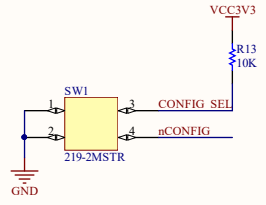
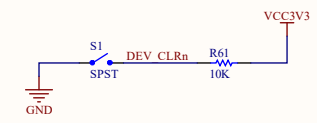
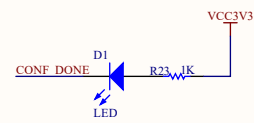
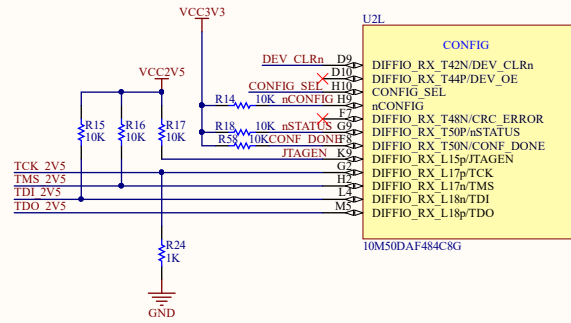
Block Diagram




DDR3

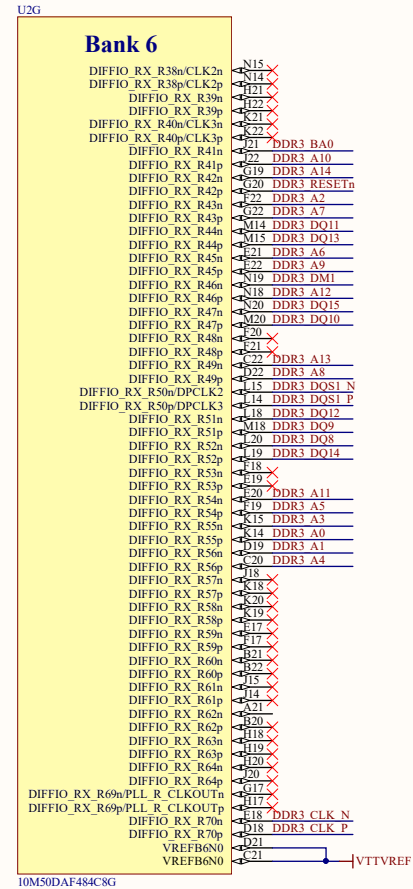
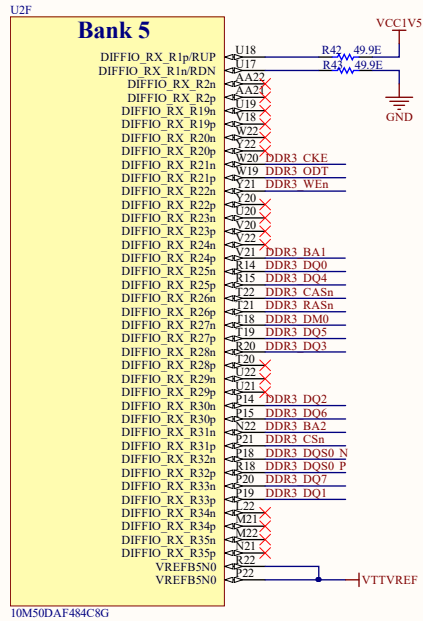
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Size: A3	Project: Telesto.PjtPcb	
Date: 1/22/2018	Time: 50:22 AM	
File: Sheet2.SchDoc	Sheet 2 of 10	






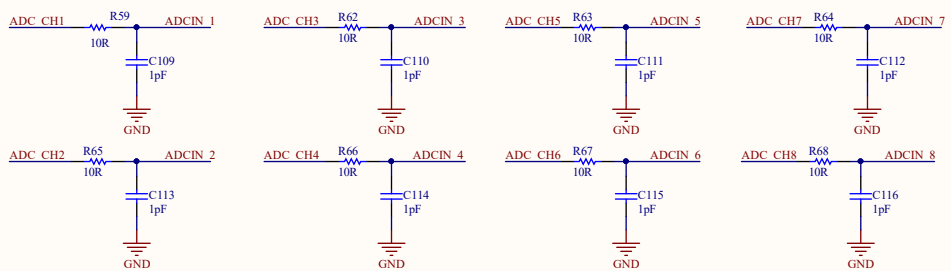
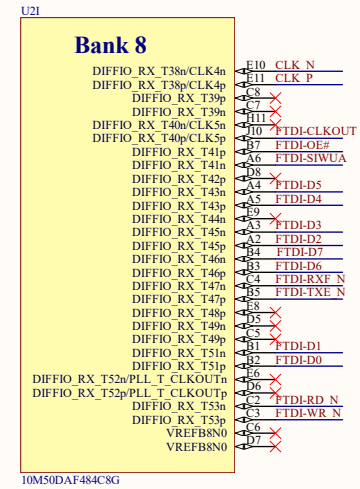
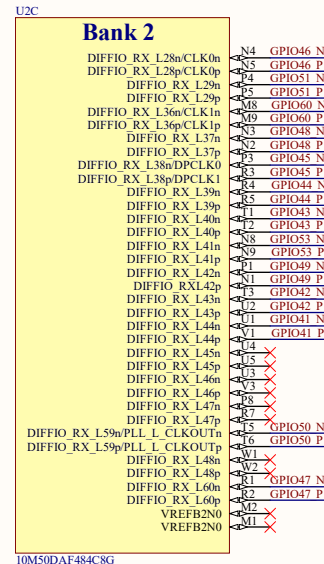
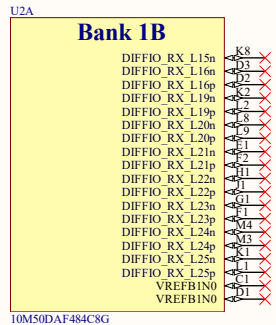
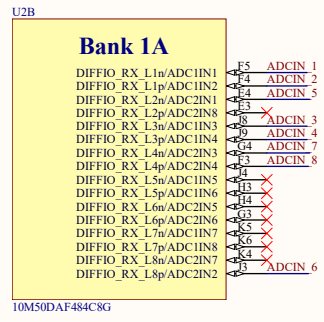
FPGA Configuration

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Size: A3	Project: TelesTo.PrtPeb		
Date: 1/22/2018	Time: 50:22 AM		
File: Sheet3.SchDoc	Sheet 3 of 10		



FPGA Banks 5&6

Title: Telesto-Max10-USB2.0	Revision: V1.1	Numato Systems Pvt Ltd		
Size: A3	Project: Telesto.PrjPeb			
Date: 1/22/2018	Time: 50:22 AM			Sheet 4 of 10
File: Sheet4.SchDoc				



FPGA Banks 1, 2 & 8

U2D

Bank 3

DIFFIO_TX_RX B1n <A5 GPIO10 N
 DIFFIO_TX_RX B1p <A6 GPIO10 P
 DIFFIO_RX B2n <V4 GPIO6 N
 DIFFIO_RX B2p <V5 GPIO6 P
 DIFFIO_TX_RX B3n <U6 GPIO9 N
 DIFFIO_TX_RX B3p <U7 GPIO9 P
 DIFFIO_RX B4n <A1 GPIO2 N
 DIFFIO_RX B4p <A2 GPIO2 P
 DIFFIO_TX_RX B5n <W3 GPIO5 P
 DIFFIO_TX_RX B5p <AA1 GPIO1 N
 DIFFIO_RX B6n <AA2 GPIO1 P
 DIFFIO_TX_RX B7n <V7 GPIO14 N
 DIFFIO_TX_RX B7p <V8 GPIO14 P
 DIFFIO_TX_RX B7p <V5 GPIO7 N
 DIFFIO_RX B8n <V6 GPIO7 P
 DIFFIO_RX B8p <R9 GPIO13 N
 DIFFIO_TX_RX B9n <P9 GPIO13 P
 DIFFIO_RX B10n <V7 GPIO16 N
 DIFFIO_RX B10p <V8 GPIO16 P
 DIFFIO_TX_RX B11n <W9 GPIO19 N
 DIFFIO_TX_RX B11p <W10 GPIO19 P
 DIFFIO_RX B12n <AB2 GPIO4 N
 DIFFIO_RX B12p <AB3 GPIO4 P
 DIFFIO_TX_RX B13n <W7 GPIO15 N
 DIFFIO_TX_RX B13p <W8 GPIO15 P
 DIFFIO_RX B14n <V3 GPIO3 N
 DIFFIO_RX B14p <V4 GPIO3 P
 DIFFIO_TX_RX B15n <R10 GPIO21 N
 DIFFIO_TX_RX B15p <P10 GPIO21 P
 DIFFIO_TX_RX B16n <AA7 GPIO11 P
 DIFFIO_TX_RX B16p <AA8 GPIO11 P
 DIFFIO_RX B17n <AB5 GPIO8 N
 DIFFIO_RX B17p <AB6 GPIO8 P
 DIFFIO_TX_RX B17p <V9 GPIO22 N
 DIFFIO_TX_RX B18n/CLK6p <V10 GPIO22 P
 DIFFIO_RX B19n <AB7 GPIO12 N
 DIFFIO_RX B19p <AB8 GPIO12 P
 DIFFIO_TX_RX B20n/CLK7n <R11 GPIO25 N
 DIFFIO_TX_RX B20p/CLK7p <P11 GPIO25 P
 DIFFIO_RX B21n <AA8 GPIO18 N
 DIFFIO_RX B21p <AA9 GPIO18 P
 DIFFIO_TX_RX B22n <V10 GPIO17 N
 DIFFIO_TX_RX B22p <AA10 GPIO17 P
 DIFFIO_RX B23n <AB9 GPIO20 N
 DIFFIO_RX B23p <AB10 GPIO20 P
 VREFB3N0 <AA3
 VREFB3P0 <AA4
 VREFB4N0 <AB4

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U2E

Bank 4

DIFFIO_TX_RX B24n <V11
 DIFFIO_TX_RX B24p <V11 GPIO26 N
 DIFFIO_RX B25n <V11 GPIO26 P
 DIFFIO_RX B25p <V12 GPIO23 N
 DIFFIO_TX_RX B26n <P12 GPIO23 P
 DIFFIO_RX B27n <AB10 GPIO24 N
 DIFFIO_RX B27p <AB11 GPIO24 P
 DIFFIO_TX_RX B28n <AA16 GPIO28 N
 DIFFIO_TX_RX B28p <AA17 GPIO28 P
 DIFFIO_RX B29n <AB12 GPIO30 N
 DIFFIO_RX B29p <AB13 GPIO30 P
 DIFFIO_TX_RX B34n <V13 GPIO31 N
 DIFFIO_TX_RX B34p <V14 GPIO31 P
 DIFFIO_RX B35n <W12 GPIO29 N
 DIFFIO_RX B35p <W13 GPIO29 P
 DIFFIO_TX_RX B36n <R13 GPIO27 N
 DIFFIO_TX_RX B36p <R13 GPIO27 P
 DIFFIO_TX_RX B37n <R13 GPIO32 N
 DIFFIO_TX_RX B37p <R14 GPIO32 P
 DIFFIO_RX B38n <AA14 GPIO34 N
 DIFFIO_RX B38p <AA15 GPIO34 P
 DIFFIO_TX_RX B39n <W14 GPIO33 N
 DIFFIO_TX_RX B39p <W15 GPIO33 P
 DIFFIO_RX B40n <AA15 GPIO36 N
 DIFFIO_RX B40p <AA16 GPIO36 P
 DIFFIO_TX_RX B41n <V15 GPIO35 N
 DIFFIO_TX_RX B41p <V16 GPIO35 P
 DIFFIO_RX B42n <AA16 GPIO38 N
 DIFFIO_RX B42p <AA17 GPIO38 P
 DIFFIO_TX_RX B43n <AA17
 DIFFIO_TX_RX B43p <AB17
 DIFFIO_RX B44n <AB18
 DIFFIO_RX B44p <AB20
 DIFFIO_TX_RX B45n <V15
 DIFFIO_TX_RX B45p <V16
 DIFFIO_RX B46n <AA18
 DIFFIO_RX B46p <AA19
 DIFFIO_TX_RX B49n <V19 GPIO37 N
 DIFFIO_TX_RX B49p <V18 GPIO37 P
 DIFFIO_TX_RX B50n <AB21 GPIO40 N
 DIFFIO_TX_RX B50p <AA20 GPIO40 P
 DIFFIO_RX B57n/PLL B CLKOUTn <W17 GPIO39 N
 DIFFIO_TX_RX B57p/PLL B CLKOUTp <W17 GPIO39 P
 DIFFIO_RX B58n <AB17
 DIFFIO_RX B58p <AB18
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U2H

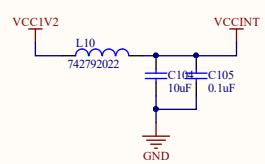
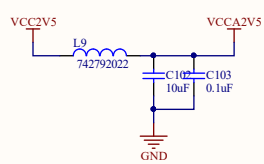
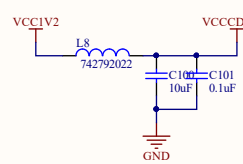
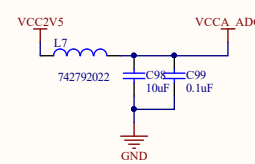
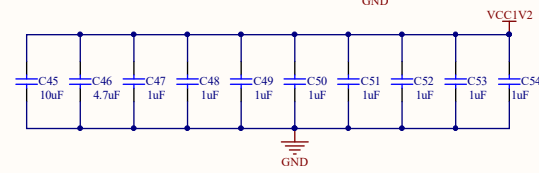
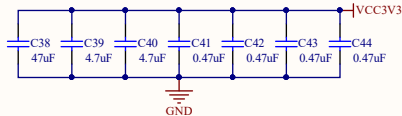
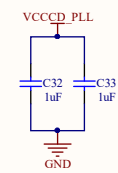
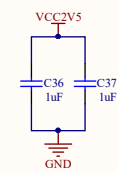
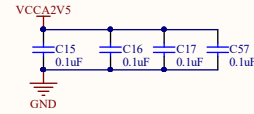
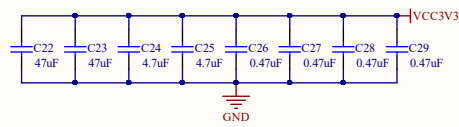
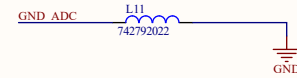
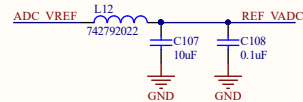
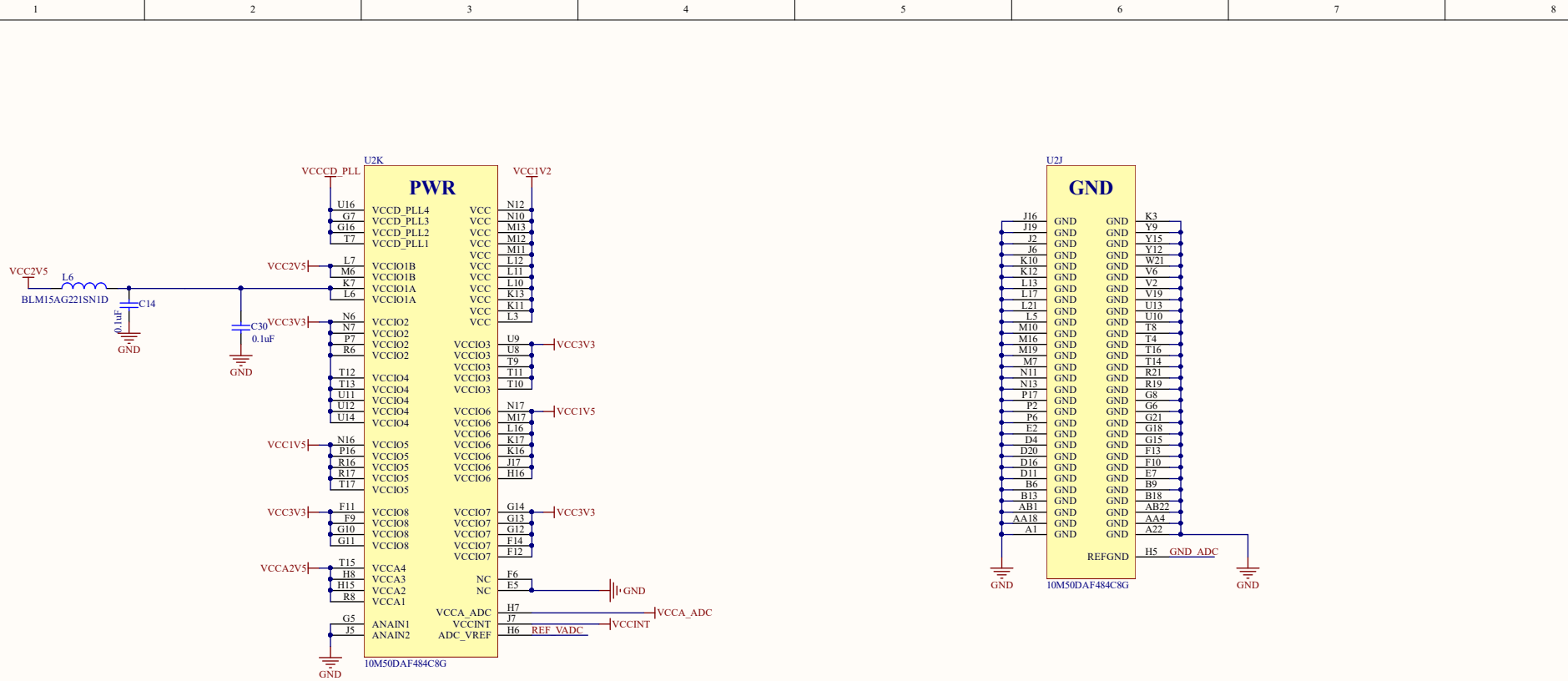
Bank 7

DIFFIO_RX T1n <E15 GPIO68 N
 DIFFIO_RX T1p <E16 GPIO68 P
 DIFFIO_RX T2n <D17 GPIO71 N
 DIFFIO_RX T2p <D17 GPIO71 P
 DIFFIO_TX T5n <E15 GPIO70 N
 DIFFIO_TX T5p <E16 GPIO70 P
 DIFFIO_RX T6n <B19 GPIO74 N
 DIFFIO_RX T6p <B19 GPIO74 P
 DIFFIO_TX T7n <C18 GPIO72 N
 DIFFIO_TX T7p <C18 GPIO72 P
 DIFFIO_RX T8n <A19 GPIO75 N
 DIFFIO_RX T8p <A20 GPIO75 P
 DIFFIO_TX T9n <E14 GPIO65 N
 DIFFIO_TX T9p <D15 GPIO65 P
 DIFFIO_RX T10n <A17 GPIO73 N
 DIFFIO_RX T10p <A18 GPIO73 P
 DIFFIO_RX T11n <C15
 DIFFIO_RX T11p <C16
 DIFFIO_TX T15p <A16
 DIFFIO_RX T16n <B16
 DIFFIO_RX T16p <A15 GPIO64 N
 DIFFIO_TX T17n <H14 GPIO64 P
 DIFFIO_RX T17p <C13 GPIO66 N
 DIFFIO_TX T18n <C14 GPIO66 P
 DIFFIO_RX T18p <B14 GPIO69 N
 DIFFIO_TX T19n <A14 GPIO69 P
 DIFFIO_TX T19p <D11
 DIFFIO_RX T20n <D11
 DIFFIO_RX T20p <D13 GPIO61 N
 DIFFIO_TX T21n <E12 GPIO61 P
 DIFFIO_TX T21p <D12 GPIO54 N
 DIFFIO_RX T22n <A13 GPIO67 N
 DIFFIO_RX T22p <A13 GPIO67 P
 DIFFIO_TX T23n <D12 GPIO55 N
 DIFFIO_TX T23p <C12 GPIO55 P
 DIFFIO_RX T24n <A10 GPIO63 N
 DIFFIO_RX T24p <A10 GPIO63 P
 DIFFIO_TX T25n <A11 GPIO63 N
 DIFFIO_TX T25p <A10 GPIO57 N
 DIFFIO_RX T26n <C11 GPIO57 P
 DIFFIO_RX T26p <B11 GPIO62 N
 DIFFIO_TX T27n <B12 GPIO62 P
 DIFFIO_TX T27p <H11 GPIO52 N
 DIFFIO_RX T28n <A8 GPIO59 N
 DIFFIO_RX T28p <A7 GPIO59 P
 DIFFIO_TX T29n <C9 GPIO56 N
 DIFFIO_TX T29p <B10 GPIO56 P
 DIFFIO_RX T30n <B8 GPIO58 N
 DIFFIO_RX T30p <A9 GPIO58 P
 DIFFIO_TX T31n <B15
 DIFFIO_TX T31p <A15
 VREFB7N0 <B15
 VREFB7P0 <A15

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FPGA Banks 3, 4 & 7

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Date: 1/22/2018	Time: 50:23 AM		
File: Sheet6.SchDoc	Sheet 6 of 10		



FPGA Power

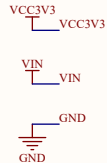
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Size: A3	Project: Telesto.PrjPcb	
Date: 1/22/2018	Time: 50:23 AM	
File: Sheet7.SchDoc	Sheet 7 of 10	



P4

GND	1	2	VCC3V3
VIN	3	4	GND
GPIO1 N	5	6	GPIO1 P
GPIO2 N	7	8	GPIO2 P
GPIO3 N	9	10	GPIO3 P
GPIO4 N	11	12	GPIO4 P
GPIO5 N	13	14	GPIO5 P
GPIO6 N	15	16	GPIO6 P
GPIO7 N	17	18	GPIO7 P
GPIO8 N	19	20	GPIO8 P
GPIO9 N	21	22	GPIO9 P
GPIO10 N	23	24	GPIO10 P
GPIO11 N	25	26	GPIO11 P
GPIO12 N	27	28	GPIO12 P
GPIO13 N	29	30	GPIO13 P
GPIO14 N	31	32	GPIO14 P
GPIO15 N	33	34	GPIO15 P
GPIO16 N	35	36	GPIO16 P
GPIO17 N	37	38	GPIO17 P
GPIO18 N	39	40	GPIO18 P
GPIO19 N	41	42	GPIO19 P
GPIO20 N	43	44	GPIO20 P
GPIO21 N	45	46	GPIO21 P
GPIO22 N	47	48	GPIO22 P
GND	49	50	GND
GND	51	52	GND
GPIO23 N	53	54	GPIO23 P
GPIO24 N	55	56	GPIO24 P
GPIO25 N	57	58	GPIO25 P
GPIO26 N	59	60	GPIO26 P
GPIO27 N	61	62	GPIO27 P
GPIO28 N	63	64	GPIO28 P
GPIO29 N	65	66	GPIO29 P
GPIO30 N	67	68	GPIO30 P
GPIO31 N	69	70	GPIO31 P
GPIO32 N	71	72	GPIO32 P
GPIO33 N	73	74	GPIO33 P
GPIO34 N	75	76	GPIO34 P
GPIO35 N	77	78	GPIO35 P
GPIO36 N	79	80	GPIO36 P
GPIO37 N	81	82	GPIO37 P
GPIO38 N	83	84	GPIO38 P
GPIO39 N	85	86	GPIO39 P
GPIO40 N	87	88	GPIO40 P
GND	89	90	GND
GND	91	92	GND
VCC3V3	93	94	VCC3V3
VCC3V3	95	96	VCC3V3

HEADER 48 X 2




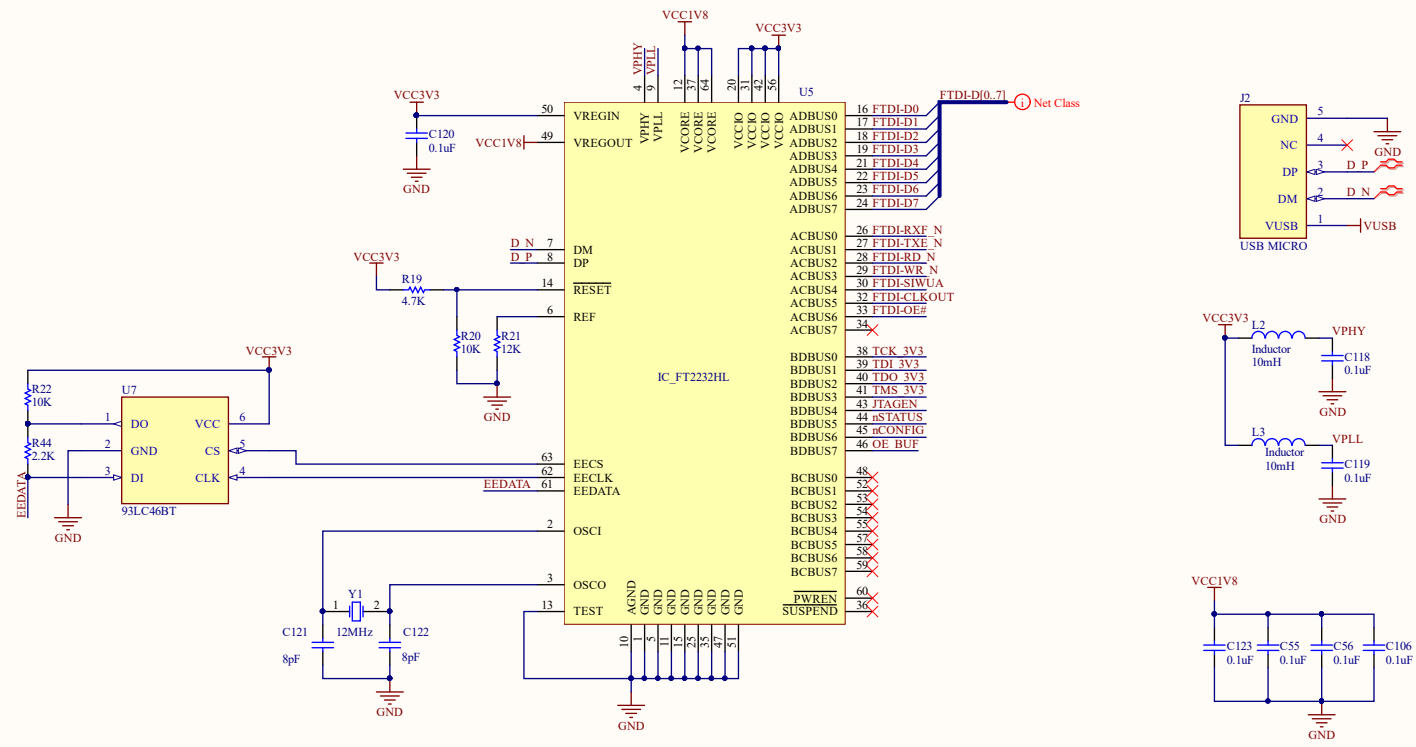
PS

ADC CH1	1	2	ADC CH2
VIN	3	4	GND
ADC CH3	5	6	ADC CH4
GPIO41 N	7	8	GPIO41 P
ADC CH5	9	10	ADC CH6
GPIO42 N	11	12	GPIO42 P
ADC CH7	13	14	ADC CH8
GPIO43 N	15	16	GPIO43 P
GPIO44 N	17	18	GPIO44 P
GPIO45 N	19	20	GPIO45 P
GPIO63 N	21	22	GPIO63 P
GPIO47 N	23	24	GPIO47 P
GPIO48 N	25	26	GPIO48 P
GPIO49 N	27	28	GPIO49 P
GPIO50 N	29	30	GPIO50 P
GPIO51 N	31	32	GPIO51 P
GPIO53 N	33	34	GPIO53 P
GPIO52 N	35	36	GPIO52 P
GPIO54 N	37	38	GPIO54 P
GPIO55 N	39	40	GPIO55 P
GPIO56 N	41	42	GPIO56 P
GPIO57 N	43	44	GPIO57 P
GND	45	46	GND
GND	47	48	GND
GND	49	50	GND
GND	51	52	GND
GPIO58 N	53	54	GPIO58 P
GPIO59 N	55	56	GPIO59 P
GPIO60 N	57	58	GPIO60 P
GPIO61 N	59	60	GPIO61 P
GPIO62 N	61	62	GPIO62 P
GPIO46 N	63	64	GPIO46 P
GPIO64 N	65	66	GPIO64 P
GPIO65 N	67	68	GPIO65 P
GPIO66 N	69	70	GPIO66 P
GPIO67 N	71	72	GPIO67 P
GPIO68 N	73	74	GPIO68 P
GPIO69 N	75	76	GPIO69 P
GPIO70 N	77	78	GPIO70 P
GPIO71 N	79	80	GPIO71 P
GPIO72 N	81	82	GPIO72 P
GPIO73 N	83	84	GPIO73 P
GPIO74 N	85	86	GPIO74 P
GPIO75 N	87	88	GPIO75 P
nSTATUS	89	90	VCC3V3
nCONFIG	91	92	VCC3V3
GND	93	94	GND
GND	95	96	GND

HEADER 48 X 2

150 GPIOs

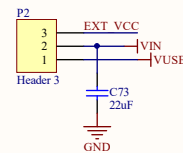
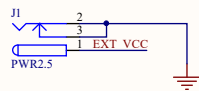
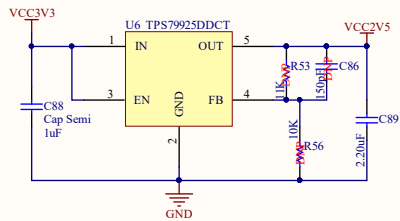
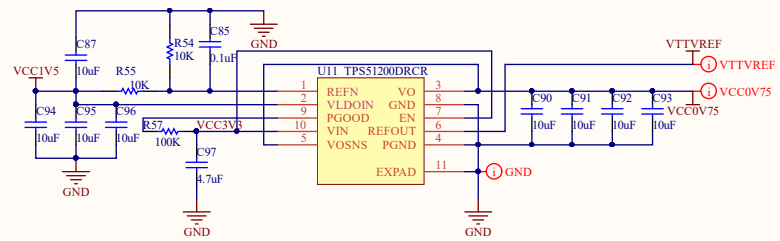
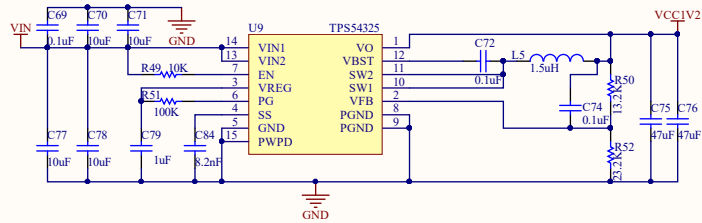
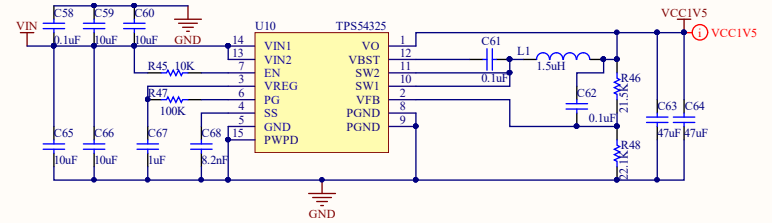
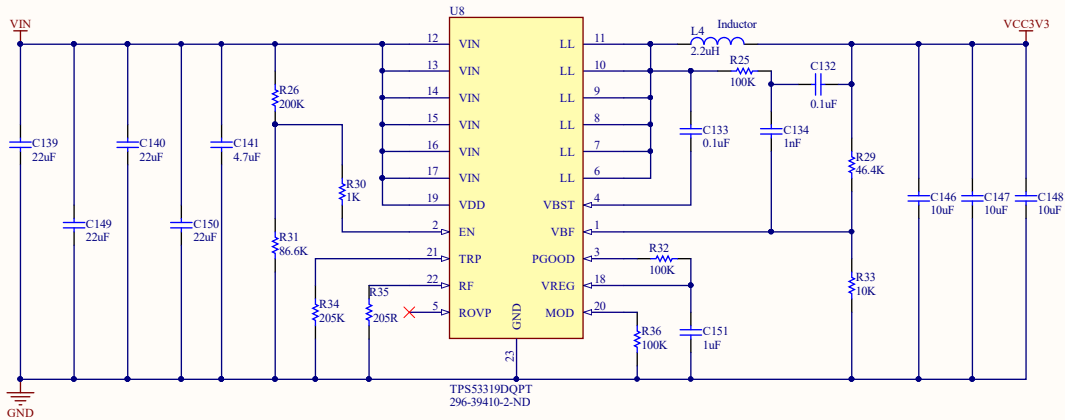
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Size: A3	Project: Telesto.PrjPcb			
Date: 1/22/2018	Time: 5:23 AM			Sheet 8 of 10
File: Sheet8_SchDoc				



FTDI

Title: Telesto-Max10-USB2.0	Revision: V1.1	Numato Systems Pvt Ltd
Size: A3	Project: Telesto.PrjPcb	
Date: 1/22/2018	Time: 50:23 AM	
File: Sheet9.SchDoc	Sheet 9 of 10	





Power Supply

Title: Telesto-Max10-USB2.0	Revision: V1.1	Numato Systems Pvt Ltd
Size: A3	Project: Telesto.PrjPcb	
Date: 1/22/2018	Time: 50:23 AM	
File: Sheet10.SchDoc	Sheet: 10 of 10	

