

A

B

C

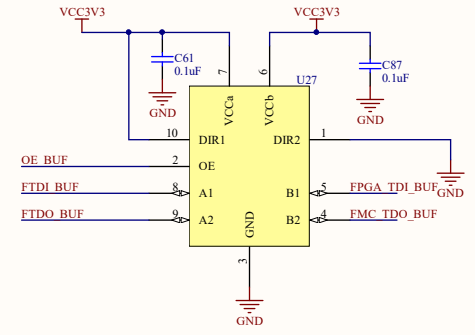
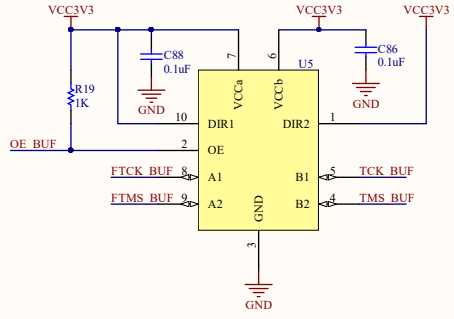
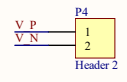
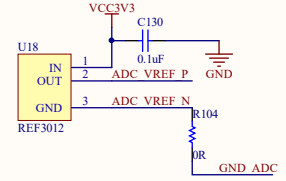
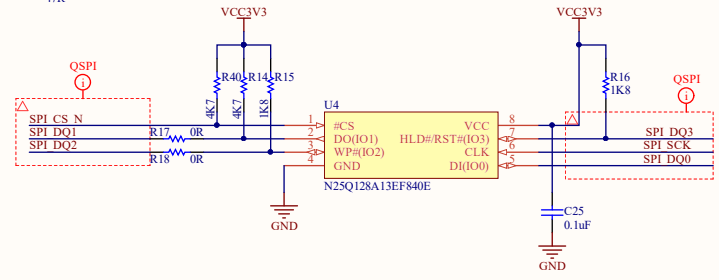
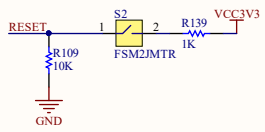
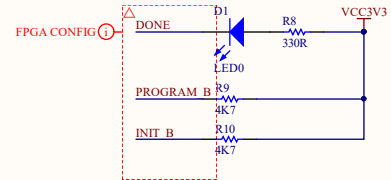
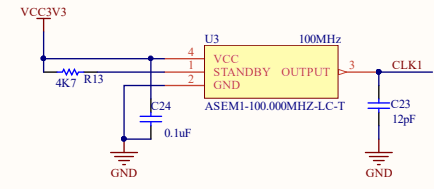
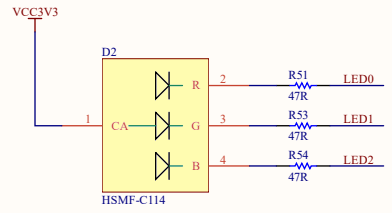
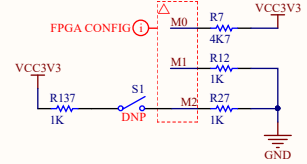
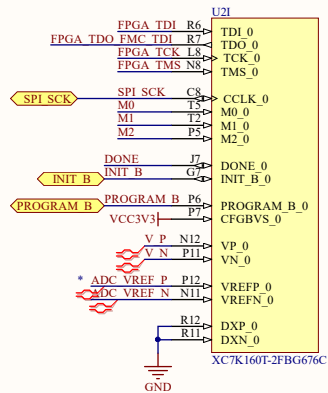
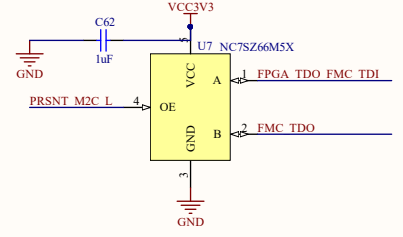
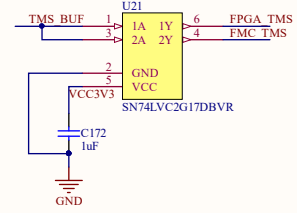
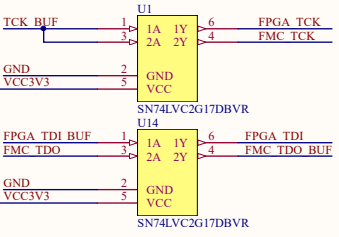
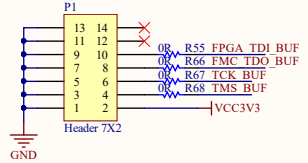
D

A

B

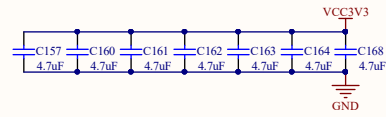
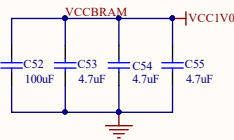
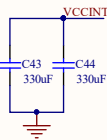
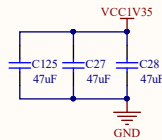
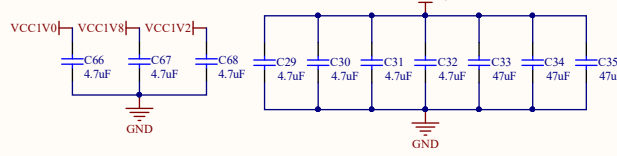
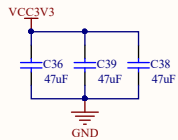
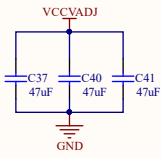
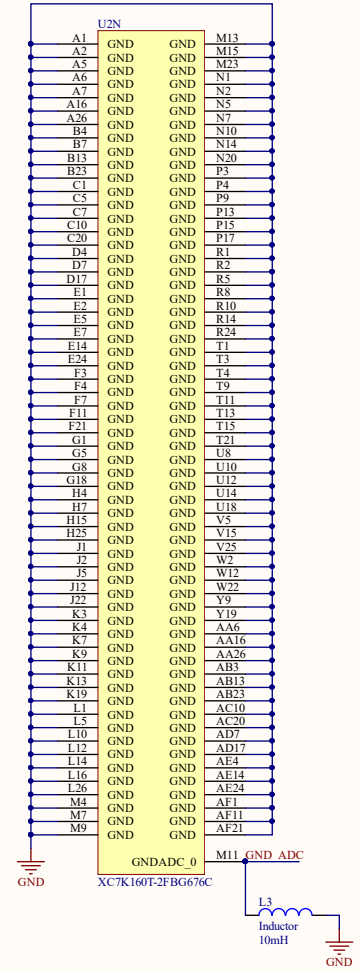
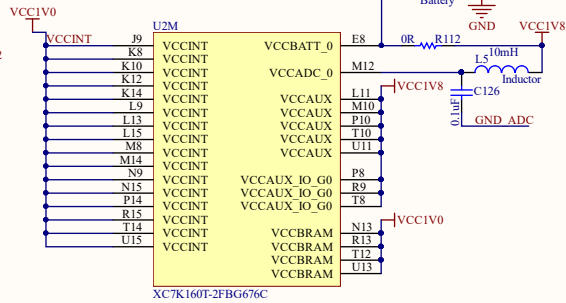
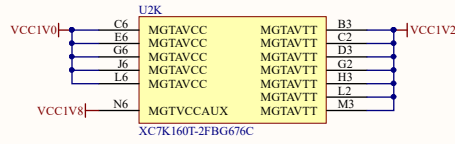
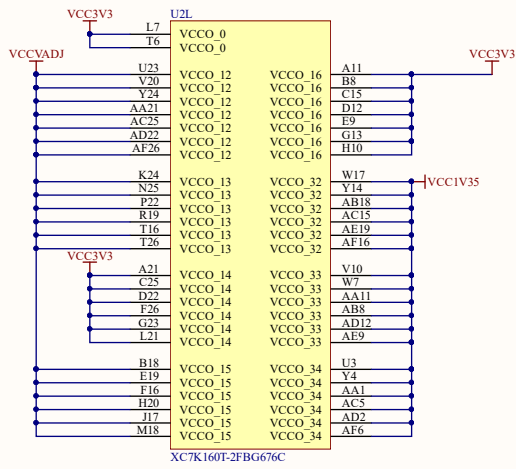
C

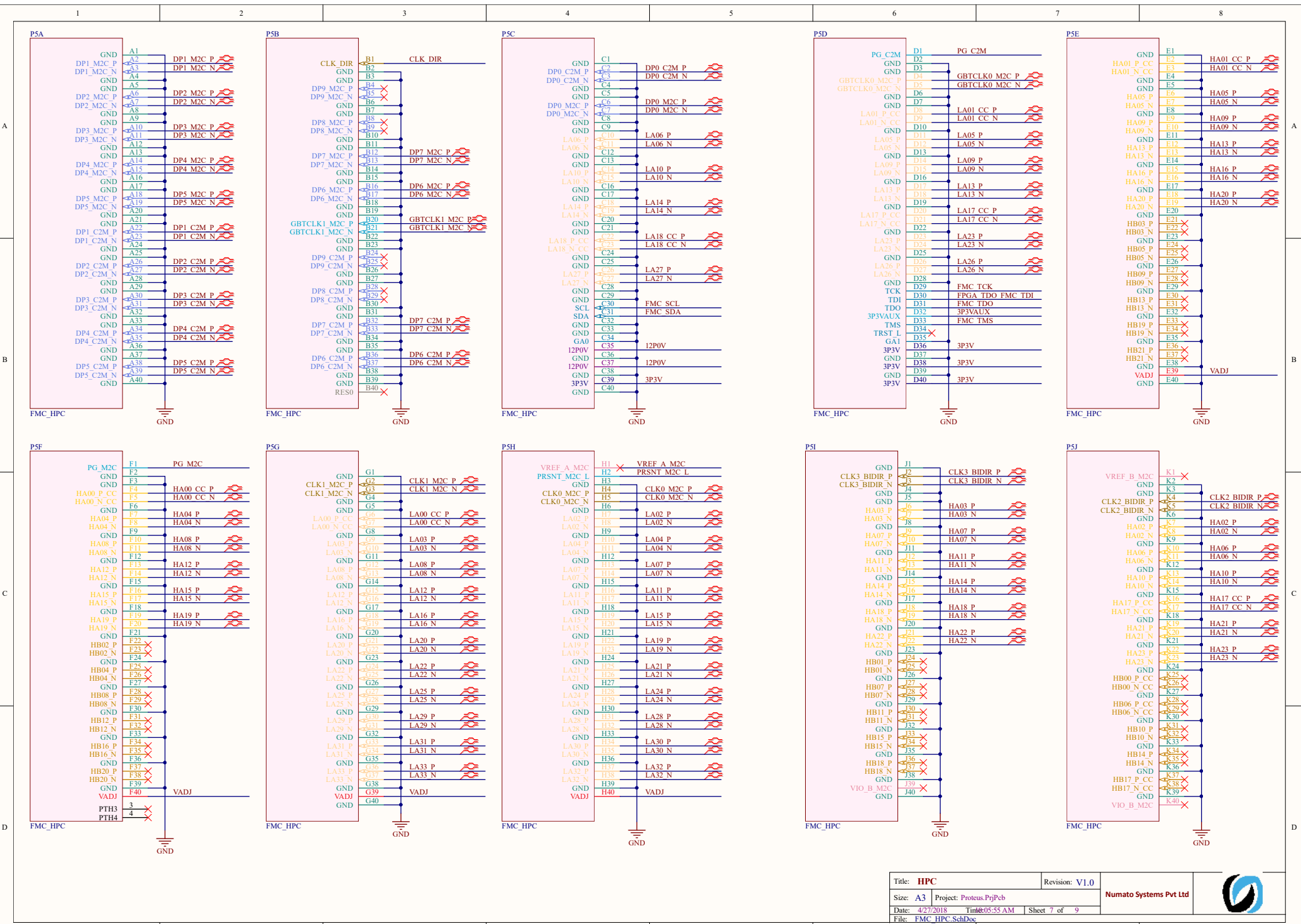
D



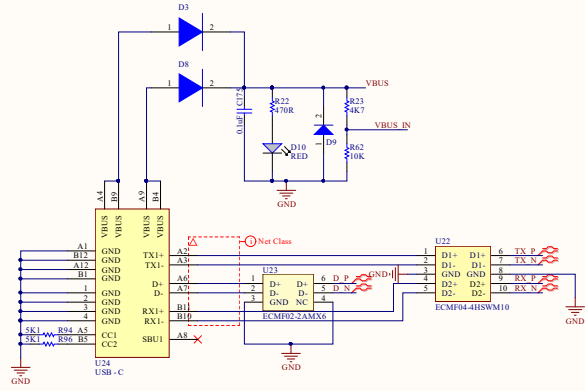
*The VRefP and VRefN lines should be treated in a similar way to the analog inputs themselves. The two lines should be routed as close as possible to one another. They should be shielded vertically and horizontally by a low-impedance quiet ground or quiet supply, such as GNDADC or VCCADC.



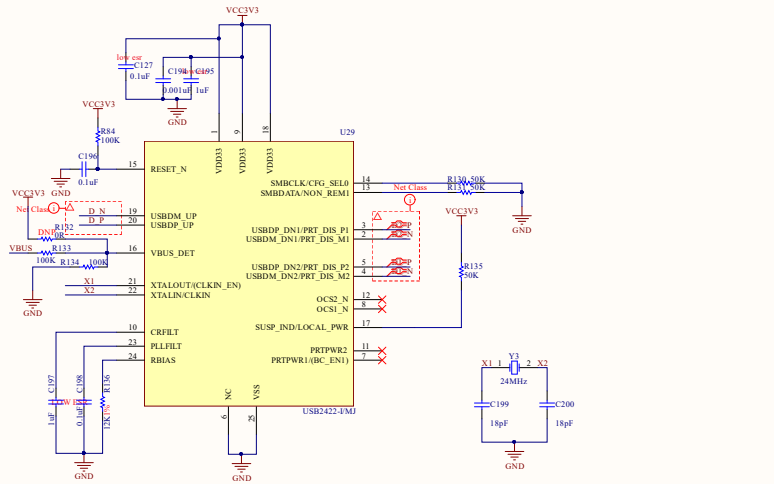




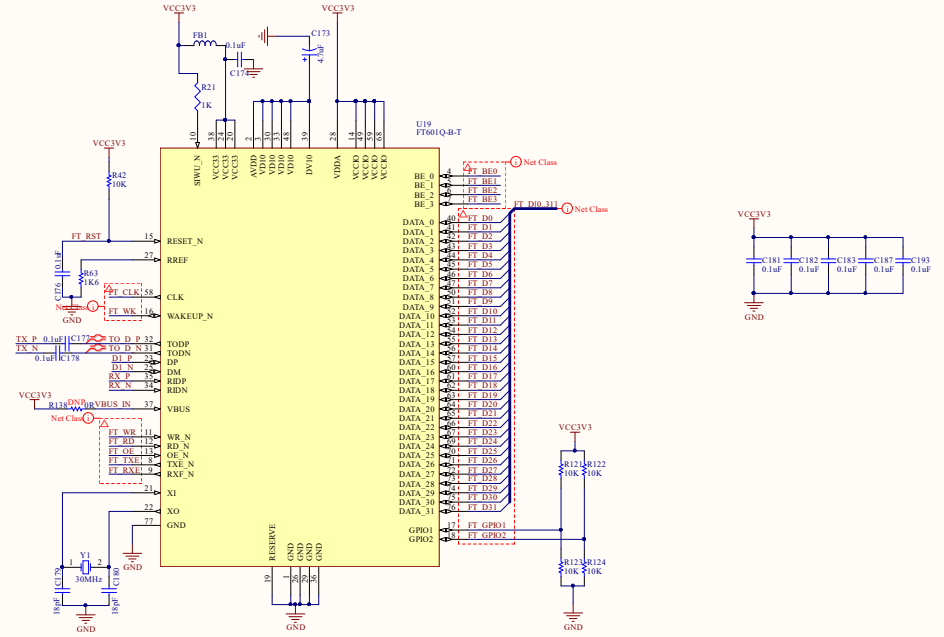
USB 3.1



USB HUB



FT601



FT232H

