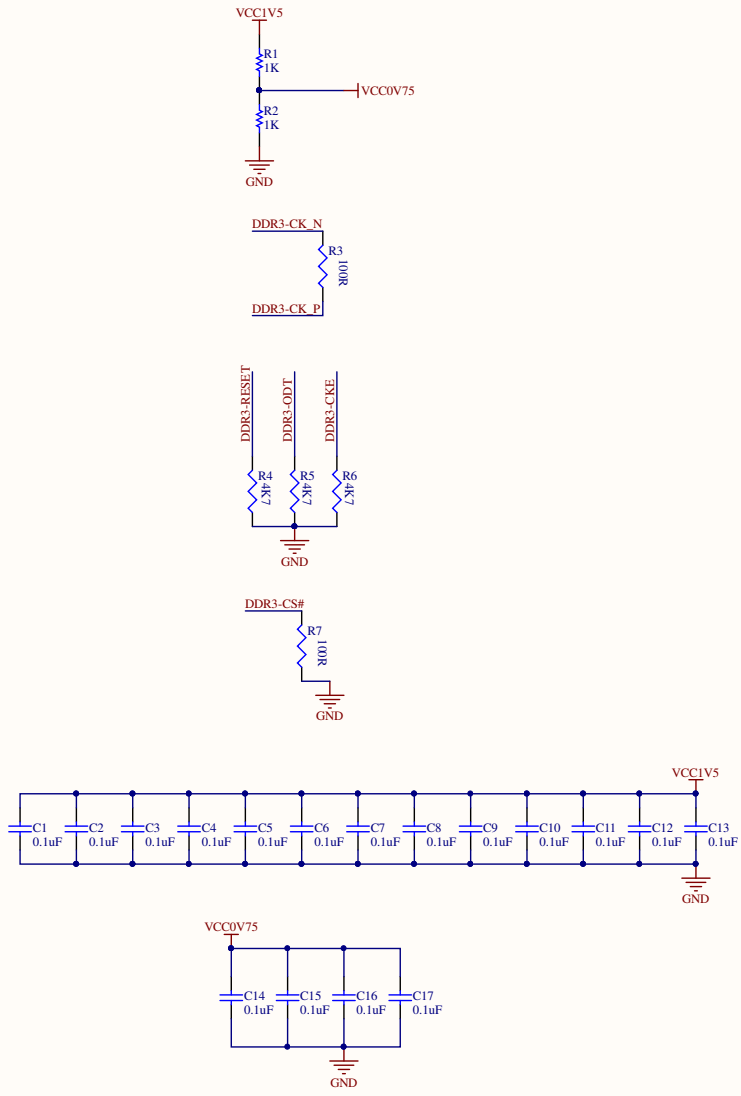
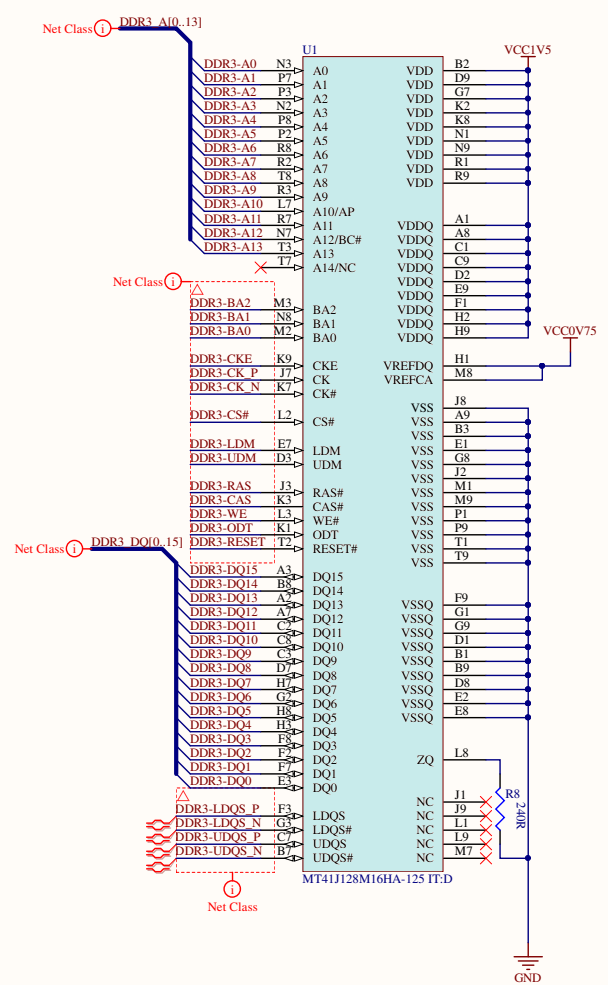
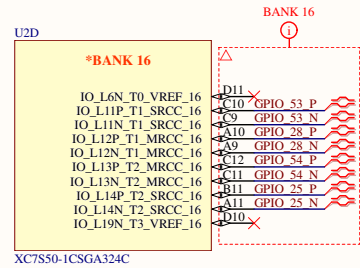
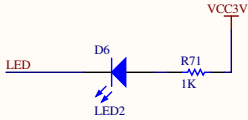
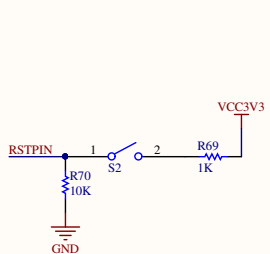
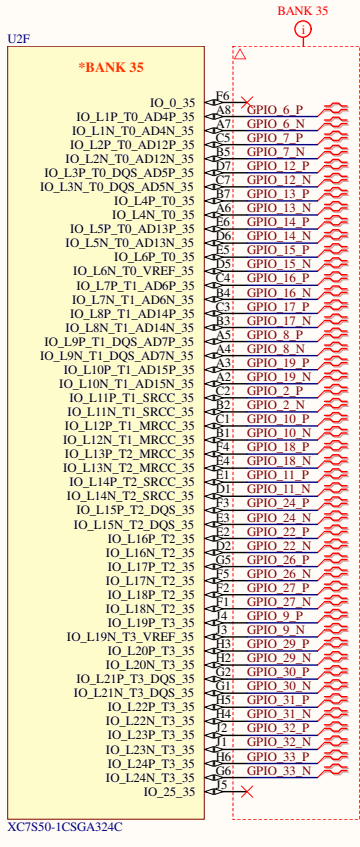
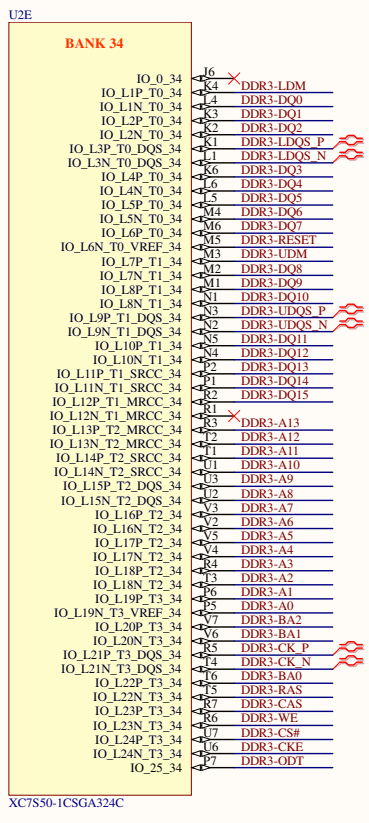
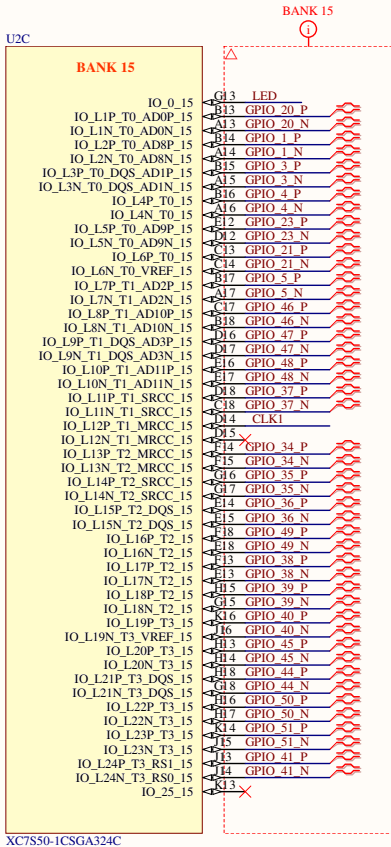
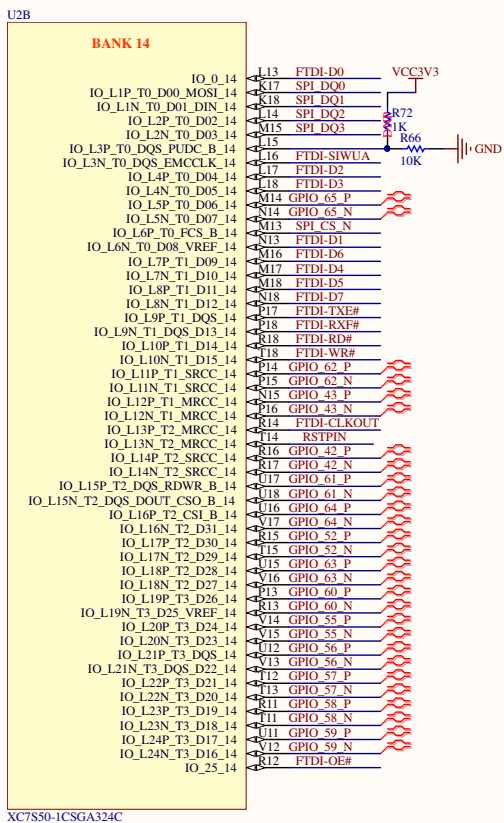


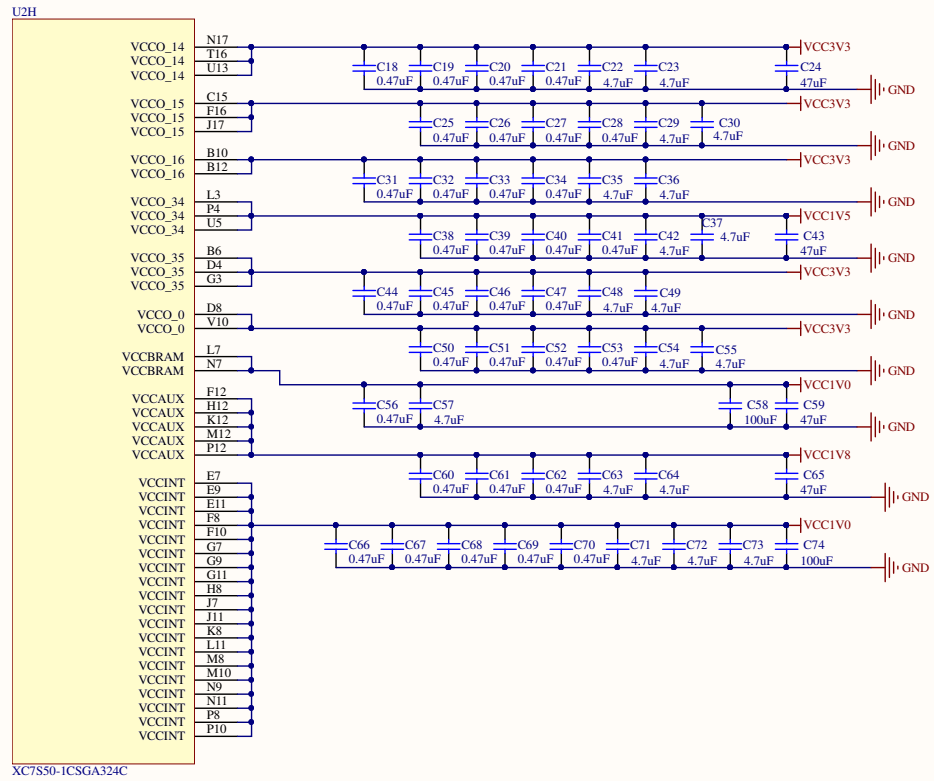
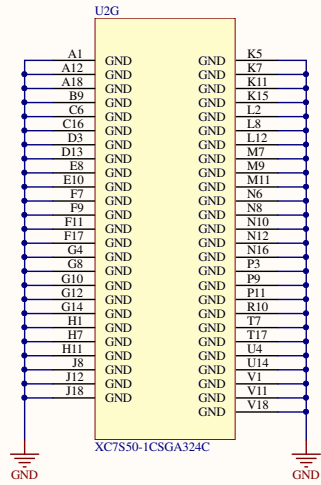
DDR3



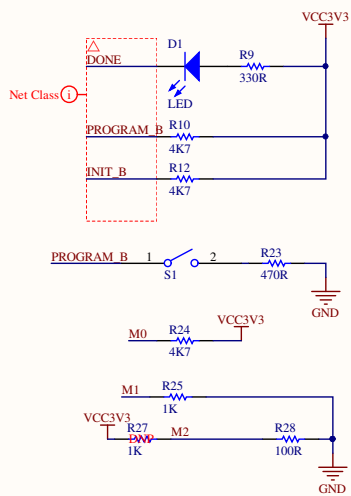
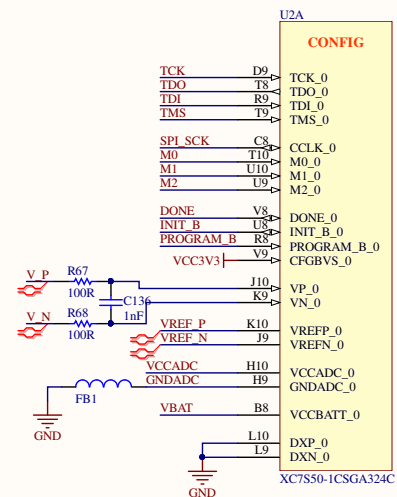
FPGA Banks



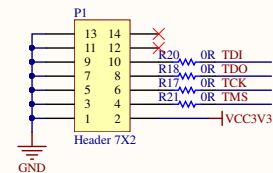
FPGA Power



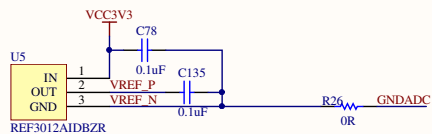
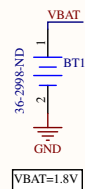
FPGA Configuration



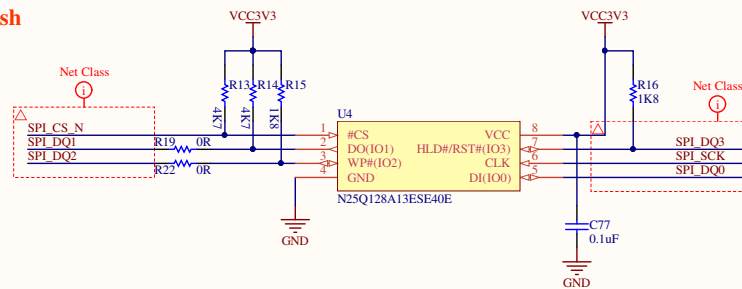
JTAG



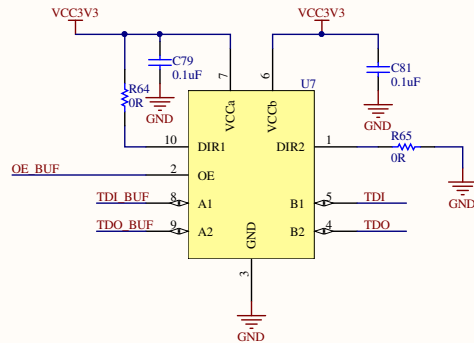
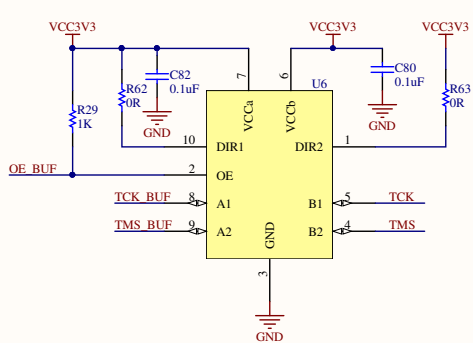
Backup Power and ADC Reference



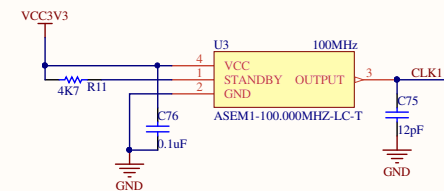
QSPI Flash



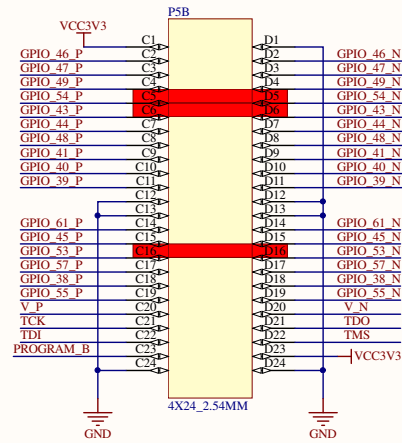
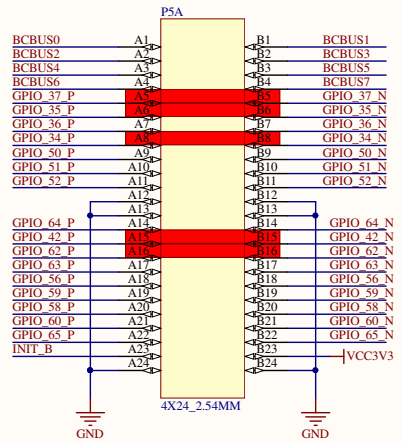
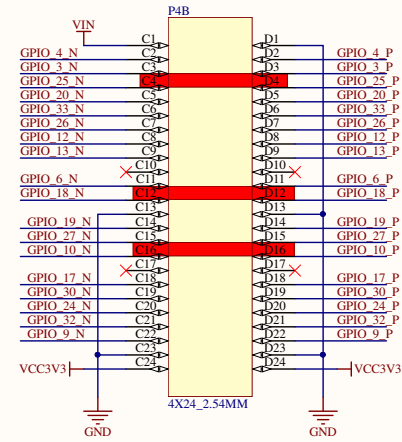
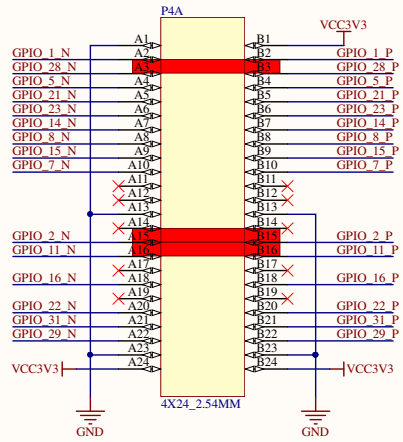
Buffers



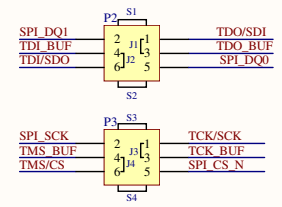
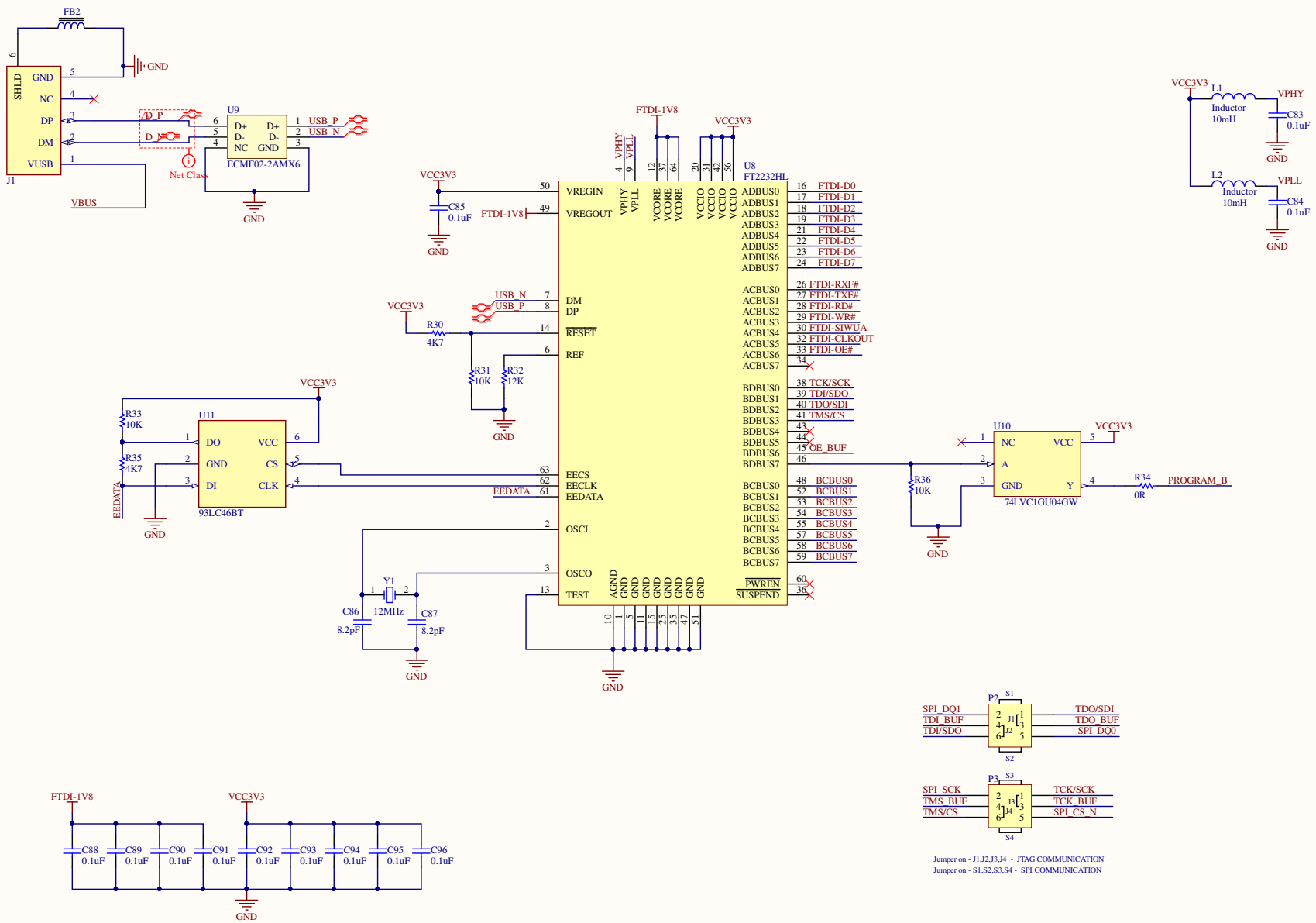
Clock



GPIO Header



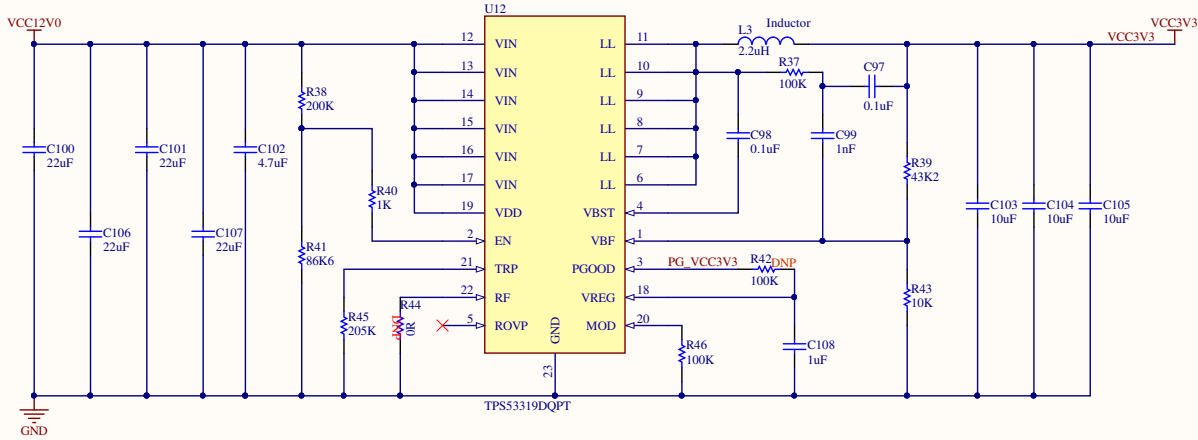
FT2232H



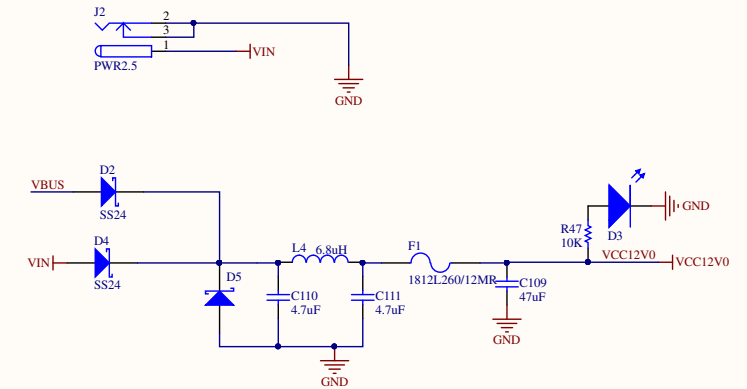
Jumper on - J12,J3,J4 - JTAG COMMUNICATION
 Jumper on - S1,S2,S3,S4 - SPI COMMUNICATION



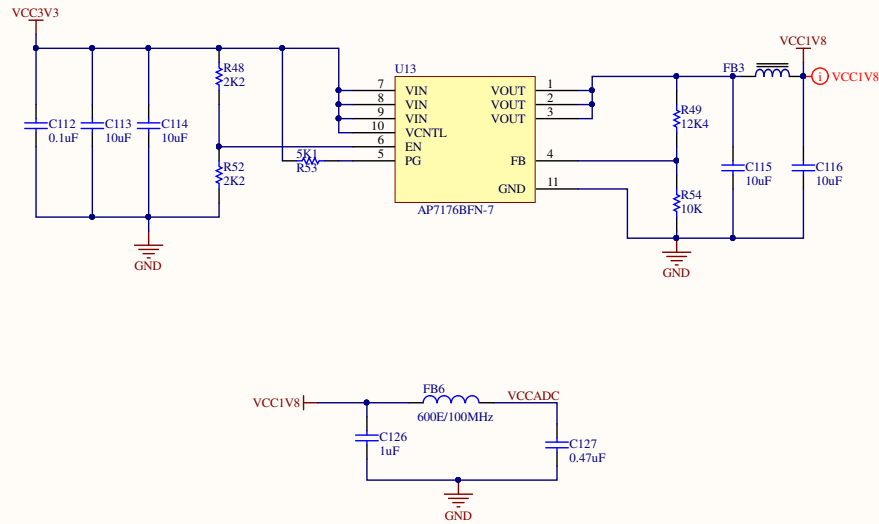
VCC3V3



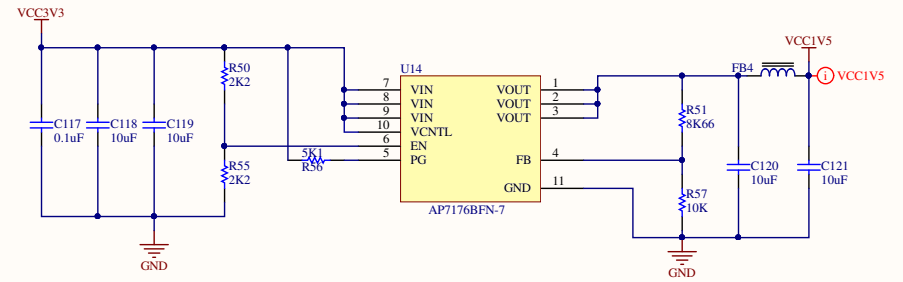
DC Jack



VCC1V8



VCC1V5



VCC1V0

