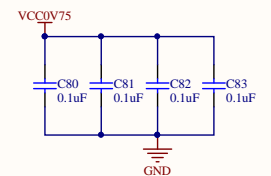
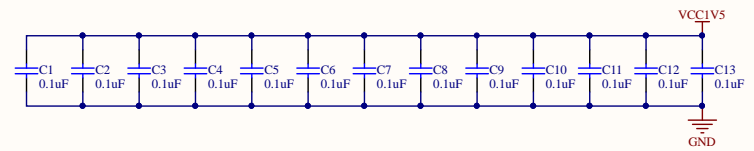
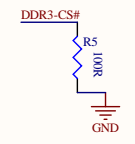
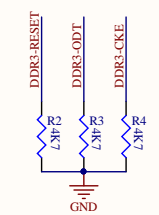
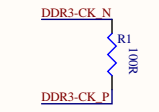
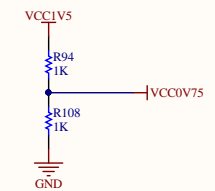
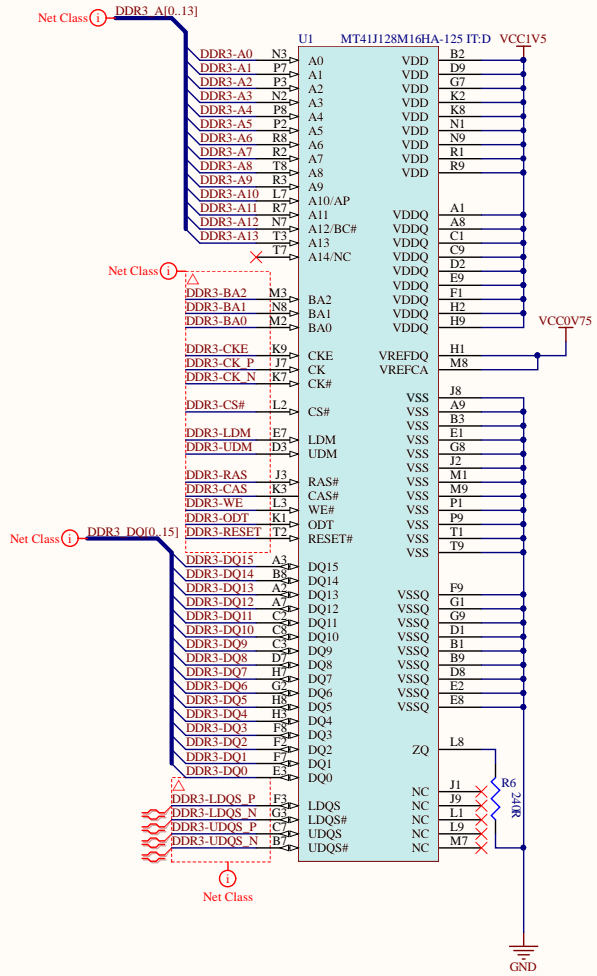

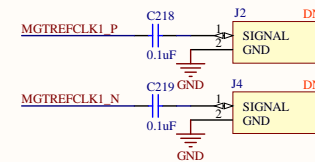
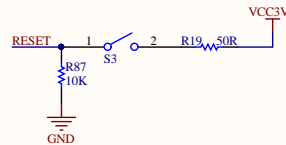
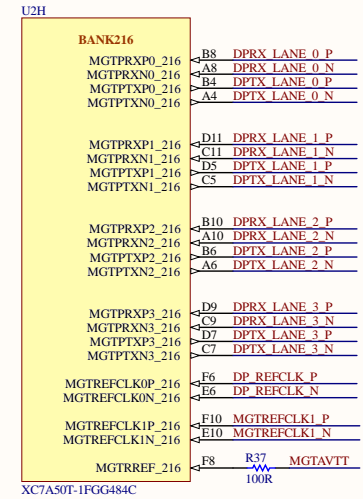
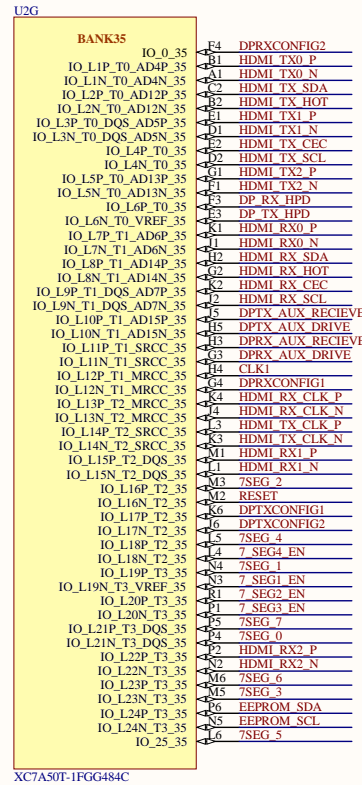
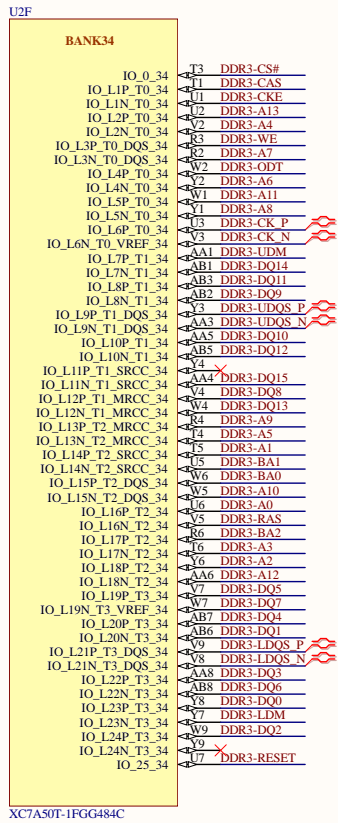


## Block Diagram



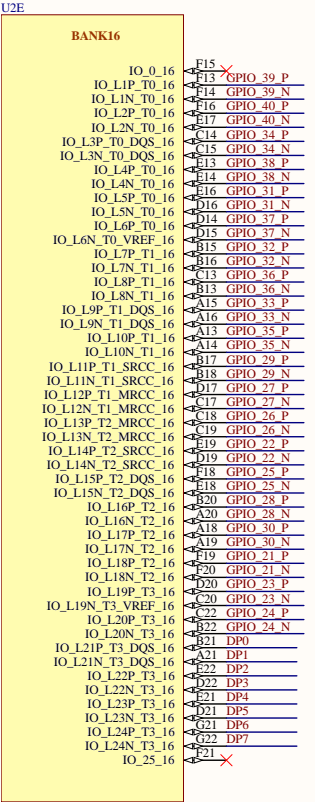
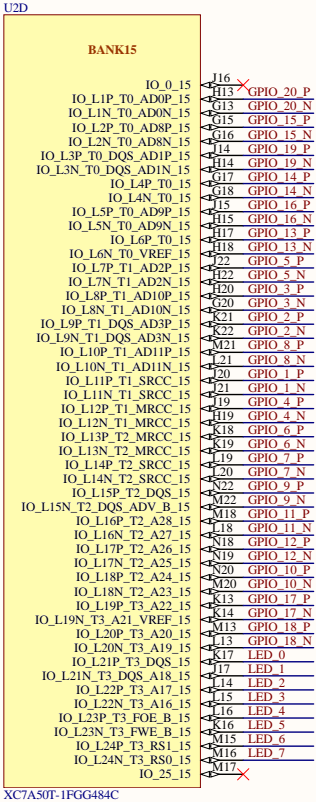
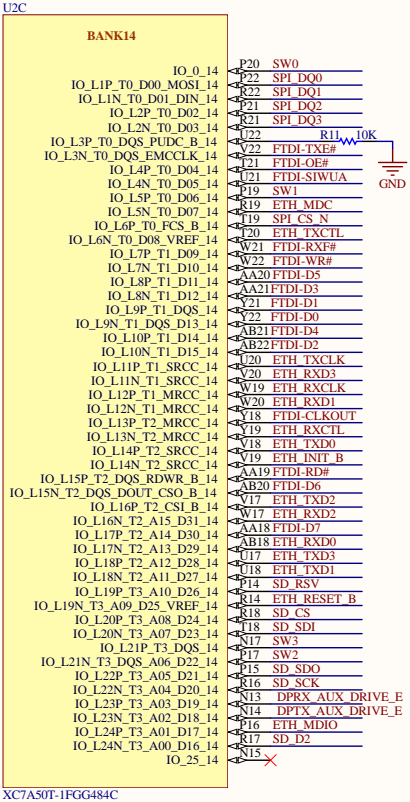
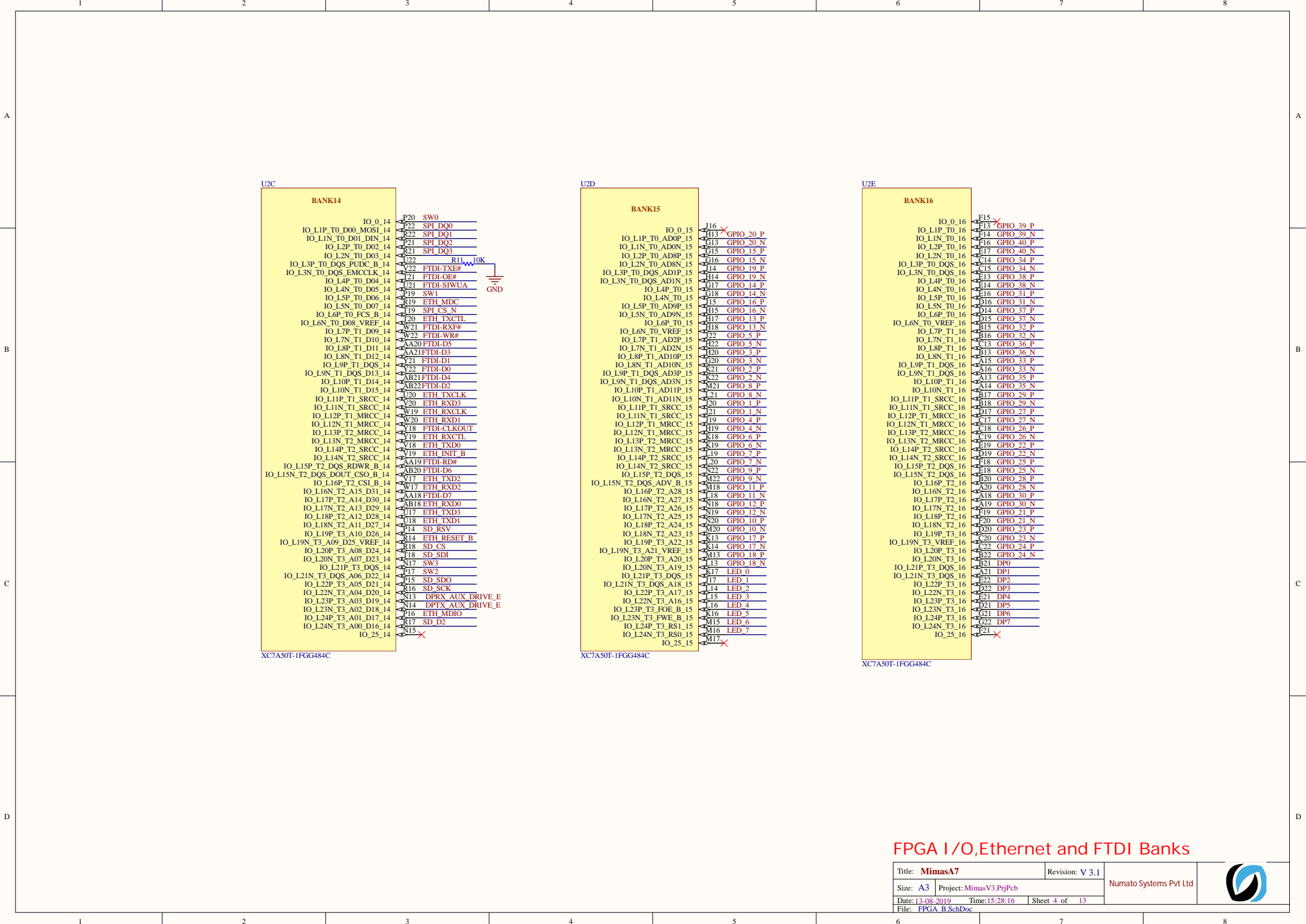
# DDR3

Title: <b>MimasA7</b>	Revision: <b>V 3.1</b>	Numato Systems Pvt Ltd		
Size: <b>A3</b>	Project: <b>MimasV3.PjgPcb</b>			
Date: <b>13.08.2019</b>	Time: <b>15:28:15</b>			Sheet <b>2</b> of <b>13</b>
File: <b>DDR3.SchDoc</b>				

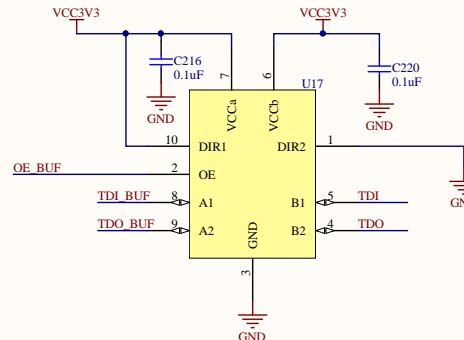
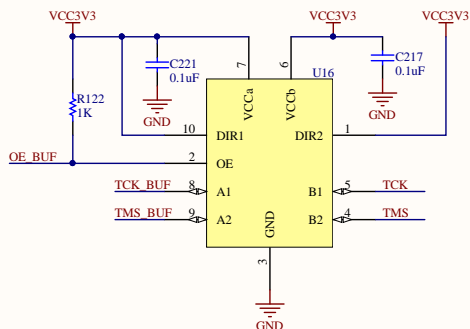
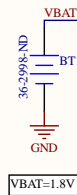
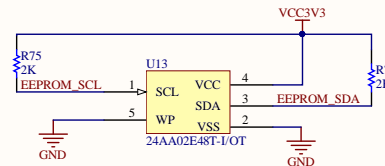
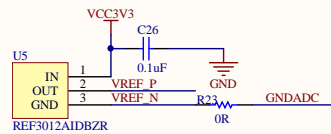
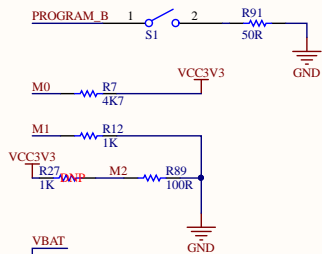
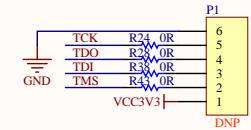
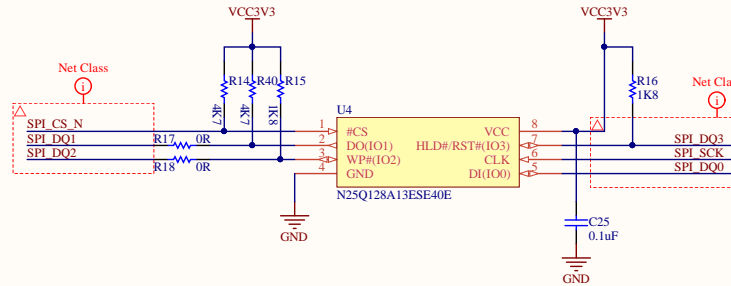
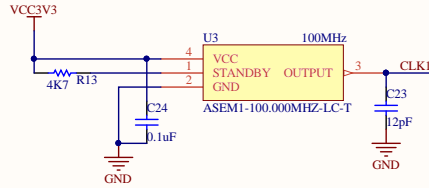
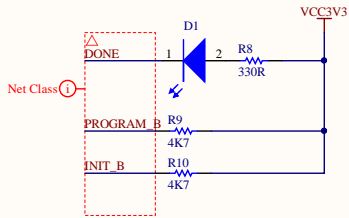
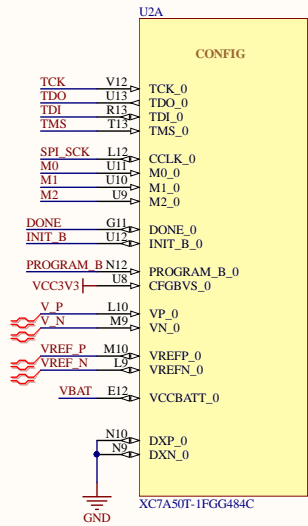


## FPGA DDR3 and Mini Display Port Banks

Title: <b>MimasA7</b>	Revision: <b>V 3.1</b>	Numato Systems Pvt Ltd		
Size: <b>A3</b>	Project: <b>MimasV3.PjgPcb</b>			
Date: <b>13-08-2019</b>	Time: <b>15:28:16</b>			Sheet <b>3 of 13</b>
File: <b>FPGA_A.SchDoc</b>				



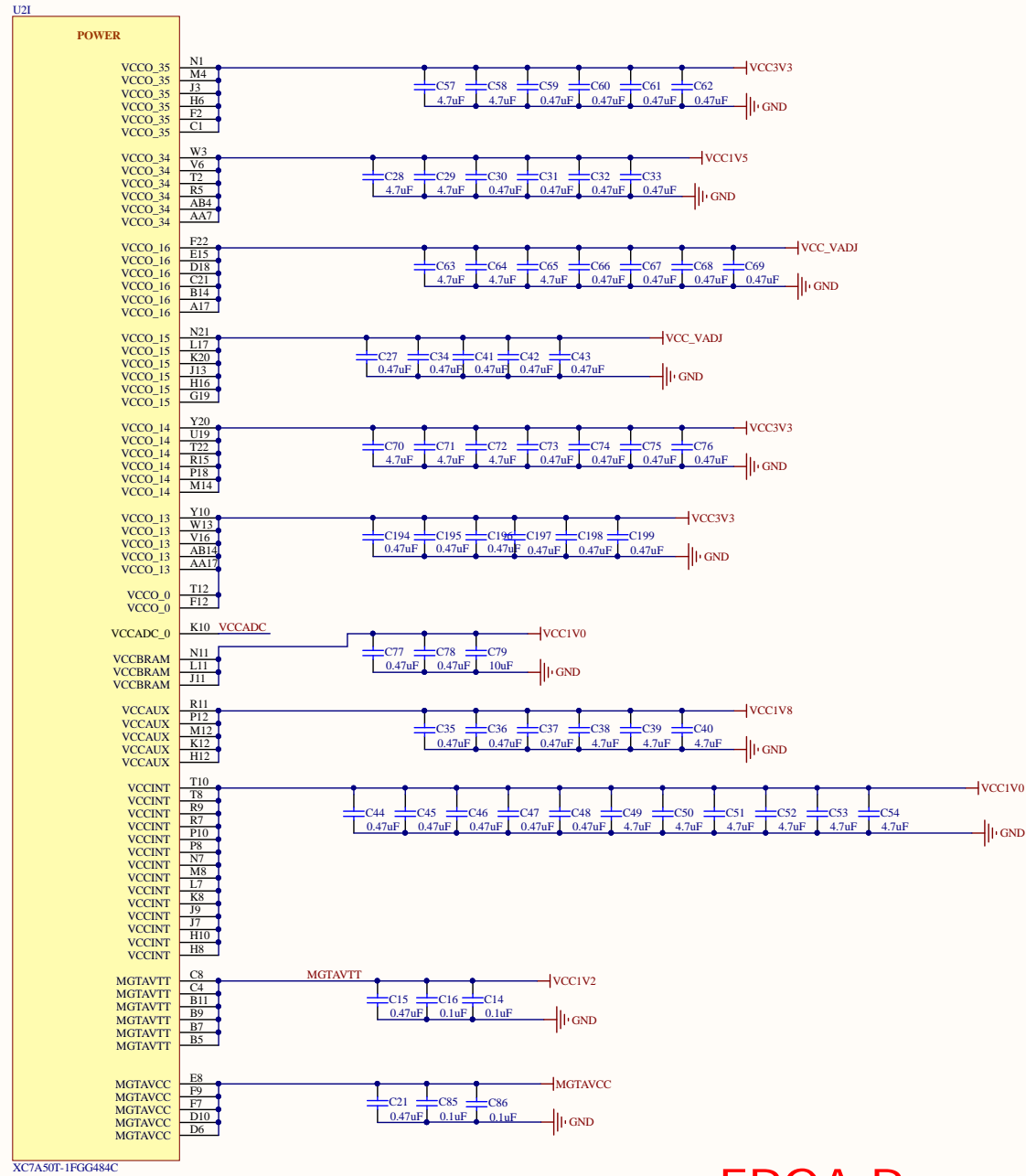
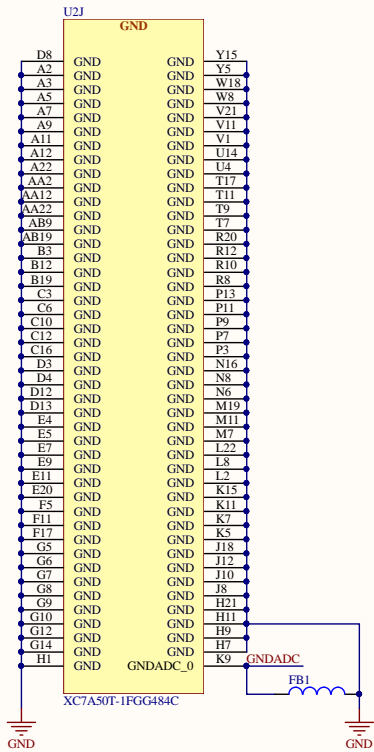
FPGA I/O, Ethernet and FTDI Banks



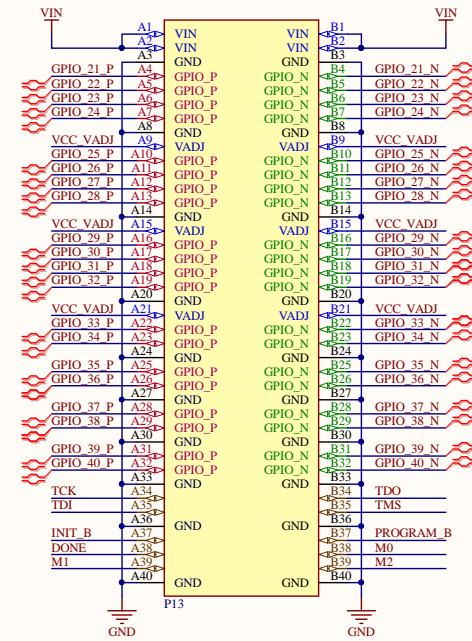
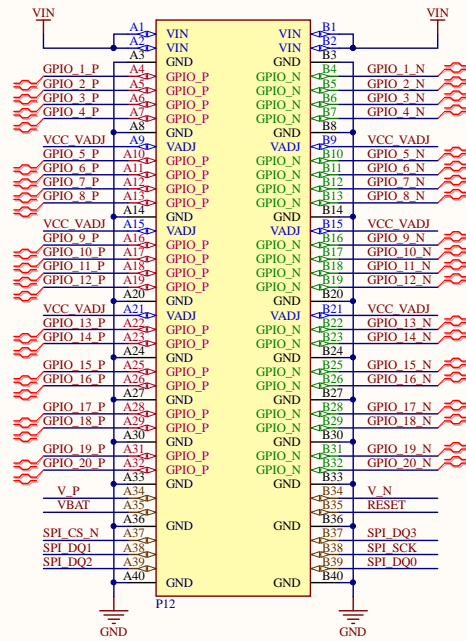
# FPGA Configuration

Title: <b>MimasA7</b>	Revision: <b>V 3.1</b>	Numato Systems Pvt Ltd
Size: <b>A3</b>	Project: <b>MimasV3.PjgPcb</b>	
Date: <b>13-08-2019</b>	Time: <b>15:28:16</b>	
File: <b>FPGA_C.SchDoc</b>	Sheet <b>5</b> of <b>13</b>	




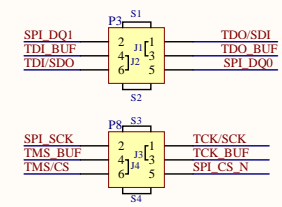
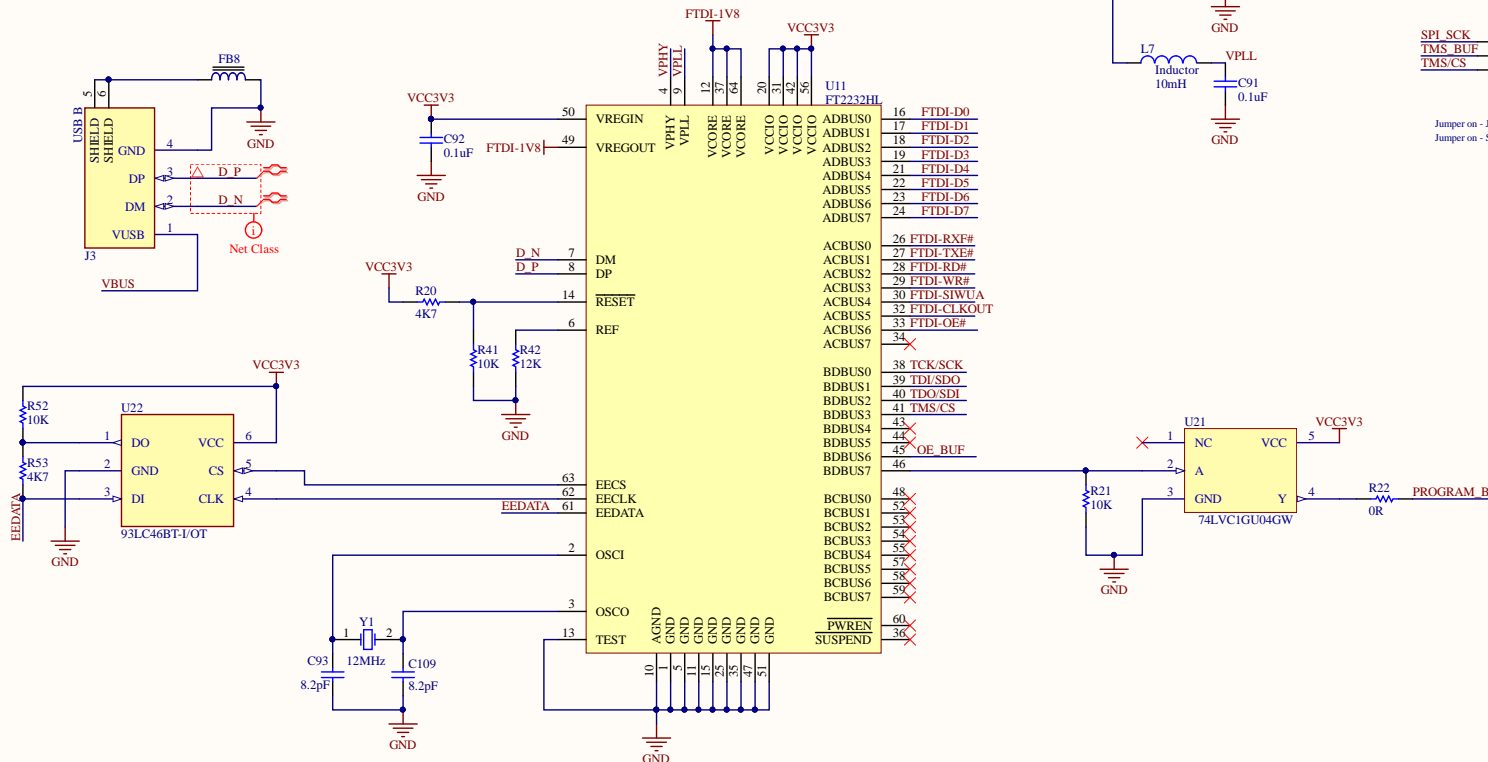


# FPGA Power

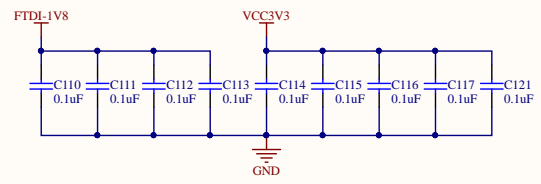


# GPIO Header

Title: <b>MimasA7</b>	Revision: <b>V 3.1</b>	Numato Systems Pvt Ltd	
Size: <b>A3</b>	Project: <b>MimasV3.PjgPcb</b>		
Date: <b>13-08-2019</b>	Time: <b>15:28:16</b>		
File: <b>GPIO.SchDoc</b>			

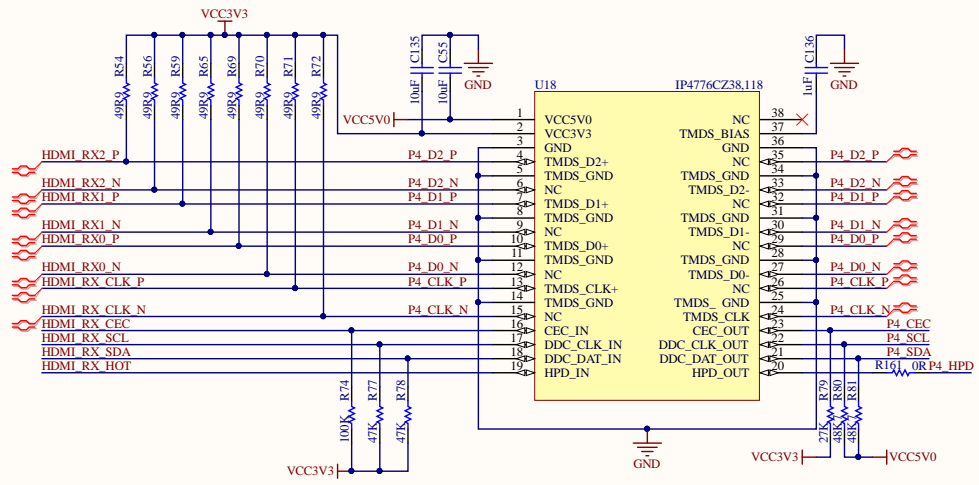


Jumper on - J1,J2,J3,J4 - JTAG COMMUNICATION  
 Jumper on - S1,S2,S3,S4 - SPI COMMUNICATION

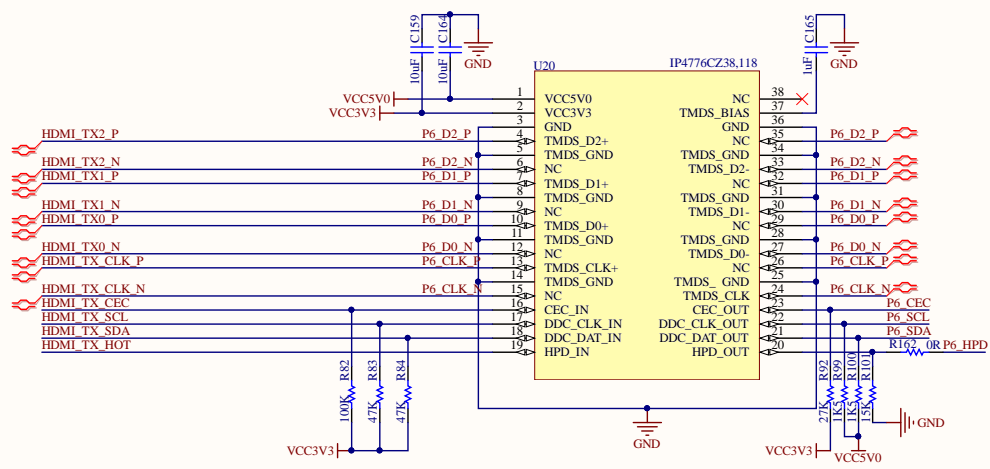
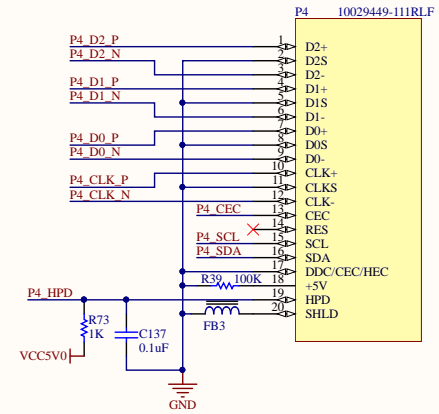


# FT2232H

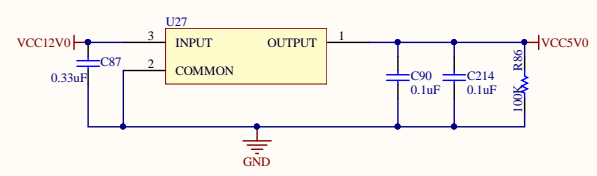
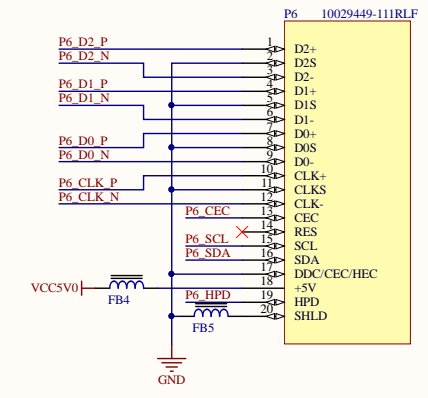





### HDMI In

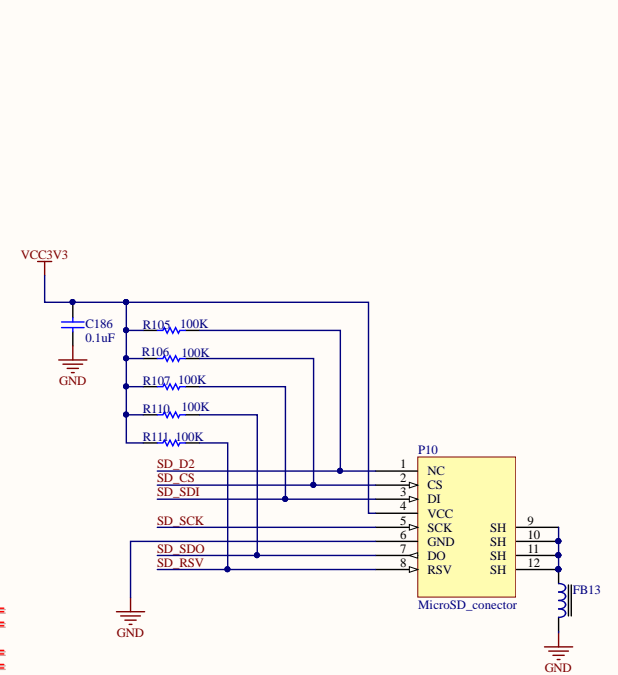
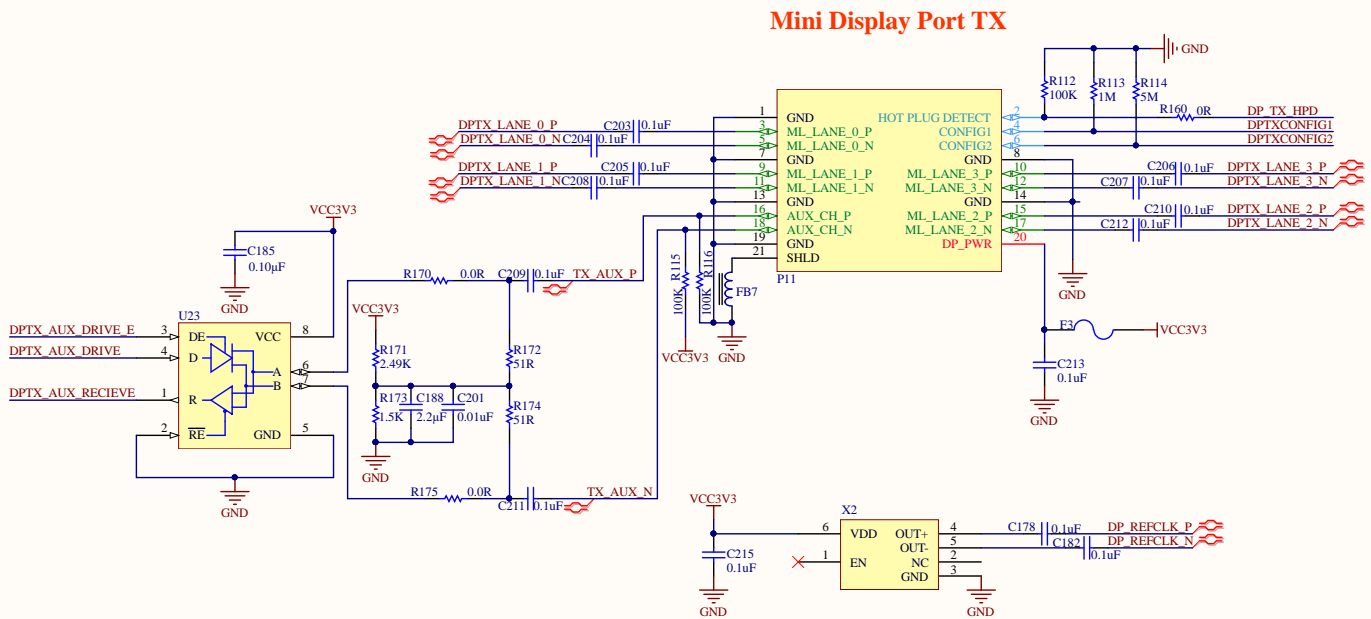
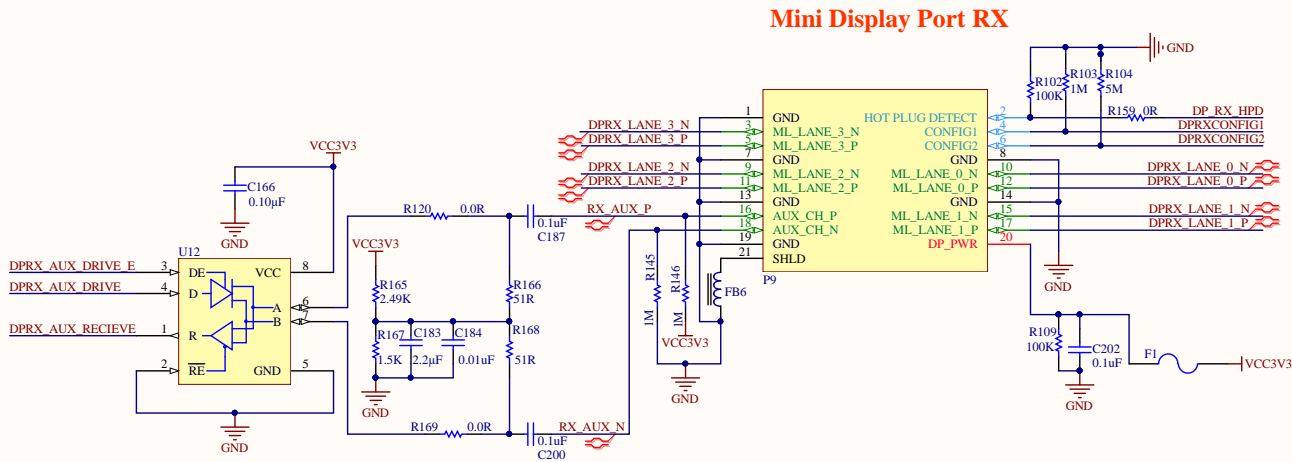


### HDMI Out



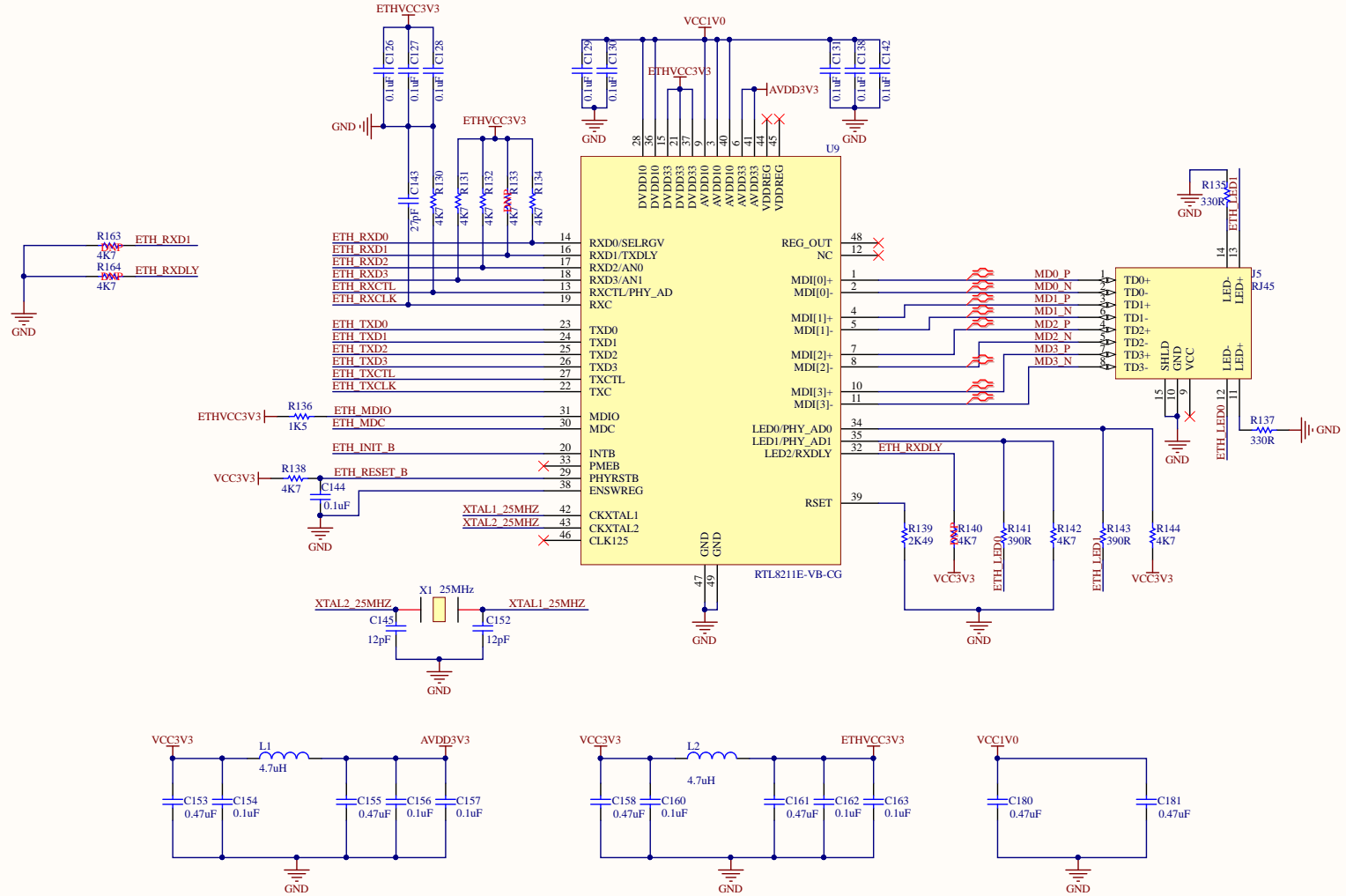
# HDMI

Title: <b>MimasA7</b>	Revision: <b>V 3.1</b>	Numato Systems Pvt Ltd		
Size: <b>A3</b>	Project: <b>MimasV3.PrgPcb</b>			
Date: <b>13.08.2019</b>	Time: <b>15:28:17</b>			Sheet <b>9</b> of <b>13</b>
File: <b>HDMI.SchDoc</b>				




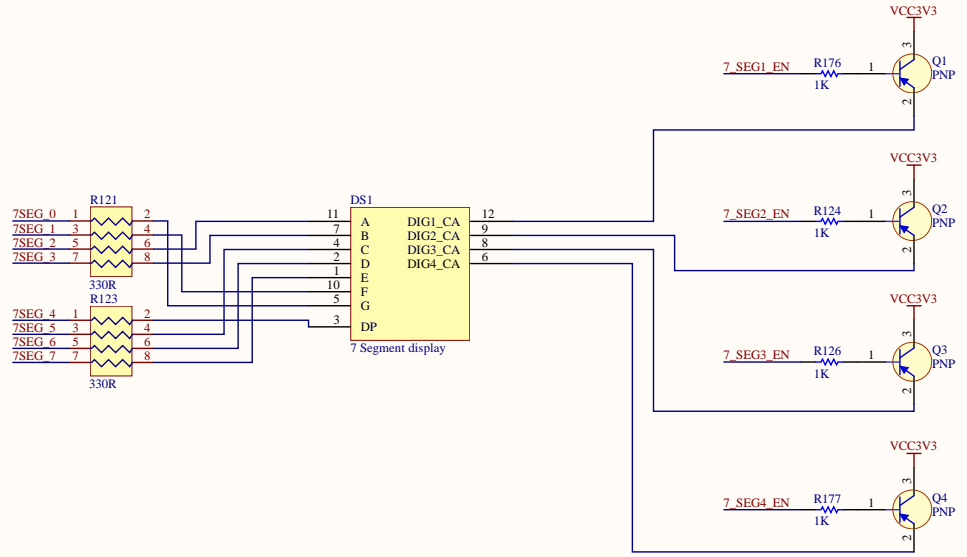
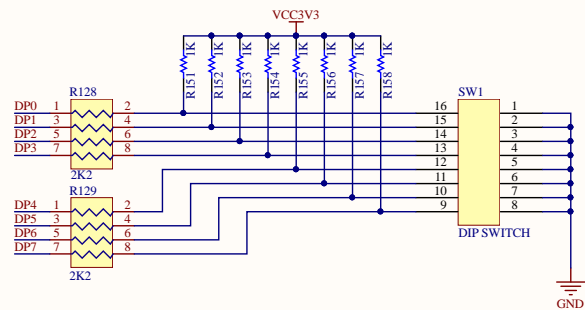
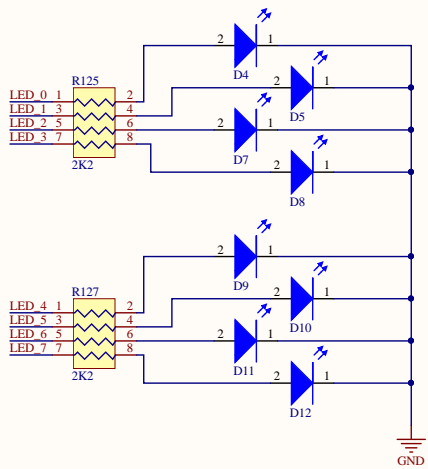
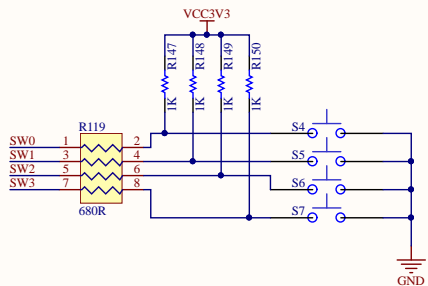
## Mini Display Port & SD Card






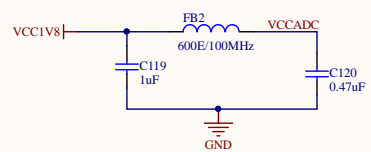
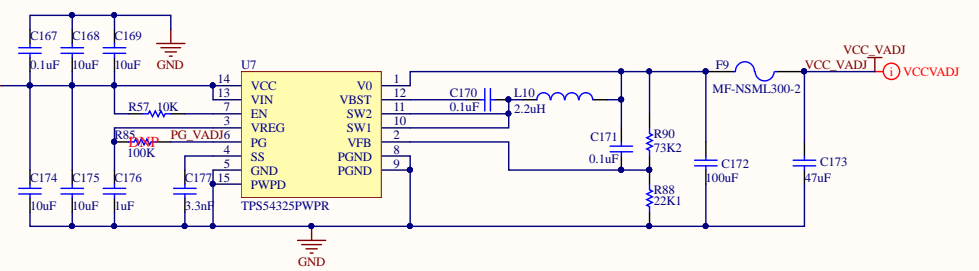
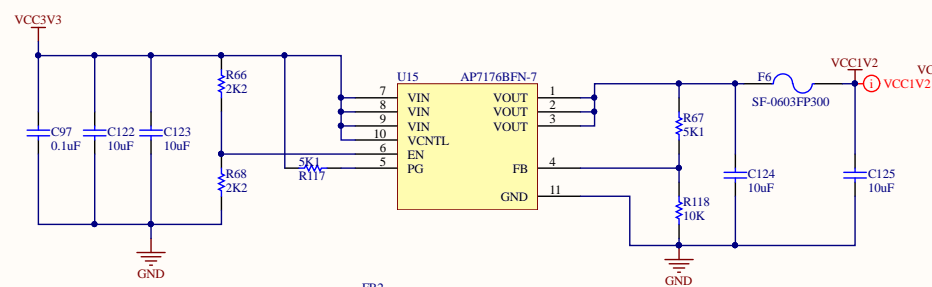
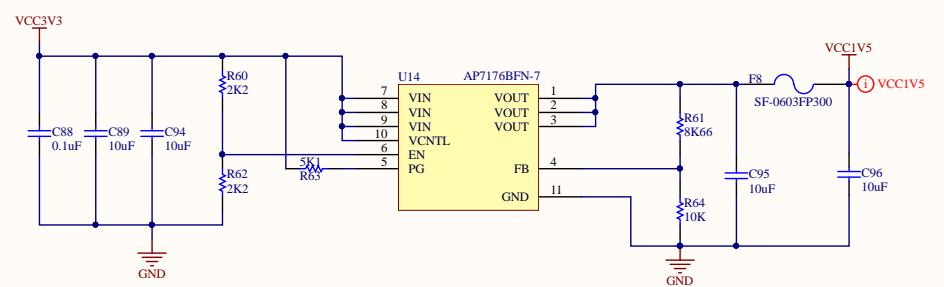
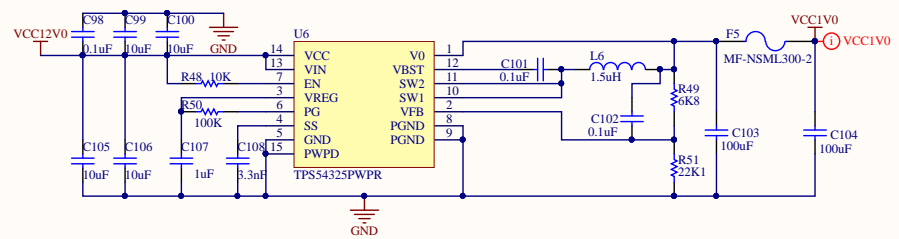
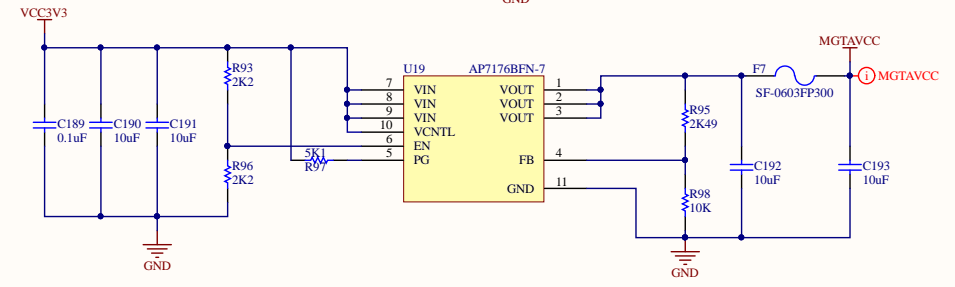
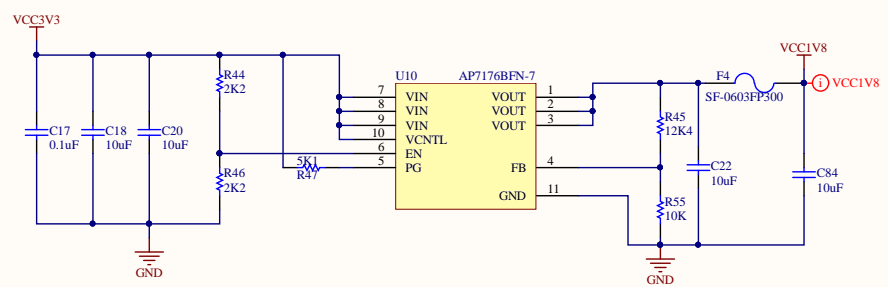
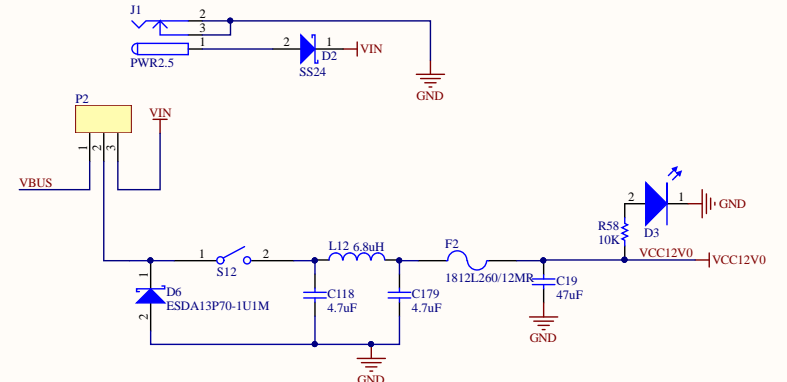
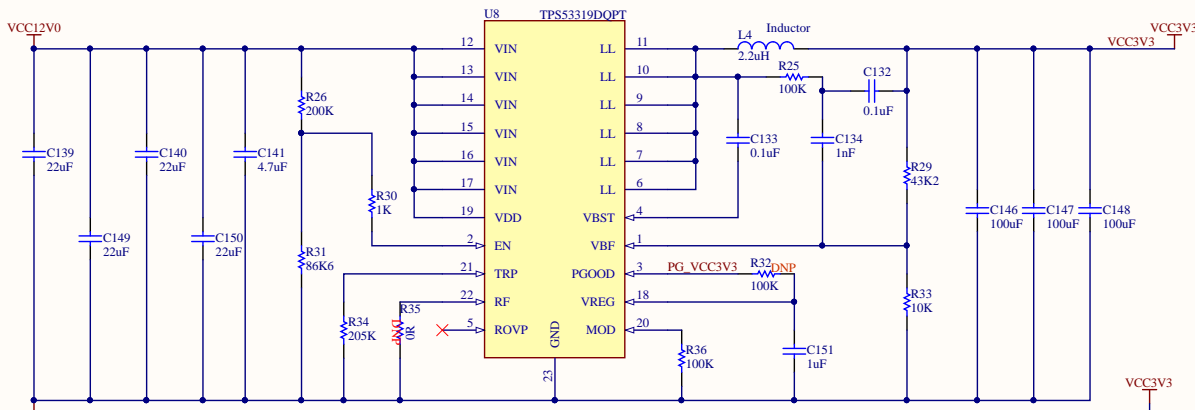
# Ethernet

Title: <b>MimasA7</b>	Revision: <b>V 3.1</b>	 Numato Systems Pvt Ltd
Size: <b>A3</b>	Project: <b>MimasV3.PjgPcb</b>	
Date: <b>13-08-2019</b>	Time: <b>15:28:17</b>	
File: <b>Ethernet.SchDoc</b>	Sheet <b>11</b> of <b>13</b>	



Push Button,DIP Switch,LEDs & 7 Segment Display

Title: <b>MimasA7</b>	Revision: <b>V 3.1</b>	Numato Systems Pvt Ltd		
Size: <b>A3</b>	Project: <b>MimasV3.PjgPcb</b>			
Date: <b>13-08-2019</b>	Time: <b>15:28:17</b>			Sheet <b>12</b> of <b>13</b>
File: <b>7_Segment_Display_SchDoc</b>				



# Power Supply

Title: <b>MimasA7</b>	Revision: <b>V 3.1</b>	Numato Systems Pvt Ltd
Size: <b>A3</b>	Project: <b>MimasV3.PjgPcb</b>	
Date: <b>13-08-2019</b>	Time: <b>15:28:17</b>	
File: <b>PowerSupply.SchDoc</b>	Sheet: <b>13of 13</b>	

